

PDC for Final Test

Terdsak Somboonchan MThai Project Engineering 2 February, 2018





- PDC Overview The 1st Day (3 hours)
- PDC Hand on The 2nd Day (3 hours)
- > PDC Change Notice The 3rd Day (3 hours)



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Final Test Setup Data - 1

- **Baan PDC : Engineering Lot, Test setup data for subcontractor**
 - Special Test Setup Options
 - Special Test Traveler

Baan-TT

Baan-TSO

- MES : In house Production Lot (MTAI, MMT, MPHL)
 - Test Setup Options : FRM-95002-031
- PDF

MES-TSO

POF

MFS-STT

MES-MTAI-STT

- Singulated Test Traveler : FRM-95002-014
- MTAI Strip Test Traveler : FRM-95002-030
- Subcon Strip Test Traveler : FRM-95002-032



MES-I FAR4YM4X

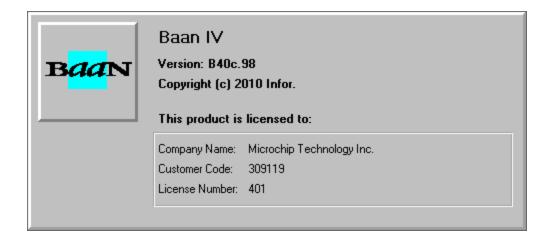
MES-YGAY2TV2X 031



- Baan was a vendor of Enterprise Resource Planning (ERP) software that is now owned by Infor Global Solutions.
- History :

The Baan Corporation was created by Jan Baan in 1978 in Barneveld, Netherlands, to provide financial and administrative consulting services.

The Baan company focused on the creation of ERP software.





Request for Baan PDC User Account

- Go to : <u>http://microchipweb/</u>
- Tools & Resources → Information Services
- Application Request (iTURF) → Create New Request
- Fill in iTURF and have your manager approve it.

[Fill in BAAN/MySo section per below example. Change role only.

If you have already had Baan user account for another function, Please check Account Type as Change.]

BAAN/MySO	
 ✓ Production △ Development (IS Only) Account Type: ● New ○ Change ○ MySO 	*Baan User Copy ID: B00404
*Baan Company: (e.g. 101)	*List all functional roles: (e.g. Marketing, Finance, etc)
101	Test Engineer

- You will be notified via email when the approval is completed and when the Baan changes have been completed.
- IS team will provide you user account and install Baan PDC software on your PC.
- Note : iTURF = Information Technology User Request Form



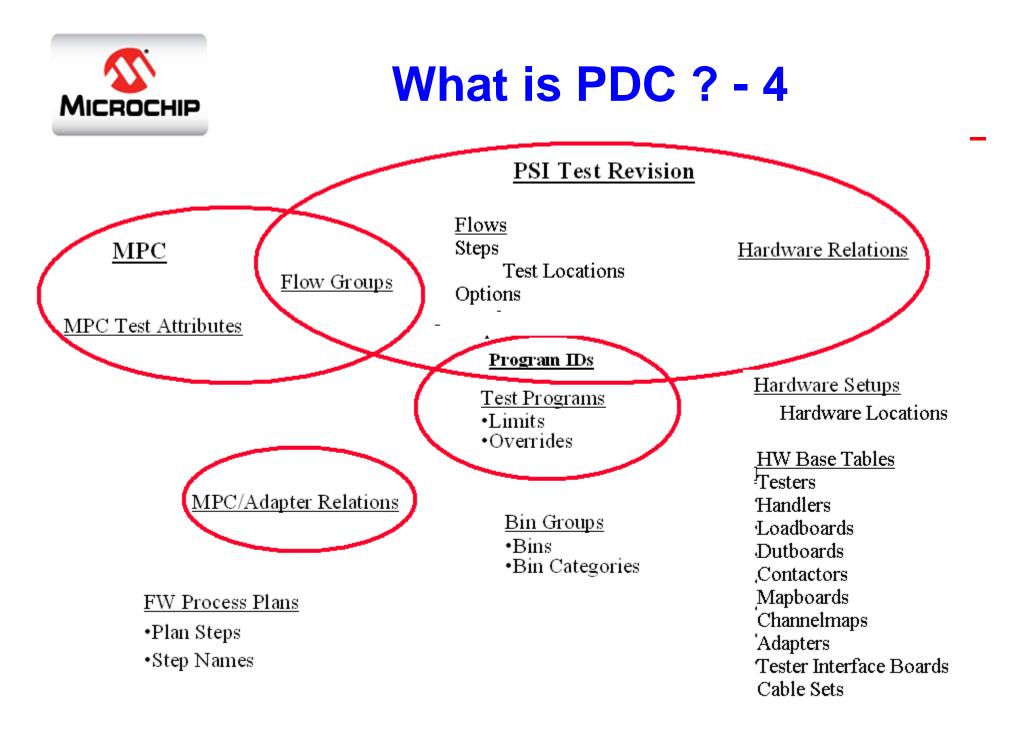
- PDC : Product Data Characteristics
 - Microchip has started using it since year 2003.
 - It contains data used to define how Microchip products are made.
 - The data structure exists in the Baan database as a separate module.
 - It's a control database that requires CN to activate the data.



- Reference Documents :
 - **BPC-00005** : PDC Test Process Guidelines
 - **BPC-00006** : PDC User's Guide Test Hardware
 - <u>PI-70009</u> : PSI Test Revision
 - <u>PI-70010</u> : PDC Test Programs
 - <u>PI-70011</u> : PDC Test Hardware
 - <u>PI-70012</u> : Process Plan Maintenance
- Find the above specification at http://microchipweb/
 - MCHPWEB → Tools & Resources
 - Tools by popular Category → Document Management System (DMS)
 - DMS SharePoint Applications → Controlled Document Libraries
 - Specification Index \rightarrow PI \rightarrow Search



- Content (Focus on the data for Final Test only) :
 - Tester Type
 - Handler Type
 - Test Hardware (Load Board, DUT Board, Contactor, etc.)
 - Test Flow
 - Test Program
 - Handler Bins (Hardware Bins Per Test Program Assignment)
 - MPC Test Attributes
 - [Tester Device (Part Number for Test Program Loading)]

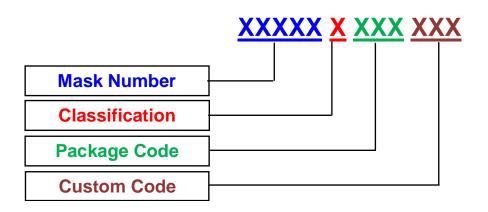




- **MPC : Manufacturing Product Code** Note : The MPC can be linked to only one Flow Group.
- Consists of a 12-digit alphanumeric code
 - 1st 5th Characters : Mask number

 - 6th Character : Classification
 - 7th 9th Characters : Package Code
 - 10th 12th Characters : Custom Code
- See more detail from :
 - <u>SPI-43506</u> : Product Identification System
 - MS-14000-001 : Package Code Tables





Product Class Code	Description	Temperature Level	Temperature Range (degrees)
1	Commercial Temperature	Commercial	0°C to +70°C
4	Extended Temperature (E)	Extended	-40°C to +125°C
7	Industrial Temperature (I)	Industrial	-40°C to +85°C
J	High Temperature Range, Tape & Reel	High	-40°C to >+125°C
S	Commercial Temperature Tape & Reel	Commercial	0°C to +70°C
Т	Industrial Temperature (I) Tape & Reel	Industrial	-40°C to +85°C
Y	Extended Temperature (E) Tape & Reel	Extended	-40°C to +125°C



Package Code	Package Code Description	Package Type (8 Char Max.)	Lead Count	Industry Standard	Package Width Or Size	Pkg Otln Dwg No.	Solder Composition (Terminal Finish)
2EX	Very Thin Quad Flatpack No-Leads	VQFN	028	JEDEC	5x5x0.9mm	C04-239	Matte Tin
2JX	Low-profile Plastic Quad Flat Pack	LQFP	176	JEDEC	20x20x1.4mm	C04-367	Matte Tin
2MX	Very, Very Thin Profile Fine Pitch Ball Grid Array	WFBGA	034	JEDEC	4x6x0.73mm	C04-164	SAC
2NX	Ultra Thin Quad Flatpack No-Leads	UQFN	028	JEDEC	6x6x0.55mm	C04-385	Matte Tin
2TX	Extremely-Thin Profile Fine Pitch Land Grid Array	XFLGA	048	JEDEC	4x6x0.6mm	C04-163	SAC
2WX	Very Thin Fine-Pitch Land Grid Array	VFLGA	068	JEDEC	8x8x1.0mm	C04-373	NiAu
2XX	Thin Profile Square Ball Grid Array	TFBGA	121	JEDEC	10x10x1.20mm	C04-148	SAC
3JX	Low Profile Fine Pitch Ball Grid Array	LFBGA	256	JEDEC	14x14x1.14mm	C04-369	SAC105
3RX	Very Thin Dual Flatpack No-Lead	VDFN	008	JEDEC	4x4x1.0mm	C04-285	NiPdAu
3XX	System In Package	SiP	121	-	8x8x1.08mm	C04-212	SAC
4AX	Super-thin Dual Flatpack No-Lead	X2DFN	006	JEDEC	1.5x1.8x0.33mm	C04-441	Au
4DX	Thin Ball Grid Array	TBGA	024	JEDEC	6x8x1.2mm	C04-199	SAC
4GX	Very Thin Fine Pitch Ball Grid Array	VFBGA	064	JEDEC	7x7x1.0mm	C04-370	SAC125+Ni
4QX	Thin Dual Flatpack No-Lead	TDFN	006	JEDEC	2x2x0.8mm	C04-078	NiPdAu
4XX	PCB Module	MODULE	043	-	32x15x2.5mm	C04-10041	NiAu
5SX	Very, Very Thin Leadless Array Package	WTLA	020	-	3x3x0.7mm	C04-184	NiPdAu
6QX	Ultra Thin Dual Flatpack No-Lead	UDFN	008	JEDEC	2x3x0.5mm	C04-136	NiPdAu
6SX	Very Thin Leadless Array Package	VTLA	044	-	6x6x0.9mm	C04-157	NiPdAu
7BX	Micro Small Outline Package	MSOP	008	JEDEC	3x3mm	C04-111	NiPdAu
8PX	Leadless Land Grid Array	LLGA	026	-	6x6x0.60mm	C04-277	NiPdAu
8XX	Super Thin Dual Flatpack No-Leads	X2SON	008	JEDEC	1.5x1.5x0.40mm	C04-338	Matte Tin
9AX	Dual Flatpack No-Leads Simulator	TDFN-S	008	JEDEC	6x5x0.8mm	C04-169	NiPdAu
9HX	Quarter Size Outline Package	QSOP	044	JEDEC	7.5mm(.300in)	C04-282	Matte Tin
9XX	Super Thin Quad Flatpack No-Leads	X2QFN	010	JEDEC	1.5x1.5x0.40mm	C04-378	Matte Tin
A4X	Thin Shrink Small Outline Package	TSSOP	008	JEDEC	4.4mm	C04-086	Matte Tin
A6X	Dual Flatpack No-lead	DFN-S	008	JEDEC	6x5x0.9mm	C04-122	Matte Tin
A7X	Dual Flatpack No-lead	DFN	008	JEDEC	3x3x0.9mm	C04-062	Matte Tin
ACA	Plastic Ball Grid Array	PBGA	289	JEDEC	19x19x2.36mm	C04-1085	SAC
ALA	Very Thin Land Grid Array	VLGA	006	JEDEC	3.2x2.5x0.9mm	C04-1215	NiAu
ASX	High-Power Dual Flatpack No-Lead	PDFN	008	JEDEC	5x6x0.9mm	C04-188	Matte Tin
B9X	Metric Plastic Quad Flat Pack	MQFP	100	JEDEC	14x20x2.7mm	C04-050	Matte Tin
C2X	Plastic Small Outline Integrated Circuit	SOIC	008	JEDEC	.150ln(3.90mm)	C04-057	Matte Tin
C3X	Plastic Small Outline IC	SOIJ	008	EIAJ	.208in	C04-056	Matte Tin
C4X	Plastic Dual-In-Line Package	PDIP	008	JEDEC	.300in	C04-018	Matte Tin
FDX	Ultra Thin Fine Pitch Ball Grid Array	UFBGA	025	JEDEC	3x3x0.6	C04-412	SAC
G3X	Shrink Small Outline Package	SSOP	020	JEDEC	.209in	C04-072	Matte Tin
L4X	Plastic J-Leaded Chip Carrier	PLCC	028	JEDEC	11.5x11.5x4.3	C04-026	Matte Tin
LFX	Very-Thin Quad Flatpack No-Lead, Routable (HLA™)	VQFN-R	025	JEDEC	6x6x0.9mm	C04-331	Cu
M2X	Quad Flatpack No-Leads	QFN-S	028	JEDEC	6x6x0.9mm	C04-124	Matte Tin
M3X	Skinny Plastic Dual-In-Line Package	SPDIP	028	JEDEC	.300in	C04-070	Matte Tin
M4X	Quad Flatpack No-Leads	QFN	028	JEDEC	6x6x0.9mm	C04-105	Matte Tin
N6X	Thin Small Outline Transistor	TSOT	006	JEDEC	2.9x1.6x1.0mm	C04-141	Matte Tin
NAX	Dual Row Quad Flatpack No-Lead	DQFN	124	JEDEC	10x10x0.85mm	C04-395	Matte Tin
Q3X	Ultra Thin Small Outline No-Leads (SON or DFN)	USON	008	JEDEC	2x3x0.6mm	C04-203	Matte Tin
Q7X	Extremely Thin Dual Flatpack No-Leads	XSON	008	JEDEC	2x2x0.45mm	C04-205	Matte Tin
RWX	Very, Very Thin Dual Flatpack No-Leads	WDFN	008	JEDEC	2x2x0.8mm	C04-261	Matte Tin
T4X	Thin Plastic Quad Flat Pack	TQFP	044	JEDEC	10x10x1mm	C04-076	Matte Tin
THX	Flip Chip Ball Grid Array	FCBGA	169	JEDEC	11x11mm	C04-309	SnPb
XLT	Fine Pitch Ball Grid Array	FPBGA	025	JEDEC	5.7x5mm	C04-310	SnPb
TVX	Plastic Metric Quad Flatpack	PQFP	044	JEDEC	10x10x2mm	C04-119	SnPb
UDX	Very Thin Quad Flatpack No Lead	SQFN	036	JEDEC	6x6x1.0mm	C04-272	Matte Tin
W6X	Thin Small Outline Package	TSOP	032	JEDEC	8x14mm	C04-174	Matte Tin



- There are 4 MPC Stages.
 - NREL : Non-Release / Engineering
 - **REL** : Released / Production Released
 - PHAS : Phase out / Last Time Buy
 - EOL : End of Life



- MPC Lookup
- Go to http://microchipweb/
 - MCHPWEB → Tools & Resources
 - Tools by popular Category → Document Management System (DMS)
 - DMS SharePoint Applications → Lookup Applications
 - MPC Lookup
 - Search by Mask, MPC, Division, CPN, Customer, etc.
 - Click "View Detail"



• **LEAD1TT4X030**

		MPC Viev	ver	Status: A	CTIVE	
MPC LEAD1TT4X030	Revision A T	Rev Date 2/21/2015	CN S0	09491 CN Da	te 2/21/2015	
мрс						
General Attributes						
Mask	LEAD1	Product Type	D63	Product Revision		
Product Class	Т	Restriction Level	CUSTOM	QA/QB Code	QA	-
Package Code	T4X	Acquisition Code		Base Qty Multiple	1200	
Custom Code	030	Sub-Cons BD	No	Emulator MPC	No	
MPC Stage	REL	BD Part No	PIC18F46K20	Device Checksum	0339	
BOM Level Code	FGM	Packing Media	T/R	Blank Checksum	0362	
BAS/DES Where Used	N/A	Pattern Type	QTP	UL Certified	No	
Catalog Part Number	*see below	Pattern Number	030	Firm ware Rev		
Catalog Part Primary	Yes	UV Sensitive	No	Buy/Resale/Foundry	No	
Functional Unit	MCHP	Test Flow Group	*see below	No Build	No	
Config Level	N/A	SDP MPC		Late Marking	No	
Default Warehouse	FB1	Temp Range		Pin Orientation	Quadrant 2	
Catalog Part Number	PIC18F46K20T-I/PT030					ľ
Test Flow Group	D2X-J750 STRIP INDUSTRI	AL QTP				
Marking						
Loc	ation	Line	V	alue	1	
	ONT	1		@	-	
	ONT	2		14827	1	



• **LEAD1TT4X030**

MPC Viewer	×	-					
O (1) mch	npweb-netapps/Dataviewer/N	IPC?MPC=LEAD1TT4X030&revision	=A				२ 🗟 🕁 🛛
	Mask Pattern Attribut	es					
	Pattern	030	SQTP Source	None			
	CheckSum OFF	AA8C	Start Address				
	CheckSum ON CheckSum ROM	0339	Byte Count Promote Metho	0			
	QCode		Start Value	d None			
	Cust Hex ID	FFFFFFF	Increment	0			
	SDP Prod ID		Hex File	-			
	SDP Cust ID		SQTP File				
	Code Source	E:Mail	Serial Hex File				
	Promote Version	PM3V8.91	Status Register				
	MPC Test Attributes						
		Tester Type	Te	st Device			
		J750 LTX_D2X		8F46K20 8F46K20			
	Essential Element Cor	nstraints					
	NSCARs Wafer Map Communic	ration					
	Acquired Part Inform						
	Assembly Instruction						
	Bill Of Material (BOM)						
	Starting Material # 1	L					
	Component Item	BOM level	CPN	Use Priority	Qty	Effective Date	
	LEAD1101XXXX	DIS		10	1	02/21/2015	
	CPN						
	Note:						
		napshot of the CPN information is sh ue to contain references to CPN data		the MPC revision whe	n looking at INAC	TIVE MPC revs.	
	Catalog Part Number	PIC18F46K20T-I/PT030	Release to Sample	No			
	CPN Stage	REL	Release to Buy- Microchip	No			
	Web Page Part #	PIC18F46K20		No			
	End Customer	TRIDONIC GmbH & Co. KG		No			



Process Step Characters

(PI-70012)

Step Character	Description
В	Burn-in
С	Temperature Cycles
E	Endurance Test (Singulated)
F	Final Test (Singulated)
I	100% Integrated Final Test (Strip Final Test + Inline QC)
J	100% Integrated (Singulated Final Test + Inline QC)
K	Bake and 24 Hour Hold Bake at Room Temperature
Q	QC Sampling (Singulated) per LTPD Table
R	Rescreen (Singulated) – 100% of the lot
S	100% Final Test (Strip)
т	QC Sampling (Strip) per LTPD Table
U	Rescreen (Strip)
V	Rescreen all QC samples from previous step (QC(n)) – QC Sample only
Y	No Final Test required step



Process Step Names (PI-70012)

Step Character	Step Name	Test Method	Description
В	Bl(n)	Singulated	Where (n) is the number of Burn - IN Test being performed
E	ET(n)	Singulated	Where (n) is the number of Endurance Test being performed
S	FS(n)	Strip	Where (n) is the number of Final Test being performed
F	FT(n)	Singulated	Where (n) is the number of Final Test being performed
I	IS(n)	Strip	Where (n) is the number of Integrated Final Test being performed
J	IT(n)	Singulated	Where (n) is the number of Integrated Final Test being performed
Q	QC(n)	Singulated	Where (n) is the number of Quality Sampling Test being performed
т	QS(n)	Strip	Where (n) is the number of QC Sampling Test being performed
K	RB(n)	Singulated	Where (n) is the number of Retention Bake being performed
U	RS(n)	Strip	Where (n) is the number of Re-screen (100% Test) being performed
R	RT(n)	Singulated	Where (n) is the number of Re-screen (100% Test) being performed
С	TC(n)	Singulated	Where (n) is the number of the Temperature Cycle being performed
Y	YY1	Singulated	Used for MPCs where no final test steps are necessary. Allows print out SCAN and QC FOI lot traveler.
V	VT(n)	Singulated	Where (n) is the number of Re-screen all QC Sample being performed



Test Program Revision (PI-70010)

📫 pctfl0120m000 : Maintain Tes	t Program Revisions	[101]		
<u>File Edit Group Options</u>	Or <u>d</u> er <u>T</u> ools <u>S</u> pe	cial <u>H</u> elp		
	<u>R M I I I </u>			T \?
Tester Type	J750	Terady	ne J750	(all models)
Program ID	122222	LEAR0_FT. FT-P	RD-CERLER	NA.
Revision	<u>a</u>	New Rev		
Version	0	New Ver	Г	Prog Overrides
			r [Prog HW Limits
Status	ACT -		Г	Model Limit
Test Program	LEAR0_FT_A48e.	XLS		
Job Name	FT-PRD-CERLER			
Checksum	16540E2			
Bin Group	STANDARD	 Bins 2 - 5 		
Correlation Process Code				
Correlation Good Bin	0			
Special Instructions	1			1
J750 OI Ver				
CN Number	1700910			-21
Comments				
RMA Disposition	No Special Rec	quirements		►
OS Version			Job Numbe	er



PSI Test Revision – 1 (PI-70009)

• PSI : Product Specification Index

 The combination of Test Flow, Test Hardware and Test Program defined for use in the production for each product (Mask Number).

• pctfl0110m0 ile <u>E</u> dit <u>G</u> ri	00 : Maintain PSI Test Revisions [101] oup <u>O</u> ptions Order <u>T</u> ools <u>S</u> pecial <u>H</u> elp	
3 8 8		N ?
PSI Mask	LEAD1	
Revision	AD New Rev	
Version	New Ver Test Flows	1
	HW Relations	
Status	ACT 🔄	
CN Number	1701736	
Description	Change hardware id of 44L QFN 8x8	
Job Number	Multi-Step Test Program 🛛 🗖	

- > Note :
- Job Number When a mass load is used to add new PSI Test Revision data to the tables, a job number is assigned by the data loader process.
- Multi-Step Test Program Set to YES if the mask may be using an 'operation number' at each test step when testing products.



PSI Test Revision – 2 (PI-70009)

- Revision is defined with letter designation. [A,B,C,..., AA,AB,AC,...,ZZ] Except I,O
- Version is defined with number designation.
 Version : 0 is the production version.
 Version : 1,2,3,...,99 is engineering version
 (Engineering Sample Test Flow, Correlation Test Flow, Special rescreen flow for quality issue lot, etc.).
- Note : When update production test flow, should start from version 0.
 When create engineering version, should have only the flow you created, delete all other flows.



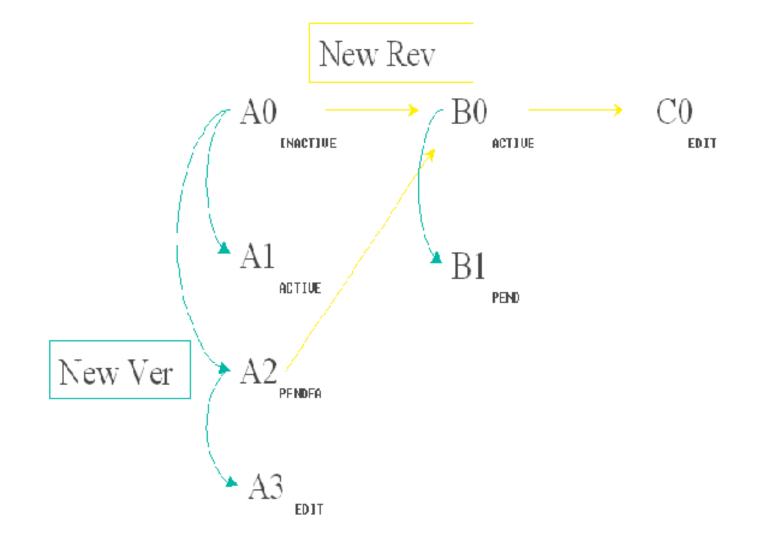
PSI Test Revision – 3 (PI-70009)

• Status

- EDIT : Add, Modify, Delete Data Default state for new revision/version
- PEND : Pending Approval (Will get all approvals before start using it in production)
- **PENDEA : Pending Engineering Approval**
 - (In urgent case, production will proceed before getting all approvals but the lots will be placed on hold in manufacturing before they are moved to Finished Goods inventory.)
- ACT : Active. Data in this status is available for use by manufacturing.
- INACT : Inactive. Data in this status is no longer available for use by manufacturing.



PSI Test Revision – 4 (PI-70009)





PSI Test Revision – 5 (PI-70009)

• Flow Type – STD (Standard – Production Test Flow)

no pctfl01	12s000 : I	Maintain Test	Flows [101]					
<u>File</u> <u>E</u> dit	<u>G</u> roup	<u>O</u> ptions	Or <u>d</u> er <u>T</u> ools	<u>Special</u> <u>H</u> elp				
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411	Form 1	Fo	orm 2	يليني المحما يتميا متمامين السنيا حمايين				
1.4	PSI Mask Now Type	1924	AD1	Revision AD Version 0	Steps	Test Location		
Flow		Config	STRIP		Copy Flow			
Numb	ber Mont	Bank	Flow	Flow Group	Process Plan Name	Description	Test Location	
2603	166 F	Г	2	D2X-J750 STRIP EXTENDED OTP CERLEI	SIQ	D2X STRIP EXTENDED OTP CER	MTAI,	<u> </u>
2603	68 F	- г	2	D2X-J750 STRIP EXTENDED OTP CERLEI	SIQ	J750 STRIP EXTENDED OTP CEI	MTAI,	
2603	74 F	Г	Г	D2X-J750 STRIP EXTENDED OTP CERLEI	FFQQ	EXTENDED OTP CERLER	MTAI,	
2604	152 F	Г	$\overline{\mathbf{v}}$	D2X-J750 STRIP EXTENDED OTP CERLEI	SIT	J750 STRIP EXTENDED OTP CEN	MTAI,	
2458	144 F	Г	Г	D2X-J750 STRIP INDUSTRIAL OTP	FFQQ	SINGULATED INDUSTRIAL OTP	MTAI,	
2458	45 Г	. г	2	D2X-J750 STRIP INDUSTRIAL OTP	SIQ	J750 STRIP INDUSTRIAL OTP :	MTAI,	
2458	46 F	Г	1	D2X-J750 STRIP INDUSTRIAL OTP	SIT	J750 STRIP INDUSTRIAL OTP :	MTAI,	
2458	47 F	Г	2	D2X-J750 STRIP INDUSTRIAL OTP	SIQ	D2X STRIP INDUSTRIAL OTP X	MTAI,	
2458	148 F	Г	Г	D2X-J750 STRIP INDUSTRIAL QTP	FFQQFR	SINGULATED INDUSTRIAL QTP	MTAI,	
2458	49 F	Г	2	D2X-J750 STRIP INDUSTRIAL QTP	SIQFR	J750 STRIP INDUSTRIAL QTP :	MTAI,	
2458	150 F	Г	1	D2X-J750 STRIP INDUSTRIAL QTP	SITFR	J750 STRIP INDUSTRIAL QTP :	MTAI,	
2458	51 F	Г	1	D2X-J750 STRIP INDUSTRIAL QTP	SIQFR	D2X STRIP INDUSTRIAL QTP X:	MTAI,	Ŧ
•				× .		- R-		+



PSI Test Revision – 6

(PI-70009)

• Flow Type – REL (Reliability Test Flow)

🚥 pctfl0112s000 : Mainta	in Test Flows [101]				
<u>File Edit G</u> roup Opti	ions Or <u>d</u> er <u>T</u> ools	<u>Special</u> <u>H</u> elp			
E 🖬 🕾 🗠 品	🖻 品 🖊 🖊		8		
+ + Form 1	Form 2				
PSI Mask	LEAD1	Revision AD Version 0	Steps	Test Location	
Flow Type	REL -				
	Config STRIP		Copy Flow		
	ank Flow	Flow Group	Process Plan Name	Description	Test Location
179187	ГГ	RELIABILITY	the second s	PRE DLT	MTAI,
179207	ГГ	RELIABILITY	BFFFFFBFFF	DLT MONITOR	mtai, —
255713	ГГ	RELIABILITY	FFFFFBRRRBRR	PRE+DLT MONITOR	MTAI,
Г	ГГ				
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Г	г г				-
Г	г г		· · · · · · · · · · · · · · · · · · ·		-
Г	ГГ				-
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PSI Test Revision – 7

(PI-70009)

• Flow Type – RSN (Rescreen - Test Flow for QC Rejected Lot)

pctfl0112s	000 : Ma	aintain Test	Flows [101]				
<u>File Edit (</u>	Group	Options (Dr <u>d</u> er <u>T</u> ools	<u>Special</u> <u>H</u> elp			
E B 8	5		R 🙀 🚺		?		
	orm 1	1	m 2				
PSIN			AD1	Revision AD Version 0	Steps	Test Location	
1.650635	Туре	1000	N +				
Flow		Config	STRIP		Copy Flow	1	
Number	Mont	Bank	Flow	Flow Group	Process Plan Name	Description	Test Location
260377	Г	Г		D2X-J750 STRIP EXTENDED OTP CERLEI	a 🕨	D2X STRIP EXTENDED OTP CER	MTAI,
260378	Г	Г		D2X-J750 STRIP EXTENDED OTP CERLEI	a 🕨	J750 STRIP EXTENDED OTF CE	MTAI, —
260379	Г	Г	Г	D2X-J750 STRIP EXTENDED OTP CERLEI	FQ	EXTENDED OTP CERLER [QC@12:	MTAI,
260380	F	Г	Г	D2X-J750 STRIP EXTENDED OTP CERLEI	RQ	EXTENDED OTP CERLER [QC@12:	MTAI,
260381	F	Г	Г	D2X-J750 STRIP EXTENDED OTP CERLEI	FQ	EXTENDED OTP CERLER [QC0-4	MTAI,
260382	Г	Г	Г	D2X-J750 STRIP EXTENDED OTP CERLEI	RQ	EXTENDED OTP CERLER [QC@-4]	MTAI,
260453	Г	Г	$\overline{\mathbf{v}}$	D2X-J750 STRIP EXTENDED OTP CERLEI	n •	J750 STRIP EXTENDED OTF CEI	MTAI,
245852	Г	Г	$\overline{\mathbf{v}}$	D2X-J750 STRIP INDUSTRIAL OTP	n •	J750 STRIP INDUSTRIAL OTP :	MTAI,
245853	Г	Г	Г	D2X-J750 STRIP INDUSTRIAL OTP	FQ	SINGULATED INDUSTRIAL OTP	MTAI,
245854	Г	Г	Г	D2X-J750 STRIP INDUSTRIAL OTP	RQ	SINGULATED INDUSTRIAL OTP	MTAI,
245855	Г	Г	Г	D2X-J750 STRIP INDUSTRIAL OTP	FQ	SINGULATED INDUSTRIAL OTP	MTAI,
245856	Г	Г	Г	D2X-J750 STRIP INDUSTRIAL OTP	RQ	SINGULATED INDUSTRIAL OTP	MTAI,
•							+



PSI Test Revision – 8 (PI-70009)

• HW Relations - Relations

pctfl011	1s000 : Maintain HW Se	tup Relati	ons [101]					
<u>File</u> dit	<u>Group</u> Options Or	der <u>T</u> oo	ls <u>S</u> pecial <u>H</u> elp	2				
		<u> </u>	())					
<u>+</u> + 1	Relations Contact/C	hannel	Locations					
PSI Ma	sk LEAD1 Revision	AD	Version 0			Copy Relat	ions	
Lead		Setup						
Count	Configuration	ID	Tester	Handler	Loadboard	Dutboard	Mapboard	
40	600DIP	74	J750	DAY	14-A2991			<u> </u>
40	600DIP	76	J750	DAY	14-A2991			
40	UQFN_SXS	2	J750	RA2	14-A4384			
44	44TQFP_10	44	J750	CAS	14-A3100			
44	44TQFP_10	45	J750	CAS	14-A3100			
44	44TQFP_10	63	J750	SCH	14-A4254			
44	44TQFP_10	122	LTX_D2X	SCH	14-A6940			
44	44TQFP_10	125	LTX_D2X	SCH	14-A6940			
44	QFN_2X8	105	J750	SCH	14-A4838			
44	QFN_8X8	183	J750	MUT	14-A3112			
44	QFN_8X8	184	J750	MUT	14-A3112			
48	UQFN_6X6	1	J750	RAS	14-A4110			<u>•</u>
•								•



PSI Test Revision – 9 (PI-70009)

• HW Relations – Contact/Channelmap

pctfl011	1s000 : Maintain HW Se	tup Relat	ions [101]								
<u>File</u> <u>E</u> dit	<u>Eile Edit G</u> roup <u>O</u> ptions Or <u>d</u> er <u>T</u> ools <u>Special H</u> elp										
<u>+</u> +	Relations Contact/Channel Locations										
PSI Ma	isk LEAD1 Revision	AD	Version 0								
Lead		Setup						1			
Count	Configuration	ID	Contactor	Channelmap		Tester Interface	Cable Set				
40	600DIP	74	14-A1062	x3day40dip				_			
40	600DIP	76	14-A1062	x4day40dip_256p							
40	UQFN_SXS	2	14-A4385	x4ras40uqfn_256p							
44	44TQFP_10	44	14-A1961	x3dlc44tqfp_128p							
44	44TQFP_10	45	14-A1961	x4dlc44tqfp_256p							
44	44TQFP_10	63	14-A2834	x20mct44tqfp							
44	44TQFP_10	122	14-A6962	x56mct44tqfp							
44	44TQFP_10	125	14-A7951	x56mct44tqfp							
44	QFN_8X8	105	14-A4091	x25mct44qfn							
44	QFN_8X8	183	14-A2683	x8mult441qfn_512							
44	QFN_8X8	184	14-A2683	x6mult441qfn_256							
48	UQEN_6X6	1	14-A1062	2ras48uqfn_128p				-			
•											
						1	1				



PSI Test Revision – 10 (PI-70009)

• HW Relations – Locations

pctfl011	1s000 : Maintain HW Set	tup Relat	tions [101]							
<u>F</u> ile <u>E</u> dit	Eile <u>E</u> dit <u>G</u> roup <u>O</u> ptions Or <u>d</u> er <u>T</u> ools <u>S</u> pecial <u>H</u> elp									
- + F	Relations Contact/Cl	hannel	Locations							
PSI Ma	sk LEAD1 Revision	AD	Version 0							
Lead		Setup		1						
Count	Configuration	ID	Test Plant(s)							
40	600DIP	74	MTAI,							
40	600DIP	76	MTAI,							
40	UQFN_SX5	2	MTAI,							
44	44TQFP_10	44	MTAI,							
44	44TQFP_10	45	MTAI,							
44	44TQFP_10	63	MTAI,							
44	44TQFP_10	122	MTAI,							
44	44TQFP_10	125	MTAI,							
44	QFN_8X8	105	MTAI,							
44	QFN_8X8	183	MTAI,							
44	QFN_8X8	184	MTAI,							
48	UQFN_6X6	1	MTAI,	<u> </u>						
•			,							
1										
<u> </u>										



- Tester Type : 20-Character Maximum
 - 141 Tester Types as of Dec 7, 2017
- Handler Type : 20-Character Maximum

96 Handler Types as of Dec 7, 2017





🚥 pcthw0501s000 : Disp	lay Tester Types [101]		<u>×</u>
<u>File Edit Group Op</u>	tions Or <u>d</u> er <u>T</u> ools <u>S</u> pecial <u>H</u> elp		
Tester Type	Description	<u>ок</u>	
A360	TERADYNE A360	Lancel	
A567	TERADYNE A567		
A580	TERADYNE A580		
ADVANTEST	Advantest		
AMIDA	Amida		
ANRITSU	Anritsu		
ASL3K	ASL3000		
ASL3K_RF	ASL3000 RF		
BENCH_BOX	Bench Box		
CATALYST	Teradyne Catalyst		



 pcthw0502s000 : Display Ha 		
	Order Tools Special Help	
		T N?
Handler		L
Туре	Description	ОК
		Cancel
50S	AETRIUM SINGLE TEMP	- -
50T	AETRIUM TRI-TEMP	
ADV_M3741A	Advantest M3741A	
ADV_M6741A	Advantest M6741A	
ADV_M6751AD	Advantest M6751AD	
ADV_M6841A	Advantest M6841A	
AET_55V	Aetrium 55V8/16	
AS1	ASECO S-130	
AS4	ASECO S-450	
ASE	ASECO	
ASM	ASM FT2018	
CAS	CASTLE LOGIC	
CASCD	CASTLE WITH CDA KIT	
CHIPRIGHT	CHIPRIGHT	
CMT6350	CMT6350	<u> </u>



- Add New Tester / Handler Type
- Discuss with Soravorn Pochpring B00022 (MThai IE) on the Tester/Handler name then submit a request.
- Go to : <u>http://mth-sv-qaapp/ApproveCenter/equipname</u>
- Login by using your PC login User name and Password.
- Select "Equipment Name".
- Choose "New Tester" or "New Handler".
- Input the purpose.
- Select "Add Tester" or "Add Handler", Fill in the data then click "Submit".
- Click "Create Approval Loop" and leave all default approvers.
- Click "Submit to Approval".
- You will get a notification email once your request got all approvals or rejected.



Load Board / Contactor Data - 1

- Load Board Design files on DMS.
- Go to <u>http://microchipweb/</u>
 - MCHPWEB → Tools & Resources
 - Tools by popular Category → Document Management System (DMS)
 - DMS SharePoint Applications → Controlled Document Libraries
 - Specification Index → 14-A Hardware Schematics
 - Search by Spec Num
 - Click "View Detail" to get .zip file.

Drag a colum	Drag a column header and drop it here to group by that column										
Detail Type Status Spec Num Revision Title Originator Owner Part Number Ind								Incorporated CN			
	•	•	14-A6940								
View Detail	14	Current	14-A6940	A	ST CREDENCE DIAMOND 2X PIC18F46K20 x56 44L TQFP 10X10 MCT-TAPESTRY	Ponthanathorn Udomrattanasiriporn - B09836	Valen Burd - C10844		CN_14001845		



• Load Board Data in Baan PDC.

	<u>Options</u> Or <u>d</u> er <u>T</u> ools <u>Special</u> <u>H</u> elp	
1 🖬 🚳 🗠		
Loadboard Type	14-A6940 Status ACTIVE Tester Types	
Loadboard Name	ST D2X PIC18F46K20 X56 44L TQFP 10X10 MCT-TAPESTRY	
Site Count	56	
LIC D. L.L.		
Mfg Part No.		

Eile Edit Group Options Order Tools Special Help \blacksquare
Loadboard Type 14-A6940 ST D2X PIC18F46K20 X56 44L TQFP 10X10 MCT-TAPESTRY
Tester Type Description
LIX_D2X Dual-chassis Diamond-X



Load Board / Contactor Data - 3

- Contactor Design files on DMS.
- Go to http://microchipweb/
 - MCHPWEB → Tools & Resources
 - Tools by popular Category → Document Management System (DMS)
 - DMS SharePoint Applications → Controlled Document Libraries
 - Specification Index → 14-A Hardware Schematics
 - Search by Spec Num
 - Click "View Detail" to get .zip file.

Drag a colun	Drag a column header and drop it here to group by that column								
Detail	Туре	Status	Spec Num	Revision	Title	Originator	Owner	Part Number	Incorporated CN
			14-A6747						
View Detail	14	Current	14-A6747	A	CT x32 6L SOT23 MCT-TAPESTRY	Noppakun Permkhuer - B02110	Valen Burd - C10844	SPC0158A-T01	B1401A



• Contactor Data in Baan PDC.

<u>E</u> dit <u>G</u> roup <u>(</u>	<u>Options</u> Or <u>d</u> er <u>T</u> ools <u>Special</u> <u>H</u> elp		
		4 PP PP T N	?
Contactor Type	Contactor Name	Status	
14-A6618	CT STE X1 16L/24L QSOP 150 Delta717	ACTIVE .	_
14-A6620	CT X2 132L DQFN 11X11 NS-8080	ACTIVE	
14-A6636	CT STE X1 128L MQFP 14X20X2.7 CASTL	ACTIVE	
14-A6637	CT STE X1 100L QFP 14X20X2.7 CASTLE	ACTIVE	
14-A6638	CT STE X1 100L TQFP 14X14X1.4 CASTL	ACTIVE -	
14-A6640	CT X1 196L CABGA 12X12X1.7 CASTLE	ACTIVE -	-
14-A6647	CT X1 100L TQFP 14X14X1 DELTA CASTL	ACTIVE -	- <u></u> 1
14-A6648	CT X1 128L QFP 14X20X2.7 CASTLE ECT	ACTIVE -	
14-A6649	CT X1 128L QFP 14X20X2.7 CASTLE	ACTIVE -	
14-A6747	CT x32 6L SOT23 MCT-TAPESTRY	ACTIVE -	
14-A6767	CT X1 40L QFN 6X6X0.9 CASTLE	ACTIVE -	
14-A6768	CT X1 208L QFP 28X28X3.4 CASTLE	ACTIVE -	
14-A6769	CT X1 40L VFBGA 4X4X0.8 DELTA EDGE	ACTIVE -	•



Hardware Setup - 1

• HW Setup is consist of Tester, Handler, Loadboard, Dutboard, Mapboard, Contactor, Channelmap, Tester Interface, Cable Set,

pcthw0109m000 : Ma	intain Hardware Setups [101]			
<u>File Edit Group Op</u>	tions Or <u>d</u> er <u>T</u> ools <u>S</u> pecial	<u>H</u> elp		
			N ?	
Lead Count	44			
Configuration	44TQFP_10 44 1	d TQFP 10x10		
Setup ID	44 Status	ACTIVE -	I Test Location	
Tester	J750	Teradyne J750 (all models)		
Handler	CAS	CASTLE LOGIC		
Loadboard Dutboard Mapboard Contactor Channelmap Tester Interface		4DC-PIC18F4XK20-X4 9 10X10X1 DELTA CASTLE - L		
Cable Set				
1				



Hardware Setup - 2

• And Test Location.

•••• pcthw0114s000 :	Maintain Hardware Setup Locations [101]		
<u>File Edit G</u> roup				
E 🖬 🖶 🗠		H4 44 14 14	<u>T</u> № ?	
Lead Count	44 Configuration	44TQFP_10	Setup ID	44
Test Plant	Description	Status		
MTAI	Microchip Thailand	ACTIVE +		•
	morounip mariana			
<u> </u>		1		
				-
1				



Final Test Data Collection for PDC Creation

- Data for PDC Creation :
 - Mask / MPC
 - Package / Package Code
 - Tester / Handler
 - Test Hardware (Loadboard, Contactor,..) Spec no.(14-Axxxx)
 - Test Flow & Test Temperature
 - Test Program Name and Test Program Option for each Test Step
 - Test Program Channel Map Name (if any) (Mapping DUT pins with Tester Pin Electronics)
 - Handler Binning (Good/Reject, Define in the test program)
 - Operating System (OS) if require special version
 - Special Instruction (if any)



Login to Baan PDC

- Login by using PC login User name and Password.
- Then press "Enter" button or click "Connect" button.

	Menu browser [User: b00404]	
	<u>File</u> Find Options <u>H</u> elp	
Baan PDC	Hardware Engineer	
Login for baanprod		
Hostname: baanprod Connect		
Username: b00404 Cancel		
Password: <u>C</u> onfigure		
	mtgxxxenr02001 Company:101 WW Ops Co -MCHP- Co	or Date:11-24-2016



Exit From Baan PDC - 1

Caution: Do not exit by clicking "X" box at the upper right hand corner of the screen.

Exit all screens via File menu or Save and Exit button [] (the button right under File menu with the blue arrow pointing the left inside a black box) to

prevent license issue.	Don't				Don't
Do					v
		120m000 : Maintain Test	t Program Revisions [101]		- 0 X
File Find Options Help		<u>c</u> dit <u>G</u> roup <u>Options</u> (Or <u>d</u> er <u>T</u> ools <u>S</u> pecial <u>H</u> elp		
Run Program		Í∎ / € ∽ R ® S		M T N?	
Exit Alt+F4	Т	Tester Type	ADVANTEST > Advantest		
	F	^P rogram ID	115676 FT: FTRKGD (GLOB11=	=0)	
	F	Revision	D New Rev		
		/ersion	New Ver	Prog Overrides	
			F	Prog HW Limits	
	9	Status		Model Limit	
		Test Program	S25VRVTA		
		lob Name	FTRKGD		
		Checksum	NA		
		Bin Group	STD SFMD BIN1 > PASS BIN1, FAIL	BIN5/6/7/8	
		Correlation Process Code			
	0	Correlation Good Bin	0		
	9	Special Instructions			
	J	1750 OI Ver			
	C	CN Number	1401974		
	() ()	Comments	GLOB11=0; FT, RETEST BIN8		
	F	RMA Disposition	No Special Requirements	→	
mtgxxxenr02001 Company:101 WW 0ps Co -MCHP- Cor Date	:11-24-2016				



Exit From Baan PDC - 2

From: Microchip Information Systems Sent: Sunday, January 15, 2017 10:27 PM To: #All WorldWide Employees Subject: Baan licenses - please read

If you are not a Baan user, you may disregard this message.

Our Baan licenses are a precious and costly resource which Microchip owns. In the past few days, we have had issues with the number of users who are able to log onto the system, especially in the heavily used Asia region.

Frustrations rise quickly if you need to log in and you get a message saying that the user limit has been reached.

We have enough licenses to support everyone's work if we are careful in how we manage our Baan log in activity.

Please be aware of 2 key rules for using your Baan account

- 1. If you are not actively working in the system, LOG OFF!
- 2. When you do log off, DO NOT use the red "X" in the upper right corner to close your account
 - a. ALWAYS use the File > Exit path to leave the application



We are monitoring system usage and we do have some clean-up tools for helping to remove stale accounts, but it is better to develop good Baan habits from the beginning than to chase these problems. Thank you for your help in keeping our Baan system available for everyone.

Please do not reply to this email address. Please send any questions to the Helpdesk at helpdesk@microchip.com or call 480.792.7600.





• Frequently use buttons :



Find/Search Function



Insert Function



Delete Function



Bowtie Function (Select the option)



Save Function

-	
	_

Save and Exit Function



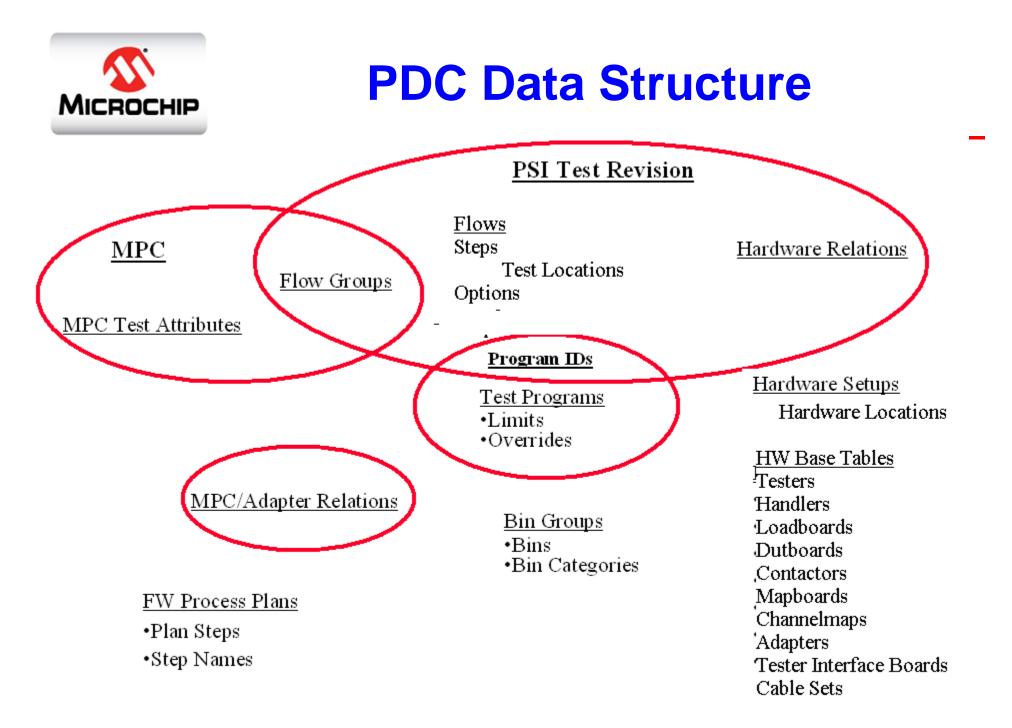
Copy Function



Next Function



Prior Function

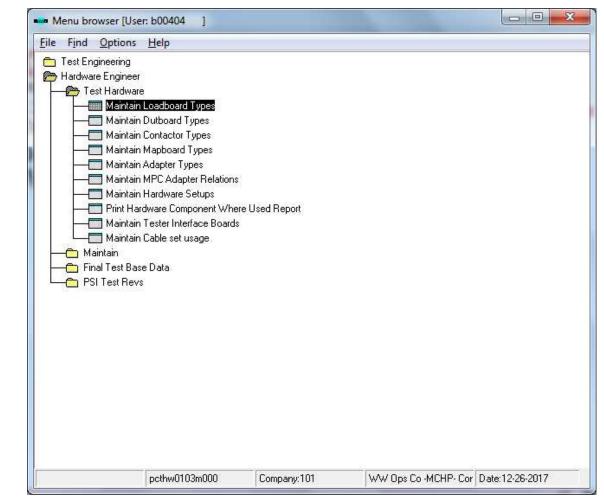




Types.

Add Loadboard Type - 1

- Double clicks "Hardware Engineer".
- Double clicks "Test Hardware" then "Maintain Loadboard





- Click Find button (M binoculars symbol)
- Input 14-Axxxxx Loadboard spec no. then press "Enter" button or click "OK" to verify if it has already existed or not.
 If not, it will go to the nearest Loadboard spec no.

pcthw0103m000 :	Maintain Loadboard T	/pes [101]				
	Options Order Too	ls <u>S</u> pecial			T N?	
Loadboard Type Loadboard Name Site Count Mfg Part No.	1-4-A11710 ATQ 12x12-80M-P 9 MCHP16-0163-A	Status IC24FJ128	ACTIVE _	T	ester Types	
					Maintain Loadboar adboard Type: 1-4-	1



•••• pcthw0103m000 : N	Maintain Loadboard Types [101]
<u>File Edit G</u> roup (<u>D</u> ptions Or <u>d</u> er <u>T</u> ools <u>S</u> pecial <u>H</u> elp
Loadboard Type	14-A11745 Status ACTIVE Tester Types
Loadboard Name	LB CREDENCE D1X MEC1322 x7 132L DQFN 11x11 Delta Edge
Site Count	7
Mfg Part No.	

pcthw0103m000 : Maintain Loadboard Types [101]		
<u>File Edit Group Options Order Tools Special Help</u>		
	` №?	
Loadboard Type Status Test	er Types	
Loadboard Name		
Site Count		
Mfg Part No.		
	add	alphanum / zoom



- Click 🔠 Insert button.
- Input 14-Axxxxx Loadboard spec no. at Loadboard Type field.
- Input Loadboard Name (120 Char. Max) per Title of DMS data or Description of Hardware Database System (HDS).

DMS Data

Drag a colur	Drag a column header and drop it here to group by that column									
Detail	Туре	Status	Spec Num	Revision	Title		Originator	Owner	Part Number	Incorporated CN
			14-A11742							
View Detail	14	Current	14-A11742	A	ST CREDENCE DIAMOND D2X PIC18F66J16/67J11 x56 64L TQFP 10x10x1 MCT-TAPESTRY		Siam Pungmaneesakul - B04711	Siam Pungmaneesakul - B04711	MCHP16-0199-A	CN_14006870

HDS Data

Drag a column header and drop it here to group by that column										
Job Number 🔺	Spec Number	Description	Board Type	Priority	Job Type	Status				
T	14-A11742		T	T	T	T				
<u>ST-18399</u>	14-A11742	ST CREDENCE DIAMOND D2X PIC18F66J16/67J11 x56 64L TQFP 10x10x1 MCT-TAPESTRY	ST	2	StripTest	RTE/Complete				



- Input Site Count (Number of maximum sites of the Loadboard).
- Click "Tester Types" button.

•••• pcthw0103m000 : I	Maintain Loadboard Types [101]	
<u>File Edit Group</u>	Options Order Tools Special Help □	
Loadboard Type	14-A11742 Status ACTIVE Tester Types	
Loadboard Name	ST D2X PIC18F66J16/67J11 X56 64L TQFP 10x10x1 MCT-TAPESTRY	
Site Count Mfg Part No.	56	
Mig Fait No.		

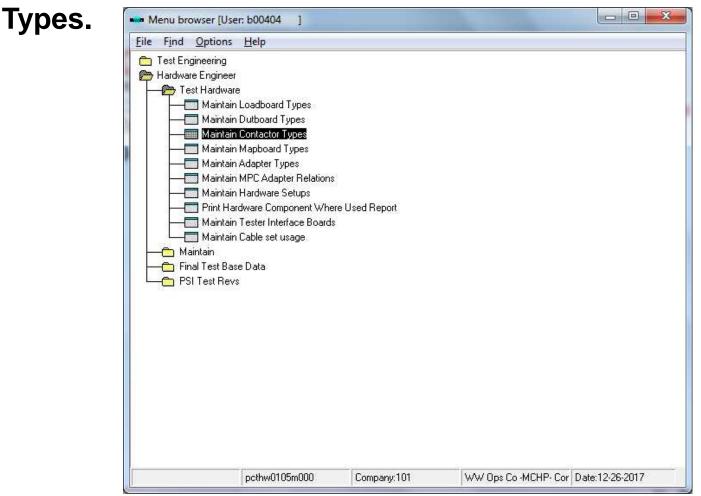


- Click Insert button. Select Tester Type or Type it in.
 Note : See the list of Tester Types by moving mouse to the triangle sign. Click mouse when it changes to magnifier.
- Click I "Save & Exit" button to go back to the 1st page.
- Click I "Save & Exit" button again to go back to "Hardware Engineer" screen.

pcthw0113s000 : Maintain Loadboard Type/Tester Types Relations [101]		
le <u>E</u> dit <u>G</u> roup <u>O</u> ptions Or <u>d</u> er <u>T</u> ools <u>S</u> pecial <u>H</u> elp		
Loadboard Type 14-A11742		
Tester Type Description		
LTX_D2X_TH > Dual-chassis Diamond-X with Stinger and Top hat		-
· · · · · · · · · · · · · · · · · · ·		
		•
	add	alphanum / zoom



- Double clicks "Hardware Engineer".
- Double clicks "Test Hardware" then "Maintain Contactor





- Click Find button (M binoculars symbol)
- Input 14-Axxxxx Contactor spec no. then press "Enter" button or click "OK" to verify if it has already existed or not.

If not, it will go to the nearest Contactor spec no.

pcthw0105m000	: Maintain Contactor Types [101]			Maintain Contactor Types - Find	×
<u>File Edit G</u> roup	<u>O</u> ptions Or <u>d</u> er <u>T</u> ools <u>S</u> pecial <u>H</u> elp				
- 🖬 🚭 🗠		() 	Γ Ν?	Contactor Type: 14-A-10550	OK
Contactor Type	Contactor Name	Status			Cancel
14-A-10550	11X11mm 80L QFN	ACTIVE -	-		
14-A-9034	CT 64L TQFP 10x10 x8 NS7k	ACTIVE -			
14-A10034	48L QFN 7X7	ACTIVE -			
14-A10252	CT 14L SOIC 150 x4 RASCO SO2000	ACTIVE -			
14-A10256	MEQ1000 QFN 4X4 RASCO	ACTIVE -			
14-A10429	CT x4 8L VDFN 5x6x0.85 RASCO SO1000	ACTIVE -			
14-A10436	48L QFN 7X7	ACTIVE -			
14-A10508	CT 24TSSOP AETRIUM	ACTIVE -			
14-A10517	16L QFN 4x4 x1 SRM	ACTIVE -			
14-A10518	SOIC	ACTIVE -			
14-A10530	CT 16L WQFN 3X3 x1 SRM	ACTIVE -			
14-A10550	80L 11x11 VQFN	ACTIVE -			
14-A10594	CT x1 44L QFN 7x7 SYNAX SX1211	ACTIVE -	-		
				1	
				U	



- Click 🔠 Insert button.
- Input 14-Axxxxx Contactor spec no. at "Contactor Type" field.
- Input Contactor Name (120 Char. Max) per Title of DMS data or Description of Hardware Database System (HDS).

DMS Data

Drag a colur	Drag a column header and drop it here to group by that column										
Detail	Detail Type Status Spec Num Re		Revision	Title	Originator	Owner	Part Number	Incorporated CN			
		-	14-A7303								
View Detail	14	Current	14-A7303	А	CT x56 64L TQFP 10X10 MCT-TAPESTRY	Murriel Natiola - A04734	Valen Burd - C10844		CN_14002107		

HDS Data

Drag a column header and drop it here to group by that column									
Job Number 🔺	Spec Number	Description	Board Type	Priority	Job Type	Status			
T	14-A7303 T	T	T		T	T	T		
<u>CT-13938</u>	14-A7303	CT x56 64L TQFP 10x10x1 MCT-TAPESTRY	ст	0	Contactor	Closed			



- Press "Tab" button to move the cursor to "Status" field. The default value is ACTIVE.
- Click 🗐 "Save & Exit" button to go back to "Hardware Engineer" screen.



- Double clicks "Hardware Engineer".
- Double clicks "Test Hardware" then "Maintain Tester Interface Boards. Menu browser [User: b00404]

<u>F</u> ile	Find	<u>Options</u>	Help			
😰 Н		Engineer				
		Hardwar	e 1 Loadboard Types			
			Dutboard Types			
			Contactor Types			
			Mapboard Types			
			Adapter Types MPC Adapter Relation	8		
			Hardware Setups	519 5		
			rdware Component Whe	See a second care a second s		
			Tester Interface Board	S		
	n Maii	i Maintain htain	i Cable set usage			
-		Test Bas	se Data			
	🛅 PSI	Test Rev	s			
-			011E000	C101	MALCON CONCURPEND	
			pcthw0115m000	Company:101	WW Ops Co -MCHP- Cor D	ate.12-26-2017



• Input Tester Type, Tester Interface (14-Axxxx) and Description (120 Char. Max) per Title of DMS data.

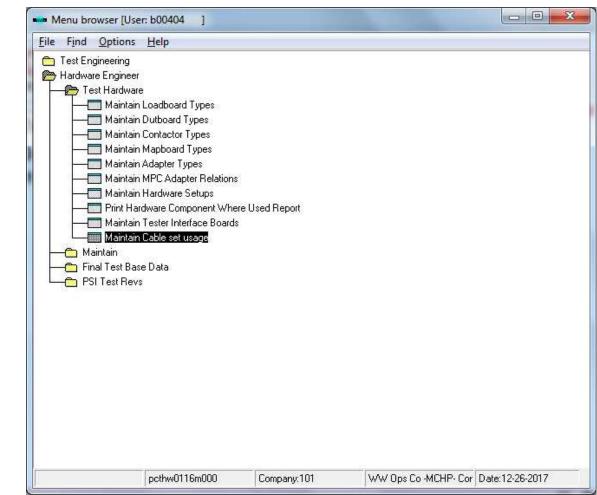
pcthw0115m000 : Maintain Tester Interface Boards [101]									
<u>File E</u> dit <u>G</u> roup <u>Options</u> Or <u>d</u> er <u>T</u> ools <u>Special H</u> elp									
Tester Type Tester Interface Description	Status								
	i	- 3							
		-							
		_							
		_							
		•							
	first								
	10.94								



usage.

Add Cable set usage - 1

- Double clicks "Hardware Engineer".
- Double clicks "Test Hardware" then "Maintain Cable set





Add Cable set usage - 2

 Input Cable Set (14-Axxxx) and Description (120 Char. Max) per Title of DMS data.

pcthw0116m00	0 : Maintain Cable set usage [101]		
	o <u>O</u> ptions Or <u>d</u> er <u>T</u> ools <u>S</u> pecial <u>H</u> elp		
E 8 🚳 👱		T N?	
Cable Set	Description	Status	
14-A12308	CSPPR Nextest Magnum 1 PV 59Z02, 59Z03 x256 WLCSP	ACTIVE	
			-
i —		İ	-



- Hardware Setup data in PDC will be shown at each test step of Test Setup Options (Per package code of the MPC).
- If "Prog HW Limits" of the Program ID has been used, it will show only the assigned Setup ID.

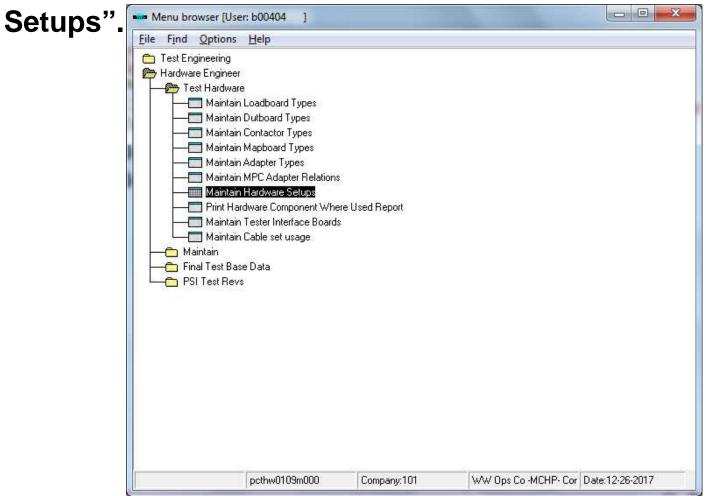
FI1@	JINZ5C		
🗌 Teste	er Type: J750	Program ID:114137	Legacy Op Num:
1) Tes	ter Models		
	J750_1024		
	J750_128		
	J750_256		
	J750_512		
	J750_640		
	J750_832		
	J750_MSO_128		
	J750_MSO_256		
	J750_SCAN		
	J750_SCAN_448		
	J750_SCAN_512		

2) Hardware Setups for 44/44TQFP_10 (T4X)

SETUP ID	HANDLR.	LOADBOARD	CONTACTOR	DUTBOARD	MAPBOARD			
	CHANNELMAP	ADAPTER	TESTER INTERFACE BOARD	CABLE SET	H/W LOCATION			
44	CAS	14-A3100	14-A1961					
	x3dlc44tqfp_128p				MTAI,			
45	CAS	14-A3100	14-A1961					
	x4dlc44tqfp_256p				MTAI,			
63	SCH	14-A4254	14-A2834					
	x20mct44tqfp				MTAI,			
	SETUP ID 44 45	SETUP ID HANDLR. CHANNELMAP 44 CAS x3dlc44tqfp_128p 45 CAS x4dlc44tqfp_256p 63 SCH	SETUP IDHANDLR. CHANNELMAPLOADBOARD ADAPTER44CAS x3dlc44tqfp_128p14-A310045CAS x4dlc44tqfp_256p14-A310063SCH14-A4254	SETUP IDHANDLR. CHANNELMAPLOADBOARD ADAPTERCONTACTOR TESTER INTERFACE BOARD44CAS 	SETUP IDHANDLR. CHANNELMAPLOADBOARD ADAPTERCONTACTOR TESTER INTERFACE BOARDDUTBOARD CABLE SET44CAS x3dlc44tqfp_128p14-A310014-A1961CABLE SET45CAS x4dlc44tqfp_256p14-A310014-A1961CABLE SET63SCH14-A425414-A2834CAS CASCAS CAS			



- Double clicks "Hardware Engineer".
- Double clicks "Test Hardware" then "Maintain Hardware





Create Hardware Setup ID - 3

- Click Find button (m binoculars symbol).
- Input Lead Count and Configuration then press "Enter" button or click "OK" button.
- It will go to the 1st Setup ID. Click Next button to see if there is the Hardware Setup ID you needed.

🚥 pcthw0109m000 : Maintain Hardware Setups [101]	Maintain Hardware Setups - Find
Eile Edit Group Options Order Iools Special Help Image: Second Count Image:	Lead Count: 44 Configuration: 44TQFP 10 Setup ID: 1 Cancel
Tester FLEX Teradyne Integra FLEX Handler MAN MANUAL HAND TEST Loadboard 14-A3015 LB Teradyne MRF24J40 x4 QFN 6X6 MULTITEST 9918-HC Dutboard Mapboard	
Contactor 14-A1062 STANDARD CONTACTOR Channelmap X4MUL40QFN Tester Interface Cable Set	



- If there is no Hardware Setup ID you needed.
- Click 🔣 Insert button to create new Hardware Setup ID.
- Input Lead Count, Configuration (handler kit), Tester, Handler.

pcthw0109m000 : Ma	intain Hardware Setups [1	01]			
<u>File Edit Group Op</u>	ntions Or <u>d</u> er <u>T</u> ools <u>S</u> p	ecial <u>H</u> elp			
E B & r R	1 🗈 🖧 M 🖊 🖣	 • • • • • • • • • • • • • • • • •	T * ?		
Lead Count					
Configuration	·				
Setup ID	999 Status	ACTIVE •	r ⊺ _{est}	Location	
Tester		•			
Handler		•			
Loadboard					
Dutboard					
Mapboard	•				
Contactor					
Channelmap					
-	· · · · · · · · · · · · · · · · · · ·				
Tester Interface					
Cable Set	•				
				add	numeric
		and the second se			

Configuration

Create Hardware Setup ID - 5

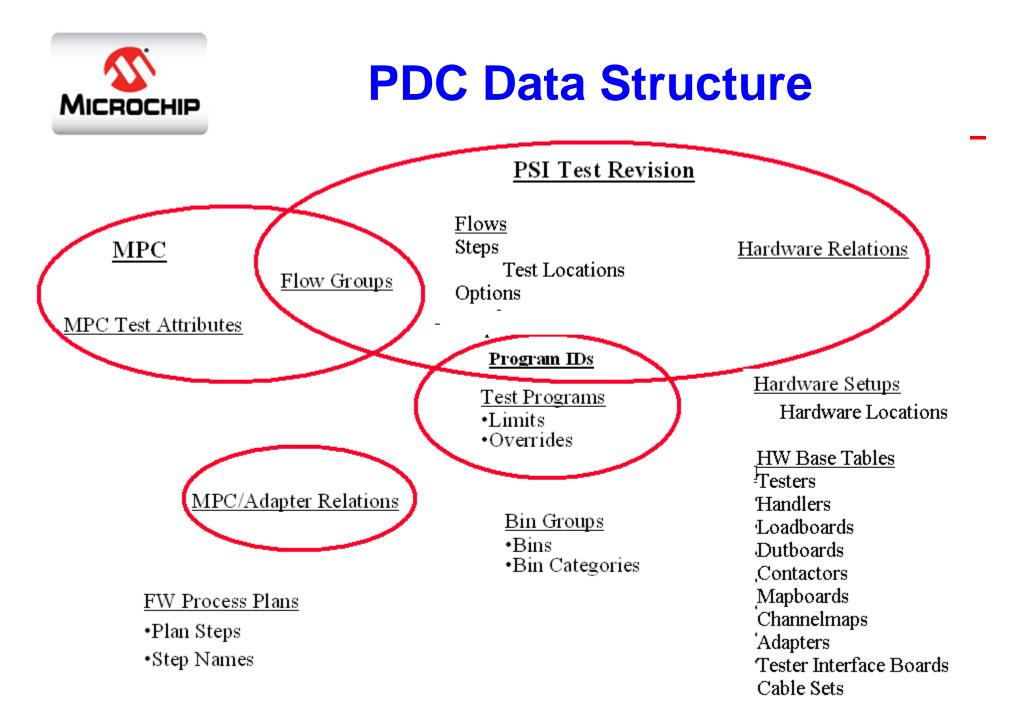
- Input Load Board, DUT Board, Map Board, Contactor, Channel Map, Tester Interface Board, Cable Set.
- Note : Not need to input all of them, depends on Tester Type.
- Click "Test Location" button to add Test Plant.

MICROCHIP

Test Plant

Tester Type

- Click I "Save & Exit" button after finished inputting all necessary data to new Hardware Setup ID.
- In case that you can't find Configuration (Package) you needed, please inform Soravorn Pochpring - B00022 (MThai IE) to create it.





• List of available Bin Groups as of Jan 4, 2018

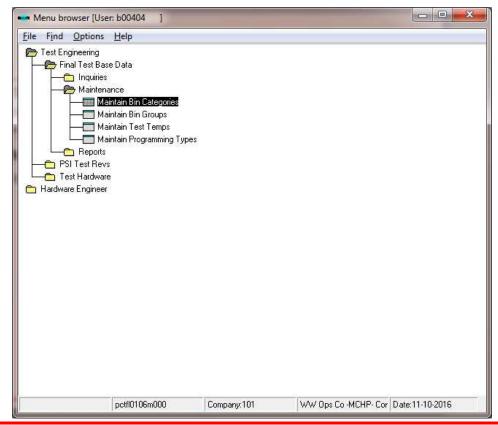


Bin Groups

<u>E</u> dit <u>G</u> roup <u>O</u> pti		line from the second second base	
		44 DD DD T N	?
Bin Group	Description	Bin Group Type	Status Bin Details
ATML ARM-NTO1	1-GOOD, 5-0/S, 6-PA/FU	PHYSICAL -	ACTIVE -
ATML ARM-RF01	1-GOOD, 2-PA, 3-FU, 4-0/S	PHYSICAL -	ACTIVE
ATML ARM-RF02	1-GOOD, 2-PA, 3-FU, 4-0/S, 5-Analog	PHYSICAL -	ACTIVE
ATML ARM-RF03	1-GOOD, 2-PA, 3-FU, 4-0/S, 5-Analog,	PHYSICAL -	ACTIVE -
ATML ARM-RF04	1-GOOD, 2-PA, 3-FU, 4-0/S, 5-Analog,	PHYSICAL -	ACTIVE -
ATML ARM-RF05	1-GOOD, 2-PA, 3-FU, 4-IDD, 5-EEPROM	PHYSICAL -	ACTIVE -
ATML CPLD1	1-GOOD, 5-PA/FU, 6-CONT/LEAK	PHYSICAL -	ACTIVE -
ATML CRYPTO1	1-GOOD, 2-0/L VOL/H, 3-PA/FU, 4-1	PHYSICAL -	ACTIVE -
ATML NTO NPI1	1-GOOD, 2-GOOD-BIN2, 3-GOOD-BIN3	PHYSICAL -	ACTIVE -
ATML NTO1	1-GOOD, 2-GOOD-B2, 3-GOOD-B3, 4-GOOI	PHYSICAL -	ACTIVE -
ATML RFA1	2-GOOD, 1-PA/FU, 3-NON RETEST, 4	PHYSICAL -	ACTIVE
ATML SEEPROM 1	1-GOOD, 2-GOOD Normal W, 5-0/S,	PHYSICAL -	ACTIVE
ATML SEEPROM 2	1-GOOD, 7-0/S, 8-OTHER	PHYSICAL -	ACTIVE -



- If no existing Bin Group you can use.
- Under "Test Engineering", Double clicks "Final Test Base Data" then Double clicks "Maintenance".
- Double clicks "Maintain Bin Categories".





- Look for Bin Categories you needed.
- If you can't find the one you needed, click button to add new Bin Category.
- Note : Can't delete any Bin Category, only change it to be INACTIVE. The artfoliom 000 i Maintain Bin Categories 1011

CURRENT Current Failure FAIL ACTIVE	
Image: Status Image: Status Bin Category Description Bin Grade Status ANA Analog Test Failure FAIL ACTIVE BIN 5 SFMD BIN 5 - STAMP FAIL ACTIVE BIN 6 FAIL INACTIVE INACTIVE BIN 7 FAIL INACTIVE FAIL INACTIVE BIN 8 Data Retention and Critical Failures FAIL ACTIVE ACTIVE BIN6 Screen Out Tested Good Units FAIL ACTIVE ACTIVE BIN7 Function and Failures After Data Retentiv FAIL ACTIVE ACTIVE BIN8 DC and Failures Before Data Retention Te: FAIL ACTIVE ACTIVE CODEC Codec Failure FAIL ACTIVE ACTIVE	
ANA Analog Test Failure FAIL ACTIVE BIN 5 SFMD BIN 5 - STAMP FAIL INACTIVE BIN 6 FAIL ACTIVE ACTIVE BIN 7 FAIL INACTIVE ACTIVE BIN 8 FAIL INACTIVE INACTIVE BIN5 Data Retention and Critical Failures FAIL INACTIVE BIN6 Screen Out Tested Good Units FAIL ACTIVE BIN7 Function and Failures After Data Retenti FAIL ACTIVE BIN8 DC and Failures Before Data Retention Te: FAIL ACTIVE CODEC Codec Failure FAIL ACTIVE ACTIVE	
BIN 5 SFMD BIN 5 - STAMP FAIL INACTIVE BIN 6 FAIL FAIL ACTIVE BIN 7 FAIL INACTIVE INACTIVE BIN 8 FAIL INACTIVE INACTIVE BIN5 Data Retention and Critical Failures FAIL INACTIVE BIN6 Screen Out Tested Good Units FAIL ACTIVE BIN7 Function and Failures After Data Retentit FAIL ACTIVE BIN8 DC and Failures Before Data Retention Te: FAIL ACTIVE CODEC Codec Failure FAIL ACTIVE CURRENT Current Failure FAIL ACTIVE	
BIN 6 FAIL * ACTIVE BIN 7 FAIL * INACTIVE BIN 8 FAIL * INACTIVE BIN5 Data Retention and Critical Failures FAIL * INACTIVE BIN6 Screen Out Tested Good Units FAIL * ACTIVE BIN7 Function and Failures After Data Retenti; FAIL * ACTIVE BIN8 DC and Failures Before Data Retention Te; FAIL * ACTIVE CODEC Codec Failure FAIL * ACTIVE CURRENT Current Failure FAIL * ACTIVE	
BIN 7 FAIL INACTIVE BIN 8 FAIL INACTIVE BIN5 Data Retention and Critical Failures FAIL ACTIVE BIN6 Screen Out Tested Good Units FAIL ACTIVE BIN7 Function and Failures After Data Retentitive FAIL ACTIVE BIN8 DC and Failures Before Data Retention Te: FAIL ACTIVE CODEC Codec Failure FAIL ACTIVE CURRENT Current Failure FAIL ACTIVE	- I
BIN 8 FAIL INACTIVE BIN5 Data Retention and Critical Failures FAIL ACTIVE BIN6 Screen Out Tested Good Units FAIL ACTIVE BIN7 Function and Failures After Data Retentit FAIL ACTIVE BIN8 DC and Failures Before Data Retention Te: FAIL ACTIVE CODEC Codec Failure FAIL ACTIVE CURRENT Current Failure FAIL ACTIVE	•
BIN5 Data Retention and Critical Failures FAIL ACTIVE BIN6 Screen Out Tested Good Units FAIL ACTIVE BIN7 Function and Failures After Data Retentit FAIL ACTIVE BIN8 DC and Failures Before Data Retention Te: FAIL ACTIVE CODEC Codec Failure FAIL ACTIVE CURRENT Current Failure FAIL ACTIVE	-
BIN6 Screen Out Tested Good Units FAIL ACTIVE BIN7 Function and Failures After Data Retention FAIL ACTIVE BIN8 DC and Failures Before Data Retention Te: FAIL ACTIVE CODEC Codec Failure FAIL ACTIVE CURRENT Current Failure FAIL ACTIVE	-
BIN7 Function and Failures After Data Retenti; FAIL • ACTIVE BIN8 DC and Failures Before Data Retention Te: FAIL • ACTIVE CODEC Codec Failure FAIL • ACTIVE CURRENT Current Failure FAIL • ACTIVE	
BIN8 DC and Failures Before Data Retention Te: FAIL • ACTIVE CODEC Codec Failure FAIL • ACTIVE CURRENT Current Failure FAIL • ACTIVE	
CODEC Codec Failure FAIL ACTIVE CURRENT Current Failure FAIL ACTIVE	-
CURRENT Current Failure FAIL - ACTIVE	-
	-
DC Failure FAIL - ACTIVE	-
	-
DEFECTS Strip Test Defect Bin FAIL · ACTIVE ·	ने 🖃



- Input Bin Category (10 Char. Max), Description (50 Char. Max).
- Select Bin Grade and set Status to be ACTIVE.
- Click Save and Exit button once complete adding all new Bin Categories.

nem pctfl0106m000 : Maintain Bin Categories [101]		
<u>File Edit Group Options Order Tools Special H</u> elp	W States and the second s	
	DI T N?	
Bin Category Description E	tin Grade Status	
INFO Information		
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	add	enum



- Double clicks "Maintain Bin Groups".
- Click 🕮 button to add new Bin Group.

🚥 pctfl0104m000 : Maintai	n Bin Groups [101]			
<u>File Edit Group Optio</u>	ns Or <u>d</u> er <u>T</u> ools <u>S</u> pecial <u>H</u> elp			
9 8 6 18		* • • • • • • • • • • • • • • • • • •	?	
Bin Group	Description	Bin Group Type	Status	Bin Details
ADC J750	ADC Product on J750	PHYSICAL -	ACTIVE -	
ADC J750 SIMECA	ANALOG ADC (ABAJ1) SOT23 5L PKG (PHYSICAL -	ACTIVE -	
ADC J750 SOT23	Analog ADC SOT23 5L on FSA / SIMI	PHYSICAL -	ACTIVE	
AIPD (MSOP-ISM)	Ismaca Bin for MSOP package	PHYSICAL -	ACTIVE	
AIPD GOOD BIN4	Multi good bin for Vref product (PHYSICAL -	ACTIVE -	
AIPD J750 STD	Standard bin for AIPD J750	PHYSICAL -	ACTIVE -	-
AIPD TEMP BIN	AIPD NEXTEST PT BINNING PASS=BIN.	PHYSICAL -	ACTIVE -	
AIPD TEMP BIN2	AIPD NEXTEST PT BINNING PASS=BIN:	PHYSICAL -	ACTIVE -	
AIPD TO92 BIN4	Multi good bin for FSA2020	PHYSICAL -	ACTIVE -	
AIPD (EPSON)	1-Good, 3-PA, 4-FU, 5-OS, 6-PAT	PHYSICAL -	ACTIVE -	
ANALOG	Bins 1, 7, 8, 5	PHYSICAL -	ACTIVE -	
ANALOG (MAN)	Bins For Manual Hand Test and Sai	PHYSICAL -	ACTIVE -	
ANALOG (SIMECA)	BIN 1,2,3,4,5,6,7	PHYSICAL -	ACTIVE -	•



- Input Bin Group (15 Char. Max), Description (50 Char. Max).
- Select Bin Group Type and set Status to be ACTIVE.

pctfl0104m000 : Maintain Bin Groups [101]	• pctfl0104m000 : Maintain Bin Groups [101]							
<u>File Edit Group Options Order Tools Special H</u> elp	<u>File E</u> dit <u>Group Options</u> Or <u>d</u> er <u>T</u> ools <u>Special H</u> elp							
Bin Group Description Bin Group Type Status	Bin Details							
SAMPLE Bins 1,3,5]							
]							
add	enum							



- Select the Bin Group you just added.
- Click "Bin Details" button.

-	pctfl0104m000 : N	Maintain Bin Groups [101]			
	e <u>E</u> dit <u>G</u> roup			171	
E	1 🖬 🎒 👱			?	
	Bin Group	Description	Bin Group Type	Status	Bin Details
V	SAMPLE	Bins 1,3,5	LOGICAL	ACTIVE ·	
					•



- Click Insert button to add Bin Number
- Press Tab button twice and select Bin Category.
- Add next Bin Number until complete all of them.

pctfl0105s000	: Maintain Bin Details [101]		-	
<u>File Edit Grou</u>	up <u>O</u> ptions Or <u>d</u> er <u>T</u> ools <u>S</u> pecial <u>H</u> elp		7	
888				
Bin Group	SAMPLE			
Bin Number	Description	Bin Category		
				1000
		_ <u>_ </u>		
	1			
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		- <u></u> •		
		_ <u> </u> ;		
		- ii		
		- <u> </u>		
		•		<u> </u>
			last	



• Click 🗏 Save and Exit button.

pctfl0105s000 : Maintain Bin Details [101]				
<u>File Edit Group Options Order Tools Special H</u> elp	1 1		T. mark Barry F.	
			1 <u>T</u> ₩?	
Bin Group SAMPLE				
Bin Number Description	Bin Catego	ſy		
1	PASS	-	Successfully tested device	. 🔺
3	PA	_•	Parametric/DC Test Failure	. —
5	0/5	<u> </u>	Open/Short Test Failure	
		_		
		-		
i i	- i			
		-		
	- <u> </u>	_		
	11	_		.
			add	numeric



• Click Save and Exit button again to go back to Test Engineering screen.

	pctfl0104m000 : Ma	intain Bin Groups [101]		
		ptions Or <u>d</u> er <u>T</u> ools <u>S</u> pecial		
E		ł 🖻 品 🖊 K K ♦ 🕨		
	Bin Group	Description	Bin Group Type Status	Bin Details
V	SAMPLE	Bins 1,3,5	LOGICAL ACTIVE	-
	1			•



- Test Program ID data in PDC will be shown at Program Options of Test Setup Options.
- Those data will be shown at Main Source, Checksum, Executable Name, Bins, OS, Special Instructions, Comments.

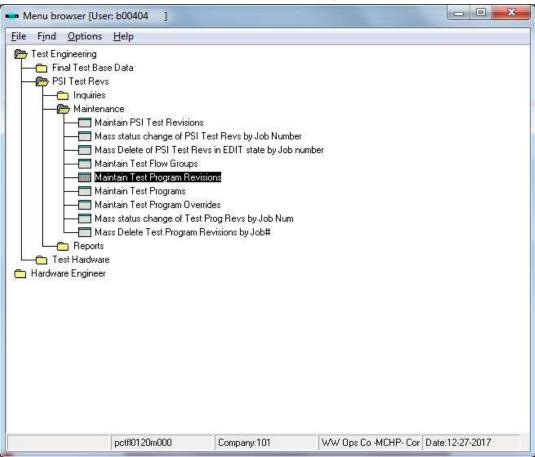
ID: 114137 Rev: AX	er: 0 Status: ACT		Verified
Main Source:	LEAR0_FT_A48e.xls		
Checksum:	16540E2		
Executable Name:	ft-qtp-std		
Part Number:	18F46K20		
Temperature:	IN25C		
Programming Type:	QТР		
Bins:	2-PASS, 3-PA, 4-FU, 5-O/S		
Hardware Limits:			
Correl. Process Code:			
Correlation Good Bin:	0		
OS Version			
Special Instructions:			
CN Number:	1700910		
Comments:			



- Review Test Program Name and Test Program Option of each test step to minimize the number of Program IDs to be created.
- Example : One Mask has 2 Part Numbers (A & B)
 - Test Flow for Part A : FT 110C \rightarrow FT 25C \rightarrow QC 25C
 - FT 110C & FT 25C : Use the same TP & TP Option Need only 1 Program ID for these 2 steps (Program ID1).
 - QC 25C uses the same TP but different TP Option Need another Program ID for this step (Program ID2).
 - Test Flow for Part B : FT 110C → FT 25C → QC 25C
 FT 110C uses the same TP & TP Option as FT 110C of Part A so, can use
 Program ID1.
 - FT 25C, QC 25C use the same TP as Part A but different TP Option. Need to create 1 Program ID for FT 25C (Program ID3)
 Need to create 1 Program ID for QC 25C (Program ID4)



- Under "Test Engineering", Double clicks "PSI Test Revs".
- Double clicks "Maintenance".
- Double clicks "Maintain Test Program Revisions".





• Click Insert button to create new Program ID.

ne pctfl0120m000 : Maintain Tes	t Program Revisions [101]
<u>File Edit Group Options</u>	Dr <u>d</u> er <u>T</u> ools <u>S</u> pecial <u>H</u> elp
Tester Type	ADVANTEST Advantest
Program ID	115676 FT: FTRKGD (GLOB11=0)
Revision	D New Rev
Version	New Ver Prog Overrides
	Prog HW Limits
Status	ACT Model Limit
Test Program	S25VRVTA
Job Name	FTRKGD
Checksum	NA
Bin Group	STD SFMD BIN1 PASS BIN1, FAIL BIN5/6/7/8
Correlation Process Code	
Correlation Good Bin	0
Special Instructions	
J750 OI Ver	
CN Number	1401974
Comments	GLOB11=0; FT, RETEST BIN8
RMA Disposition	No Special Requirements
OS Version	Job Number



• Select Tester Type then click OK button.

🚥 pctfl0120m000 : Maintain Test Progr	am Revisions [101]		
<u>File Edit Group Options Order</u>	<u>T</u> ools <u>S</u> pecial <u>H</u> elp		
∃ ■ ● ▶ № № № ₩	<u> </u>		
Tester Type			
Program ID	pcthw0501s000 : Displa	iv Tester Types [101]	
Revision 🔽	1	•	
Version	<u>File Edit Group Optic</u>		al and free l
		<u>∎</u> <u>M</u> <u> </u>	I T № ?
Status	Tester Type	Description	
	rester rype	Description	ОК
	ICTS	ICTS	Cancel
Checksum	IHT	IHT	
Bin Group			
Correlation Process Code	IMPACT	Impact	
Correlation Good Bin	ITS9K	XCERRA SCHLUMBERGER	
Special Instructions	√ <u>J750</u>	Teradyne J750 (all models)	
J750 OI Ver	J750_AERO	Teradyne J750 with AeroFlex test option	
CN Number	J750_HD	Teradyne J750HD	
Comments	J750_LITE	Teradyne J750 with Litepoint option	
RMA Disposition	J750_MS0	Teradyne J750s with MSO Option	
OS Version	J750_SCAN	Teradyne J750 with Scan option	
Γ		ľ	



• Input Test Program name (60 characters maximum ; Main Source on Test Setup Options).

pctfl0120m000 : Maintain Te	st Program Revisio	ons [101]		
<u>File Edit Group Options</u>	Or <u>d</u> er <u>T</u> ools <u>S</u>	pecial <u>H</u> elp		
	<u>R M I (</u>	IN N	44 >> >>	T N?
Tester Type	J750			
Program ID	999999			
Revision	A	New Rev		
Version		New Ver	E I	Prog Overrides
			Г _ F	Prog HW Limits
Status	EDIT -		Г	Model Limit
Test Program	TEST1			
Job Name				
Checksum				
Bin Group		}		
Correlation Process Code				
Correlation Good Bin				
Special Instructions				Ĩ
J750 OI Ver				
CN Number				
Comments				
RMA Disposition	Contact Engi	neering	•	
OS Version	1		Job Number	
			add	alphanum



- Input Test Program Option (Job Name for below example, 60 characters maximum; Executable Name on Test Setup Options). Can leave it blank for some Tester Types.
- Test Program Checksum is an option, can leave it blank.
- Select Bin Group.

<u>File Edit Group Options</u>	Or <u>d</u> er <u>T</u> ools <u>S</u> pecial <u>H</u> elp			
🖸 📕 🎒 🗠 🚠 🖻		4 44 44 44 4	T N?	
Tester Type	J750			
Program ID	999999			
Revision	A New Rev			
Version	0 New Ver	Г	Prog Overrides	
			Prog HW Limits	
Status	EDIT	Г	Model Limit	
Test Program	TEST1			
Job Name	OPTION1			
Checksum				
Bin Group	STANDARD Bir	s 2 - 5		
Correlation Process Code				
Correlation Good Bin				
Special Instructions				
J750 OI Ver				
CN Number				
Comments				
RMA Disposition	Contact Engineering			
OS Version	1	Job Nu	mber	
			add alphar	num



- Put Special Instructions (120 Characters maximum) if needed.
- Put Comment (200 Characters maximum) if needed.
- Put OS Version (25 Characters maximum) if needed.
- If you want to limit Test HW that can be used for this program ID, click "Prog HW Limits" button then add Setup ID.

me pctfl0122s000 : Maintain Test Program HW Limits [101]	
Eile Edit Group Options Order Tools Special Help	
HW Setups Contact/Channel Locations	
Tester Type J750 Teradyne J750 (all models)	
Program ID 130997	
Revision A	
Version 0	
Lead Setup	
Count Configuration ID Tester Handler Loadboard Dutboard Mapbo	bard
	-
	·
	<u>•</u>
	•
	last



- Click "Save" button.
- Change Status from EDIT to PEND (Pending Approval).
- Click Circle "Save" button.
- Click Insert button to add the other new Program ID(s).
- Click I "Save & Exit" button after finished creating new Program ID(s).



- MThai Bin Groups
- **"STANDARD**" is standard MThai Bin Group for MCU Product.
 - [Bin 2 = PASS; Bin 3 = PA; Bin 4 = FU; Bin 5 = O/S]

🚥 pctfl0105s000 : Maintain Bin Details [101]				
Eile Edit Group Options Order Tools Special Help		ne sere constante de la		
▋▋▟▏▞ቘቘቘቘቚ፞ቚቚቚቚ	14 144 44	▶ ▶ T \?		
Bin Group STANDARD Bins 2 - 5				
birdioup pratoato bila 2 3				
Bin Number Description	Bin Catego	y		
2	PASS	Successful	ly tested device	<u> </u>
3	PA	Parametric	:/DC Test Failure	
4	FU	and states therein and	lity Test Failure	
5	0/5	Open/Short	: Test Failure	
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- MThai Bin Groups
- "STD PATTERN" is standard MThai Bin Group for Memory Product.
 - [Bin 2 = PASS
 - Bin 3 = PA ; Bin 4 = FU ; Bin 5 = O/S ; Bin 6 = PAT]

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Bin Number	Description	 	Bin Category PASS PA TU D/S PAT	 Successfully tested device Parametric/DC Test Failure Functionality Test Failure Open/Short Test Failure Pattern Code Failure 	
		f			•



 After the CN to release the Test Program has been approved, MThai Document Control Team will change its Status from PEND to ACT.

pctfl0120m000 : Maintain Tes	t Program Revisio	ons [101]			
<u>File Edit Group Options</u>	Dr <u>d</u> er <u>T</u> ools <u>S</u>	pecial <u>H</u> elp			
	<u>a a i </u>		44 	1 T N?	
Tester Type	J750	► Ter	adyne J750	(all models)	
Program ID	122222	LEARO_FT. F	-PRD-CERLE	ER	
Revision	U	New Rev			
Version		New Ver	Г	Prog Overrides	
		19	Г	Prog HW Limits	
Status	ACT -		Г	Model Limit	
Test Program	LEAR0_FT_A48	Be.XLS			
Job Name	FT-PRD-CERLER				
Checksum	16540E2				
Bin Group	STANDARD Bins 2 - 5				
Correlation Process Code					
Correlation Good Bin	0				
Special Instructions					
J750 OI Ver					
CN Number 1700910					
Comments					
RMA Disposition	No Special I	Requirements		•	
OS Version			Job Num	nber	

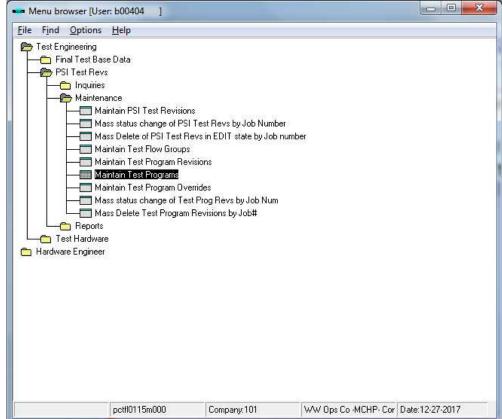


- When you need to release the new Test Program revision to production, click "New Rev" button, Update Test Program and Checksum.
- Click "Save" button and change Status from "EDIT" to "PEND"

pctfl0120m000 : Maintain Test	t Program Revisions [101]			
<u>File Edit Group Options</u>	Or <u>d</u> er <u>T</u> ools <u>S</u> pecial <u>H</u> elp			
Tester Type	J750 Teradyne J750 (all models)			
Program ID 122222 LEAR0_FT. FT-PRD-CERLER				
Revision	Vew Rev			
Version	New Ver Prog Overrides			
	Prog HW Limits			
Status	ACT - Model Limit			
Test Program	LEAR0_FT_A48e.XLS			
Job Name	FT-PRD-CERLER			
Checksum	16540E2			
Bin Group	STANDARD Bins 2 - 5			
Correlation Process Code				
Correlation Good Bin	0			
Special Instructions				
J750 OI Ver				
CN Number	1700910			
Comments				
RMA Disposition	No Special Requirements			
OS Version	Job Number			



- Program ID is 6-digit number which is very hard to remember so, we need to put description to each ID.
- Under "Test Engineering", Double clicks "PSI Test Revs".
- Double clicks "Maintenance" then "Maintain Test Programs".





- Click Find button (binoculars symbol), select Tester Type and click OK.
- The new Program ID you just added will be at the bottom of the table.

	∽ॾॎॾॾॺ॒ॺॺ		
Tester Type	ADVANTEST	Advantest	
Program ID	Description	Maintain Test Programs - Find	
115676	FT: FTRKGD (GLOB11=0)	Tester Type: J750	ОК
115677	FT: FTRKGD_BIN7 (GLOB11=	Program ID: 122222	Cancel
115678	EQC: QC		
115807	FT: FTH (GLOB11=0)		
115808	FT: FTHBIN7 (GLOB11=0)		
115810	FT: FTH (GLOB11=1)		
115811	EQC: QC		
115812	FT: FTHCT047 (GLOB4="CT0-	47" & GLOB11=0)	
115813	FT: FTHCT048 (GLOB4="CT048" & GLOB11=0)		
115815	FT: FTHCT049 (GLOB4="CT049" & GLOB11=0)		
115816	FT: FTHCT052 (GLOB4="CT052" & GLOB11=0)		
115817	FT: FTHCT053 (GLOB4="CT0	53" & GLOB11=0)	1.



- Input unique Description (50 characters maximum) for each Program ID you created.
- We usually put Test Program Name with no revision and Test Program Option.
- Click ""Save & Exit" button after finished adding description.



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Tester Typ	De LTX_D30 Triple-chassis Diamond		
Program ID	Description		
126399	UFAC0.STRIP.D30.F1-PRD-STD	*	
126481	DE058.STRIP.D30.F1-PRD-STD-ATO-IQC		
126484	DEBN0.STRIP.D30.F1-PRD-STD-IQC-ATO		
126485	DEBN0.STRIP.D30.F1-PRD-QTP-IQC-ATO		
126504	DE029.STRIP.D30.F1-PRD-STD-ATO		
126505	DE029.STRIP.D30.F1-PRD-QTP-ATO		
126592	DE044.STRIP.D30.F1-PRD-STD-CE		
126593	DE044.STRIP.D30.F1-PGM-QTP-CE		
126594	DE044.STRIP.D30.F1-RES-QTP-CE		
126651	DECU0.STRIP.D30.F1-PRD-STD-ATO		
126652	DECU0.STRIP.D30.F1-PRD-QTP-ATO		
126691		•	
		-	



pctfl0115m00	0 : Maintain Test Programs [101]	
<u>File Edit Grou</u>	up <u>O</u> ptions Or <u>d</u> er <u>T</u> ools <u>S</u> pecial <u>H</u> elp	
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Tester Type	e NEXTEST_PT Nextest PT	
Program ID	Description	
106754	C5AC2.2WIRE_150K 24AA256 Final Test	<u> </u>
106755	C5AC2.2WIRE_150K 24AA256 QC Sampling	
106756	C5AE2.2WIRE_150K 24LC128_MSOP Final Test	
106757	C5AE2.2WIRE_150K 24LC128_MSOP QC Sampling	
106758	C5AE2.2WIRE_150K 24AA128_MSOP Final Test	
106759	C5AE2.2WIRE_150K 24AA128_MSOP QC Sampling	
106760	C5AC2.2WIRE_150K 24LC256_MSOP Final Test	
106761	C5AC2.2WIRE_150K 24LC256_MSOP QC Sampling	
106762	C5AC2.2WIRE_150K 24AA256_MSOP Final Test	
106763	C5AC2.2WIRE_150K 24AA256_MSOP QC Sampling	
106770	C5AS1.2WIRE_150K 24LC01B Final Test Non Ambient	
106771	C5AS2.2WIRE_150K 24LC02B Final Test Non Ambient	+
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pctfl0115m000) : Maintain Test Programs [101]	
<u>File E</u> dit <u>G</u> rou	ip <u>O</u> ptions Or <u>d</u> er <u>T</u> ools <u>S</u> pecial <u>H</u> elp	
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Tester Type	ULTRAFLEX_2 Teradyne Ultra FLEX 24 Slots	
Program ID	Description	
124068	TCB01-0S81110-ULTRAFLEX_24-FT1	_
124069	TCB01-0S81110-ULTRAFLEX_24-FT2	
124070	TCB01-OS81110-ULTRAFLEX_24-QC1	
124071	TCB01-0581110-ULTRAFLEX_24-RT1	
124072	TCB01-0581110-ULTRAFLEX_24-SpynicFlash	
124073	TCB01-0581110-ULTRAFLEX_24-SpynicFV	
124504	TA208-LAN89218-ULTRAFLEX_24-FT1	
124505	TA208-LAN89218-ULTRAFLEX_24-FT2	
124506	TA208-LAN89218-ULTRAFLEX_24-QC1	
124507	TA208-LAN89218-ULTRAFLEX_24-RT1	
124751	TCC01-OS81118-ULTRAFLEX_24-FT1	
124752	TCC01-OS81118-ULTRAFLEX_24-FT2	.
	first	



pctfl0115m000	: Maintain Test Programs [101]					
<u>File Edit Group</u>	p <u>O</u> ptions Or <u>d</u> er <u>T</u> ools <u>S</u> pecial <u>H</u> elp					
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Tester Type	J750 Teradyne J750 (all models)					
Program ID	Description					
124132	LECF0_F.F1-PGM-SQTP	<u> </u>				
124133	LECF0_F.Q1-RES-SQTP					
124134	LECI0_LF:F1-PRD-STD-IQC					
124135	LECI0_LF:F1-PRD-CTE-IQC					
124150	MTAI:UGC01:FT1					
124151	MTAI:UGC01:FT2					
124152	MTAI:UGC01:FT3					
124153	MTAI:UGC01:QC1					
124154	MTAI:UGC01:RT1					
124179	YGAU0.Q1_STD_33F_44L_STRIP					
124180	YGAU0.Q1_STD_24H_44L_STRIP					
124181	YGAU0.Q1_MAGNA_33F_44L_STRIP					
r						



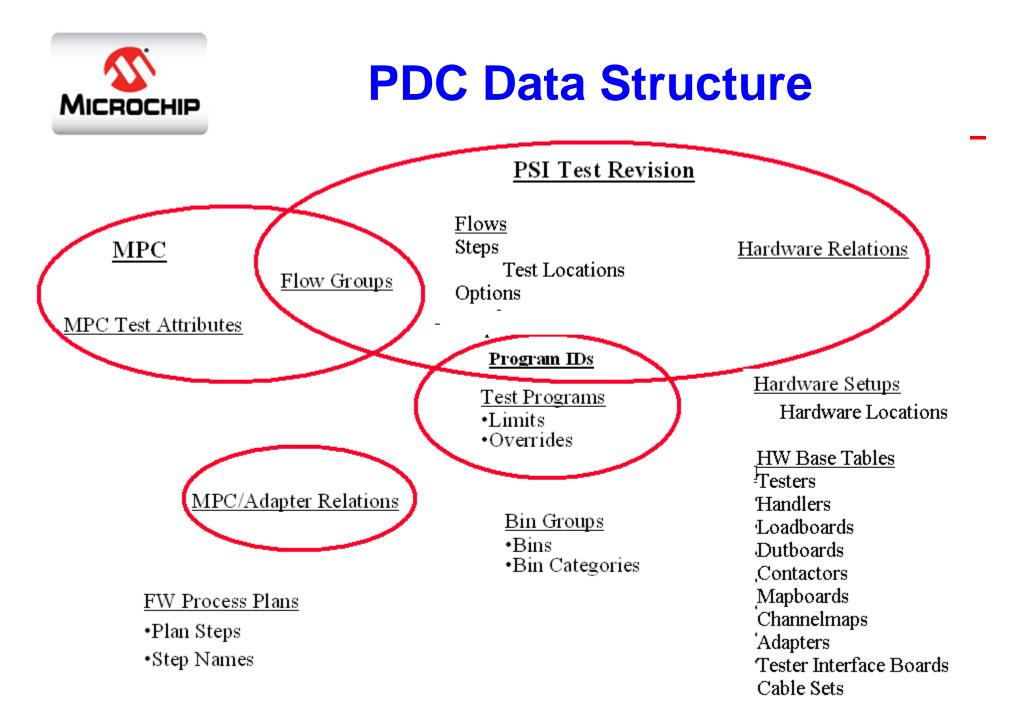
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<u>File Edit Grou</u>	ip <u>O</u> ptions Or <u>d</u> er <u>T</u> ools <u>S</u> pecial <u>H</u> elp	
E B 6	► 器 B <pb <="" p=""> /pb></pb></pb></pb></pb></pb>	2
Tester Type	ETS300 Eagle ETS300	
Program ID	Description	
124138	MCP6272_FI2_QIS	<u> </u>
124139	MCP6272_QC2_QTS	
124140	MCP6273_FT2_Analog QTS	
124141	MCP6273_FT2_Analog RASCO (05SOT23)	
124142	MCP6273_FT2_05SOT23 (ISM)	
124143	MCP6273_QC2_Analog QTS	
124144	MCP6273_QC2_Analog_RASCO (05SOT23)	
124145	MCP6273_QC2_05SOT23 (ISM)	
124146	MCP6282.FT2025C	
124147	MCP6282.QC2025C	
124148	MCP6292.FT2@25C	
124149	MCP6292.QC2@25C	·
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pctfl0115m000	Maintain Test Programs [101]			
<u>File Edit Group</u>	<u>O</u> ptions Or <u>d</u> er <u>T</u> ools <u>S</u> pecial <u>H</u> elp			
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Tester Type	D10I_UC Single Chassis (ITE)_576I0-6DP-HDVI-VIS-N	W		
Program ID	Description			
130630	58z56_S0101_FT_Tmin_X2	_		
130631	58z56_S025_FT_Tmin_X2			
130632	58z56_S051_FT_Tmin_X2			
130633	59002d_X3_BGA217_FT1			
130634	59003e_X2_BGA324_FT			
130635	91001g_ATMEL_3X_FT1_25_TFBGA217_HDVI			
130636	91001g_ATMEL_3X_FT1_25_TFBGA247_HDVI			
130637	91001g_SAGEM_3X_FT1_25_TFBGA217_HDVI			
130638	91001g_SAGEM_3X_QA1_Hbin6_25_TFBGA217_HDVI			
130639	91007d_X3_BGA217_SAM9G25_FT1			
130640	91007d_X3_BGA217_SAM9G35_FT1			
130641	91007d_X3_BGA217_SAM9X25_FT1	-		



•••• pctfl0115m000	: Maintain Test Programs [101]	
<u>File Edit Group</u>	<u>O</u> ptions Or <u>d</u> er <u>T</u> ools <u>S</u> pecial <u>H</u> elp	
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Tester Type	M1_SV_1024 MAGNUM I - SV 1024 pins	
Program ID	Description	
130832	29657_13_SOIC_AA	<u> </u>
130833	29657_SOIC_AA	
130834	56912_SOIC_AA	
130835	56919_MAG_STRIP_SOIC	
130836	58961_SOIC_AA	
130837	58961_SOIC_AA-rev05	
130838	58961P_GQFN_MAG	
130839	58962_SOIC_CZ_DA	
130840	58962_SOIC_NO_PER_508	
130841	58962_SOIC_ST_ALL_508	
130842	58963_SOIC	
130843	59Z02_Z03_WLCSP_X256	
r		





- Review all Test Flows and Test Program Options at every test step of all MPCs (MPNs) of that Mask to minimize the number of Test Flows.
- Define name of each Test Flow.
- Create Test Flow Group Name in PDC.

Note 1 : We call Flow Group since we can have more than 1 Test Flows under the same Flow Group Name.

Note 2 : The MPC can be linked to only one Flow Group.



- Under "Test Engineering", Double clicks "PSI Test Revs".
- Double clicks "Maintenance" then Double clicks "Maintain Test Flow Groups".

Test Engineering Final Test Base Data PSI Test Revs Inquiries Maintenance Maintenance Maintenance Mass status change of PSI Test Revs by Job Number Mass Delete of PSI Test Revs in EDIT state by Job number Maintain Test Flow Groups Maintain Test Program Revisions Maintain Test Program Revisions Maintain Test Program Qverrides Mass Delete Test Program Revisions by Job Num Mass Delete Test Program Revisions by Job Num Test Hardware	Menu browser [User	r: b00404]		
	Eile Find Options Test Engineering Final Test Base Final Test Base Final Test Revs Final Test	Help Data Data nce ntain PSI Test Revisi ss status change of P ss Delete of PSI Test ntain Test Flow Group ntain Test Program R ntain Test Program O ss status change of T ss Delete Test Progra	SITest Revs by Job Numbe Revs in EDIT state by Job n sevisions verrides est Prog Revs by Job Num	



- Click Find button, input Mask number then click OK.
- If it is the initail release product, click Bowtie button to add new Mask.
- Input new Mask then click Save button.

pctfl0107m000 : Maintain Test Flow Groups [101]				
<u>File Edit Group Options Order Tools Special H</u> elp				
PSIMask 001KX Product Specification Inde	x 001KX			
Flow Group	Old Flow Name	Status		
CHUCK FLOW		ACTIVE -		
E-TEMP EAMS LC STRIP M2		ACTIVE		
EMC1414.YS		ACTIVE		
FT: S0201.ADVT5581.YS		ACTIVE		
IND IAMS AA STRIP M2		ACTIVE -		
IND IAM3 LC STRIP M2		ACTIVE -		
INDUSTRIAL OTP ATO		ACTIVE -		
NEW FLOW ON TUES		ACTIVE		
NO TEST REQUIREMENT		ACTIVE -		
NOT INTEGRATED - ATMEL		ACTIVE		
RESTRICTED - A		ACTIVE -		



- Click 🔠 Insert button. Input the 1st Flow Group Name.
- Press "Tab" key for 3 times, the cursor will go to the next line.
- Input the 2nd Flow Group Name, the 3rd, the 4th,... until complete all of them.
- Click 📃 Save & Exit button.

📫 pctfl0107m000 : Maintain Test Flow Groups [101]		x
<u>File Edit G</u> roup <u>Options</u> Or <u>d</u> er <u>T</u> ools <u>Special</u> <u>H</u> elp		
	HH TR?	
PSI Mask 001KX >		
Flow Group Old Flo	Tow Name Status	
	<u></u>	
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pctfl0107m000 : Maintain Test Flow Groups [101]		
<u>File Edit Group Options Order Tools Special H</u> elp		
	< << >>	T N ?
PSIMask GBBQ1 Product Specification Index GBB	3Q1	
Flow Group	Old Flow Name	Status
IND_GBBQ1	FQQQ	ACTIVE -
NO TEST REQUIREMENTS	Y	ACTIVE
RMA_GBBQ1		ACTIVE
STRIP_IND_GBBQ1		ACTIVE
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	1	
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pctfl0107m000 : Maintain Test Flow Groups [101]		
le <u>E</u> dit <u>G</u> roup <u>O</u> ptions Or <u>d</u> er <u>T</u> ools <u>S</u> pecial <u>H</u>	<u>-</u> elp	
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PSIMask C5AC2 Product Specification		
Flow Group	Old Flow Name	Status
COMM C1 AA	[C1	ACTIVE -
СОММ САМЗ АА		ACTIVE -
COMM CAM3 LC	CAM3	ACTIVE
COMM CAM3 LC STRIP	CAM3	ACTIVE -
DLT		ACTIVE
E-TEMP E1 LC	E1	ACTIVE
E-TEMP E1 LC MSOP	E1	ACTIVE -
E-TEMP E3 LC	E3	ACTIVE -
E-TEMP E3 LC MSOP	E3	ACTIVE -
E-TEMP EAM2 LC STRIP M1		ACTIVE -
E-TEMP EAM2 LC STRIP M1 BOSE		ACTIVE -
	1017.0	
	firs	st



Edit Group Options Order Tools Special □ □ □ □ □ □ □	
PSIMask LEBC1 Product Specification	
Flow Group	Old Flow Name Status
INDUSTRIAL OTP CTE QS FLOW	ACTIVE -
INDUSTRIAL OTP HTE	ACTIVE -
INDUSTRIAL OTP HIE QS FLOW	ACTIVE -
INDUSTRIAL OTP HTR	INACTIVE -
INDUSTRIAL OTP QS	ACTIVE -
INDUSTRIAL OTP QS HTE	ACTIVE -
INDUSTRIAL QTP	INACTIVE -
NO TEST REQUIREMENT	ACTIVE -
RELIABILITY	ACTIVE -
SPECIAL OTP	ACTIVE -
STRIP EXT OTP 44TQFP QS FLOW	ACTIVE .



pctfl0107m000 : Maintain Test Flow Groups [101]				
<u>File Edit G</u> roup <u>Options</u> Or <u>d</u> er <u>T</u> ools <u>S</u> pecial	<u>H</u> elp			
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PSIMask TA203 Product Specificatio	n Index TA2	03 SMSC mask OS	81092	
Flow Group		Old Flow Name	Status	
0581092-FT1:125C-FT2:25C-FT3:-45C-QA:250	C-FT4:25C		INACTIVE -	-
OS81092-FT1:130C-FT2:25C-FT3:-45C-QA:250	C-FT4:25C		ACTIVE -	
OS81092-FT1:130C-FT2:25C-QA:25C-FT3:25C	1		ACTIVE -	
OS81092-FT1:130C-FT2:25C-QC:25C			ACTIVE	
0581092-QC1:25C-QC2:130C			ACTIVE -	
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📫 pctfl0107m000 : Maintain Test Flow Groups [101]		
<u>File Edit Group Options Order Tools Special H</u> elp		
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PSIMask 34161 Product Specification Index 341	.61	
Flow Group	Old Flow Name	Status
IND_FFQ40C_85C_85C_S0101B-CU		ACTIVE -
IND_FFQ40C_85C_85C_S051B-CU	1	ACTIVE -
NO TEST REQUIREMENT	1	ACTIVE
NOT INTEGRATED - ATMEL		ACTIVE
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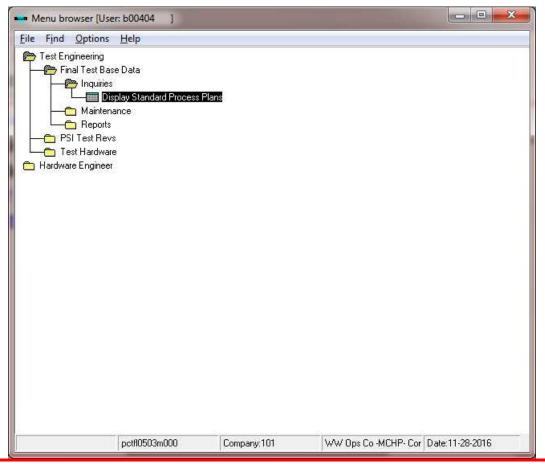
pctfl0107m000 : Maintain Test Flow Groups [101]		
<u>File Edit Group Options Order Tools Special Help</u>		
	ka ka 🕨 🙌 👘	Γ 💦
PSIMask 29657 Product Specification Index 2	9657	
Flow Group	Old Flow Name	Status
IND_I_25C_150SOIC_STRIP		ACTIVE -
IND_I_25C_150SOIC_STRIP_13		ACTIVE
NO TEST REQUIREMENT		ACTIVE
NOT INTEGRATED - ATMEL		ACTIVE



Process Plans - 1

To see all available STD/REL/RSN Process Plans :

- Under "Test Engineering", Double clicks "Final Test Base Data"
- Double clicks "Inquires" & "Display Standard Process Plans".





Process Plans - 2

- Click Find button, input Flow Type & Process Plan you require.
- If you can't find the Process Plan you needed, please contact Surasak Chairattanarom - B00717 (MThai IS).

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		Г 💦	Flow Type:	
Flow			Process Plan Name: BFFFBFFF	Ca
Туре	Process Plan Name			
STD	BFFFBFFF	<u> </u>		
STD	BFQQ			
STD	BRRBRRR			
STD	CFQQ			
STD	CFRQ			
STD	F			
STD	FBFFQF	i i		
STD	FBFFQFQ	<u>Vee</u>		
STD	FBFFQFQFR			
STD	FBFFR			
STD	FBFQFQ			
STD	FBFQFQFR			
STD	FBFQQ			
STD	FBFQQFR			
	FBJJFR	-		

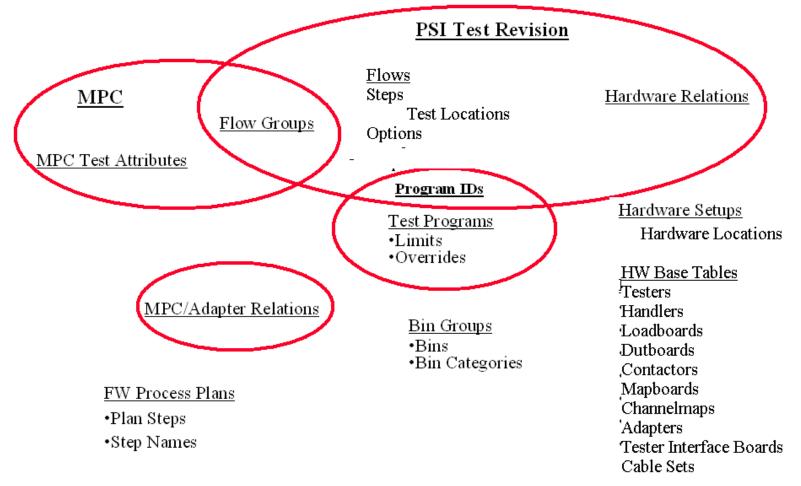


Process Plans - 3

pctfl0503m000 : Display Standard Process Pla	ans [101]	
<u>File Edit Group Options Order Tools</u>	Special Help	X
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		Process Plan
Flow	pctfl0503m000 ; Display Standard Process Plans [101	
Type Process Plan Name	Eile Edit Group Options Order Tools Special	
REL BF		
REL BFF		
REL BFFBFF	Flow	pctfl0503m000 ; Display Standard Process Plans [101]
REL BFFF	Type Process Plan Name	
REL BFFFBFF		<u>File Edit Group Options Order Tools Special Help</u>
REL BFFFBFFF	RSN CFQ	
REL BFFFF	RSN CRQ	
REL BFFFFBFFF	RSN F	Flow
REL BFFFFBFFFF	RSN FF	Type Process Plan Name
REL BFFFFFBFFF	RSN FFF	
REL BQQ	RSN FFFFQ	STD BFFFBFFF
REL BQQBQQ	RSN FFQ	STD BRRBRRR
REL BRRBRR	RSN FQ	STD CFQQ
REL BRRR	RSN FQFQ	STD CFRQ
REL BRRRBRRR	RSN FQFQFQ	STD F
	RSN FQQ	STD FBFFQF
	RSN FQQFR	STD FBFFQFQ
	RSN FQQFRQ	STD FBFFQFQFR
	RSN FQQQQQ	STD FBFFR
	RSN FQR	STD FBFQFQ
		STD FBFQFQFR
	T.	STD FBFQQ
	L	STD FBFQQFR
		STD FBJJFR
		STD FBJQ
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• PSI Test Revision is the place to create relationship among Hardware Setups, Test Flows and Program IDs.





 PSI Test Revision data will be shown at FLOW STEPS, FLOW DESC, Hardware Setups, Temperature, Programming Type on Test Setup Sheet.

SPECIAL TEST SETUP OPTIONS -- MPC: LEAD1TT4X030 FLOW#: 245849 REV: AD VER: 0

MPC:	: LEAD1TT4X030 LOT:-					ART NUMBER:	PIC18F46K20T-I/PT030
						WAFER LOT 1:	-
						WAFER LOT 2:	
						DUE DATE:	
						LEAD/CONFIG:	44/44TQFP_10 (T4X)
FLOW STEPS:	FS1@IN25C, IS1@85C, QC1@IN-	40, FT1@IN25C, RT1@IN25C					
FLOW DESC:	J750 STRIP INDUSTRIAL QTP X2	OLF					
PSI REV:	M			TRACE	CODE:		
PSI TEST REV:	LEAD1 Rev. AD Ver. 0			MASK CAL	L REV:	-	
SDP PROD ID:				CP ON CHEC	KSUM: 0339		
SDP CUST ID:				CP OFF CHEC	ECKSUM: AA8C		
SDP MPC:				9	CODE:		
TEMP CN NO .:	1701736 COMMEN	TS: Change hardware id of 44L	QFN 8x8				
TEST LOCATION .:	MTAI,						
MULTI-STEP TES	T PROGRAM .: NO						
	CP ON :						
	CP OFF :						
	QCODE :						



 PSI Test Revision data will be shown at FLOW STEPS, FLOW DESC, Hardware Setups, Temperature, Programming Type on Test Setup Sheet.

2) Hardware Setups for 44/44TQFP_10 (T4X)

SETUP ID	HANDLR. CHANNELMAP	LOADBOARD ADAPTER	CONTACTOR TESTER INTERFACE BOARD	DUTBOARD CABLE SET	MAPBOARD H/W LOCATION	
44	CAS x3dlc44tqfp_128p	14-A3100	14-A1961		MTAI.	
45	CAS x4dic44tqfp_256p	14-A3100	14-A1961		MTAI,	
63	SCH x20mct44tqfp	14-A4254	14-A2834		MTAI,	



 PSI Test Revision data will be shown at FLOW STEPS, FLOW DESC, Hardware Setups, Temperature, Programming Type on Test Setup Sheet.

D:114137 Rev: AX	Ver: 0 Statu	s: ACT	Ve	rified
Main Source:	LEAR0_FT_A4	8e.xls		
Checksum:	16540E2		C	
Executable Name:	ft-qtp-std			
Part Number:	18F46K20		C	
Temperature:	IN25C		C	
Programming Type:	QTP			
Bins:	2-PASS, 3-PA,	4-FU, 5-O/S		
Hardware Limits:				
Correl. Process Code:				
Correlation Good Bin:	0			
OS Version				
Special Instructions:				
CN Number:	1700910			
Comments:				



- Under "Test Engineering", Double clicks "PSI Test Revs".
- Double clicks "Maintenance" then Double clicks "Maintain PSI Test Revisions".

Menu browser [Use	er: b00404]			
	s ance <u>sintain PSI Test Revisi</u> ass status change of P ass Delete of PSI Test sintain Test Flow Grou, aintain Test Program R aintain Test Program O ass status change of T ass Delete Test Progra e	SITest Revs by Job Numbe Revs in EDIT state by Job r ps evisions verrides est Prog Revs by Job Num		
	pctfl0110m000	Company:101	WW Ops Co -MCHP- Cor Da	ate:12-27-2017



- For initial release, need to add new Mask in PDC.
- Click Insert button, input Mask number.
- Input Description.
- Click Save button.

📫 pctfl0110m000 : Maintain PSI Test Revisions [101]		
<u>File Edit Group Options Order Tools Special Help</u>		
	T 💦	
PSI Mask		
Revision A New Rev		
Version 0 New Ver Test F	lows	
HW Re	lations	
Status EDIT -		
CN Number		
Description		
Job Number Multi-Step Test Program		
	add	alphanum / zoom



- Click HW Relations button to add Hardware Setup.
- Click Insert button, Input Lead Count, Select Configuration (Package), Select Hardware Setup.

octfl0111s000 : Maintain HV				_				
<u>Edit</u> <u>Group</u> <u>Options</u>	Order <u>T</u> ools	<u>Special</u> <u>H</u> elp						
	<u>* M</u>		(4 44 88 881	T \?				
Relations Conta	ct/Channel	Locations						
PSI Mask LEAD1 Rev		sion 1				Copy Relat	ions	
Lead	Setup							
Count Configuration	ID ·	Tester	Handler	Lo	adboard	Dutboard	Mapboard	
44 44TQFP_10	T 🚺 🖌 🗖	pcthw0509s000 : Display Setups by L	ead Cnt and Config [101]				and the second s	
		le <u>E</u> dit <u>G</u> roup <u>Options</u> Or <u>d</u> er						
	E	□ 🖬 🛎 🗠 🛤 🛤 🖊		(44)>> >> T N ?				
		Hardware Setups Details	Locations					
		Lead Count 44 Con	figuration 44TQFP_10					
i i								<u> </u>
		Setup ID Tester	Handler	Loadboard Dutboard	Mapboard	Contactor		Cancel
		78 ADVANTEST	SUB	14-A4890		14-A1062		
		37 DATA IO		14-A01:	L 14-A2845			
		38 DATA IO			14-A2845			
		133 ETS300	CAS	14-A9016		14-A1961		
		57 FLEX	CAS	14-A3903		14-A1062		
i – i – – – – – – – – – – – – – – – – –		112 FLEX_MICRO 9 J750	CAS	14-A3903 14-A0253		14-A1961 14-A1062		
		10 J750	CAS	14-A0253		14-A1062		
		1 J750	CAS	14-A0287		14-A1062		
		2 J750	CAS	14-A0287		14-A1062		•
1 1								•



- Add the next Hardware Setup.
- Click Save & Exit button after finished adding all Hardware Setups you needed (need to have at least 1 Setup per Package Type).

pctfl011	🚥 pctfl0111s000 : Maintain HW Setup Relations [101]								
Eile Edit Group Options Order Tools Special Help									
	s <u>s z a</u> z	M	• • > >						
<u>+</u> +	Relations Contact/C	hannel	Locations						
PSI Ma	sk LEAD1 Revision	AD	Version 0			Copy Relations			
Lead		Setup							
Count	Configuration	ID	Tester	Handler	Loadboard	Dutboard Mapboard	1.000		
40	600DIP	74	J750	DAY	14-A2991		_		
40	600DIP	7.6	J750	DAY	14-A2991				
40	UQFN_SXS	2	J750	RA2	14-A4384				
44	44TQFP_10	44	J750	CAS	14-A3100				
44	44TQFP_10	45	J750	CAS	14-A3100				
44	44TQFP_10	63	J750	SCH	14-A4254				
44	44TQFP_10	122	LTX_D2X	SCH	14-A6940				
44	44TQFP_10	125	LTX_D2X	SCH	14-A6940				
44	QFN_8X8	105	J750	SCH	1 4- A4838				
44	QFN_8X8	183	J750	MUT	14-A3112				
44	QFN_8X8	184	J750	MUT	14-A3112				
48	UQFN_6X6	1	J750	RAS	14-A4110		<u>.</u>		
•							F		
-									



• Click Test Flows button to start adding Test Flow Group.

pctfl0110m0	00 : Maintain PSI Test Revisions [101]		
<u>File E</u> dit <u>G</u> r	oup <u>O</u> ptions Or <u>d</u> er <u>T</u> ools <u>S</u> pecial <u>H</u> elp		
3 8 8		T N?	
PSI Mask	LEAD1 >		
Revision	AD New Rev		
Version	1 New Ver Test Fl	lows	
	HW Rel	lations	
Status	EDIT _		
CN Number			
Description	Change hardware id of 44L QFN 8x8		
Job Number	Multi-Step Test Program 🛛 🔽		
		сору	alphanum



- Click M Bowtie button then select Flow Type.
- The default Flow Type is "STD" (Standard Production Flow).

pctfl0112s	000 : Ma	intain Test Fl	lows [101]								
<u>File Edit</u>	<u>G</u> roup	Options Or	<u>d</u> er <u>T</u> ools	<u>Special</u> <u>H</u> elp							
	n I	<u></u>	M	<u> </u>		T	?				
→ F	orm 1	Form	2								
PSI		LEAI	1010	Revision A) Version	1	Steps		Test Location		
10000000	Туре	STD	Concerns and the second second								
Flow	2020000	Config	STRIP				Copy Flow				
Number	2	Bank	Flow	Flow Group			Process Plan Name	Description		Test Location	_
		Г	Г					. k.		_	-
		F	г Г					-		-	
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	· _	, _	Г				l <u>.</u>			-	
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	· _	Г	Г					-		-0	
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	Г	Г	Г	1							
	Г		Г	[2		<u> </u>			-
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											enum
		_									



- Click Insert button, "999999" will be shown at Flow Number field of the line you are working on.
- Press Tab key to move cursor to the next field.

🛶 pctfl0112s000 : Maintain Test Flows [101]												
<u>File E</u> dit <u>G</u>												
∃ 	5	R 🖻 品	M 1	< > >	M4 44 44		N?					
+ For	rm 1	Form	2									
PSI M	ask	LEAD		Revision	AD	Version 1	Steps	Test Location				
Flow 1	Гуре	STD	-									
Flow		Config	STRIP				Copy Flow					
Number		Bank	Flow	Flow Group			Process Plan Name	Description	Test Location	1.1		
999999			Г	1			-			-		
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										•		
									add	enum		



• Flags :

Mont : "Monitor" – Tick on special flow [Ex : - QC Test on every 15 lots (STD Flow : FQ , Mont Flow : FQQQ) - Keep the previous test flow for the lot in pipeline] Note : Assembly loader can't see this flow when issues the AI (Assembly Instruction). **Config Bank : Tick when Test Flow is shared across many** configurations of the product. (For Micrel – TCG (MEMS) products) **STRIP Flow :** Tick when there is at least 1 Strip Test Step in the Test Flow



- Select Flow Group name from the list of Flow Group names you created previously.
- Input Process Plan Name (Test Flow).
- Input Flow Description.

pctfl0112s	000 : Ma	aintain Test F	lows [101]								
<u>File E</u> dit (<u>G</u> roup	Options O	r <u>d</u> er <u>T</u> ools	<u>Special</u> <u>H</u> elp							
E 🖬 🖨	5		- M	4))	< ++ +> +	T	?				
+ + Fe	orm 1	Form	n 2								
PSIN	Mask	LEA	.D1	Revision	AD Version	1	Step	s	Test Location		
Flow	Туре	STD	i -]								
Flow		Config	STRIP				Copy F	low			
Number	Mont	Bank	Flow	Flow Group			Process Plan Name		Description	Test Location	
999999	Г	Г	Г	INDUSTRIAL O	TP		FQ		Singulated Test Flow		<u>-</u>
	Г		Г							-	
	Г	Г	Г								
	Г	Г	Г			j.				1.0	
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	Г		Г								
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	Г	Г	Г						-		
	Г	<u> </u>	Г								
	Г	Г	Г			<u> </u>					
	Г	Г	Г	1			1				<u>•</u>
•											۲
										add	alphanum



- Press Tab key to add the 2nd Flow Group,
- After finish adding all Test Flow Groups, select one test flow then click Steps.

pctfl0112	s000 : Ma	aintain Test F	lows [101]								
<u>File Edit G</u> roup <u>Options</u> Or <u>d</u> er <u>T</u> ools <u>Special H</u> elp											
	3		- M	A P P PK		T N ?					
41	Form 1	Form	12								
PS	Mask	LEA	D1	Revision A) Version	1 Ste	eps	Test Location			
Flo	w Туре	STD	1								
Flow		Config	STRIP			Сору	Flow				
Number	Mont	Bank	Flow	Flow Group		Process Plan Name	Descrip	ion	Test Location		
✓ 29694		Г	Г	INDUSTRIAL OT	2	ΕQ	 Standa 	ard Production Flow		<u> </u>	
29694	B 🔽	Г	Г	INDUSTRIAL OT	2	FQQQ	 QC Mor 	nitoring Flow			
	Г	Г	Г								
		F	Г								
	Г	Г	Г								
			Г						1		
		Г	Г								
		Г	Г								
	Г	Г	Г								
			Г								
		Г	Г								
		Г	Г	1						<u> </u>	
•										B.	
									update		
-											



• Select Test Temperature for each Test Step.

pctfl0113s000 : Maintain Flow Steps [101]											
<u>File Edit Group Options Order Tools Special H</u> elp											
PSI Mask LEADI Revision AD Version 1											
Flow No. 296947 Standard Production Flow											
Step Step Recipe Cksum Prog Options											
No. Name Temp Time(hr) Rule Type Opts Special Instructions	Legacy Op Nbr										
✓ 1 FT1 → 0.00 F/A → → 0											
2 QC1 • 0.00 N/A • 0	- <u> </u>										
	_ <u> </u>										
	-										
	- -										
	- <u>- </u>										



 Recipe Time (hr) – Required for Burn-In, Endurance and Retention Bake steps. Not allowed on the other steps.
 The default value is 0.00.

- pctfl01	🚥 pctfl0113s000 : Maintain Flow Steps [101]												
<u>F</u> ile <u>E</u> di	t <u>G</u> roup	<u>Option</u>	s Or <u>d</u> er	Tools S	pec	ial <u>H</u> e	lp						
E													
	PSI Mask IEAD1 Revision AD Version 0												
Flow No. 179207 DLT MONITOR Step Step Recipe Cksum Prog Options													
No.	Name	Temp	Time(hr)	Rule		Type		Opts		Legacy Op Nbr			
1	BI1)	150C)	96.00	N/A	-		5	0					
2	FT1)	25C)	0.00	N/A	-	OTP	•	1					
3	FT2 >	25C .	0.00	N/A	-	OTP	•	1		-			
4	FT3)	125C)	0.00	N/A	-	OTP	•	1					
5	FT4 >	25C)	0.00	N/A	•	OTP	•	1					
6	FT5 >	25C)	0.00	N/A	•	OTP	Ъ.	1					
7	BI2 >	150C >	408.00	N/A	•	OTP	Þ	0					
8	FI6)	25C)	0.00	N/A	-	OTP	•	1					
9	FT7)	25C)	0.00	N/A	•	OTP	•	1					
10	FT8 >	125C)	0.00	N/A	-	OTP	•	1					
1													
<u> </u>							_			130			



• Cksum Rule (Device Checksum Rule) is the part to control which device checksum value (N/A, Blank, Device) will be shown on Test Traveler at that Test Step.

ne pctfl0113s000 : Maintain Flow Steps [101]											
<u>File Edit Group Options Order Tools Special H</u> elp	<u>File E</u> dit <u>G</u> roup <u>O</u> ptions Or <u>d</u> er <u>T</u> ools <u>S</u> pecial <u>H</u> elp										
PSI Mask LEADI Revision AD Version 1											
Flow No. 296947 Standard Production Flow											
Step Step Recipe Cksum Prog Options											
No. Name Temp Time(hr) Rule Type Opts Special Instructions	Legacy Op Nbr										
1 FT1 > 25C > 0.00 N/A + > 0											
2 QC1 > 25C > 0.00 Blank > 0											
	- <u></u>										
	- <u></u>										
m	nodify enum										



 Prog Type (Programming Type) is the part that will be shown on Test Setup Sheet at each test step.

There are 5 options : Blank (default), ROM, OTP, QTP, SQTP.

pctfl0508s000 : Display Programming Types [101]	
<u>File Edit Group Options Order Tools Speci</u>	ial <u>H</u> elp
Programming Type	
	[OK]
OTP	Cancel
QTP	
ROM	
SQTP	
	<u> </u>
	first



 We can put Special Instructions for each test step but it will be shown on Test Traveler only.

		124% 💌 📘				Тос	ols Sign
MPC: LEAD	1TT4X030 FLC	W#: 245848	REV: AC	VER: 0	LOT: -		
T3: FINAL TEST (PI-91139, PI-92001, PI-92	002)					
FIRST TEST	RETEST	TOTAL	L	TEST PROGRAM			
QTY IN:	QTY IN:	QTY IN:		TESTER ID:		PM DUE:	
TY OUT BIN1:	QTY OUT BIN1:	QTY OUT BIN1:		HANDLER ID:		PM DUE:	
QTY OUT BIN2:	QTY OUT BIN2:	QTY OUT BIN2:		TEMPERATURE:		ž.	
YIELD:	YIELD:	YIELD:		SOAK TIME:			
REJECTS:	REJECTS:	REJECTS:		LOADBOARD ID:		PM DUE:	
PA:	PA:	PA:		CONTACTOR ID:		PM DUE:	
FU:	FU:	FU:		ESD BOX ID:		PM DUE:	
OS:	OS:	OS:	2	SETUP OPR:		20	2
PAT:	PAT:	PAT:		PROMATE CSUM:	0339		
VM:	VM:	VM:		CSUM:	1: 2:		BY:
LOSS:	LOSS:	LOSS:		CSUM(IF DOWN):	1: 2:		BY:
DROP:	DROP:	DROP:		OPR1 NO .:		DATE/SHIFT:	
				OPR2 NO.:		DATE/SHIFT:	
CTRL YIELD: 0.00			2	OPR3 NO.:		DATE/SHIFT:	2
PPO:	MACHINE PERFORMANC	E: SITE=	PROBLEM=:			•	•



- Input Legacy Op Nbr if any (For Atmel Products)
- Select Test Step then click Options button.

-	pctfl01	113s000 :	Maintain	Flow Steps	[101]						<u>د</u>
Ei	le <u>E</u> di	it <u>G</u> roup	<u>O</u> ptio	ns Or <u>d</u> er	<u>T</u> ools <u>S</u>	pecial	Help				
E											
-											_
		PSI Mask			vision	AD		Vers	ion 1		
		flow No.	2		ndard P			Low			
		Step		Recipe	Cksum		og		Options		
	No.	Name	Temp	Time(hr)	Rule	Ty	/pe	Opts	Special Instructions	Legacy Op Nbr	
1	1	FII	<u> </u>	0.00	N/A	그는	^	0			
	2	QC1 >	<u> </u>	0.00	N/A	그는	^	0			
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	1	-	_				_				
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- Click Insert button. Select Tester Type, input Program ID.
- If you did not input Program ID description previously, it will be very hard to tell if you selected the right Program ID.

pctfl0114s000 : Maintain Step Options [101]	
<u>File Edit Group Options Order Tools Special H</u> elp	
PSI Mask IEADI Revision AD Version I	
Flow Number 296947 Step No. 1 Step Name FT1	
Tester Type Program ID Description	
J750 • 113738 • LEAR0-FT-PRD-CTE	-
	_
add	alphanum / zoom



- If we can test the part on >1 Tester Types, input another Tester Type and Program ID.
- Click Save & Exit button.
- Assign Test Options for the remaining steps.

🚥 pctfl0114s000 : Maintain Ste	ep Options [101]			
Eile Edit Group Options PSI Mask LEADI Flow Number 296947 Tester Type J750 LTX_D2X	Order Iools Spo	Version Step Name Description LEAR0-FT-PRD-	<u>T</u> N	
			add	alphanum / zoom



• Select Test Flow then click Test Location button.

nun po	tfl0112s0	100 : Ma	iintain Test F	lows [101]							
Eile	<u>E</u> dit <u>G</u>	roup	Options O	r <u>d</u> er <u>T</u> ools	<u>Special</u> <u>H</u> elp						
E		5		- M	< > > > >		Γ Ν?				
4 +	Fo	rm 1	Form	n 2							
	PSI M	ask	LEA	.D1	Revision	AD Version 1	Steps	1	Test Location		
	Flow 1		STD								
	Flow		Config	STRIP			Copy Flov	W			
	Number	Mont	Bank	Flow	Flow Group		Process Plan Name		Description	Test Location	
	296947	Г	Г	Г	INDUSTRIAL C	1981) -	ΕQ				<u> </u>
	296948	2	Г	Г	INDUSTRIAL C	TP	FQQQ		QC Monitoring Flow		
		Г		Г							
		Г —		Г				_	<u> </u>		
		Г		Г	-			_			
3		Г	F	Г				_	-		
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- Click insert to add Test Location button.
- Can put > 1 Test Locations.
- Add Test Location for the rest of test flows.
- Click Save & Exit button after completed.

•••• pctfl0116s000 : N	Naintain Test Flow Location	[101]	
<u>File Edit G</u> roup	<u>Options</u> Or <u>d</u> er <u>T</u> ools		
∃ 			T N?
PSI Mask	LEAD1 Revision	AD Version	1
Flow Type	STD 🚽 Flow Number	296947	
Test Location	Description		
			-
1			_
		last	



• Example : LEAD1 (MCU) Test Flow Groups.

pctfl0112s	🛥 pctfl0112s000 : Maintain Test Flows [101]								
<u>File Edit (</u>	<u>File E</u> dit <u>G</u> roup <u>O</u> ptions Or <u>d</u> er <u>T</u> ools <u>S</u> pecial <u>H</u> elp								
E B 8	5		M		?				
-1+1 E									
	PSI Mask LEAD1 Revision AD Version 0 Steps Test Location								
Flow	Туре	SID	5-1						
Flow		Config	STRIP		Copy Flow		1		
Number	Mont	Bank	Flow	Flow Group	Process Plan Name		Description	Test Location	
178939	Г	Г	Г	EXTENDED OTP	FFQQ	-,	EXTENDED OTP	MTAI,	<u> </u>
178937	Г	Г	Г	INDUSTRIAL OTP	FFQQ	-,	INDUSTRIAL OTP	MTAI,	
205091	Г	Г	Г	INDUSTRIAL QTP	FFQQFR	-	INDUSTRIAL QTP	MTAI,	
212095	Г	Г	Г	INDUSTRIAL SQTP	FFQQFR	-	INDUSTRIAL SQTP	MTAI,	
161760	Г	Г	Г	NO TEST REQUIREMENT	Y	-,	NO TEST REQUIREMENT		
178943	Г		Г	STRIP EXTENDED OTP	FFQQ	-,	EXTENDED OTP	MTAI,	
178944	Г	F	1	STRIP EXTENDED OTP	SIQ	-,	STRIP EXTENDED OTP	MTAI,	
244913	Г	Г	$\overline{\mathbf{v}}$	STRIP EXTENDED OTP	SIT	-	STRIP EXTENDED OTP [QC COL]	MTAI,	
228038	Г	Г	1	STRIP EXTENDED OTP CERLER	SIQ	-,	STRIP EXTENDED OTP CERLER	MTAI,	
228039	Г		Г	STRIP EXTENDED OTP CERLER	FFQQ	-,	EXTENDED OTP CERLER	MTAI,	
244914	Г	F	1	STRIP EXTENDED OTP CERLER	SIT	-	STRIP EXTENDED OTP CERLER	MTAI,	
179592	Г	Г	Г	STRIP INDUSTRIAL OTP	FFQQ	•	INDUSTRIAL OTP	MTAI,	•
•									•
	_					_	1	1	
						_			



• Example : TA201 (HMID) Test Flow Groups.

pctfl0112s	000 : Ma	intain Test f	lows [101]						
<u>File E</u> dit (Group	Options O	r <u>d</u> er <u>T</u> ools	<u>Special</u> <u>H</u> elp					
E 8 8	5		A		N?				
IL E	orm 1	For	n2						-
PSI t	Mask Type	TA2		Revision B Version 0	Steps		Test Location		
Flow		Config	STRIP		Copy Flow		1		
Number	Mont	Bank	Flow	Flow Group	Process Plan Name		Description	Test Location	
216723	F	Г	Г	CAP1006 - FT/QC@25C	FQ	•	CAP1006 : FT@25C> QC@25	SIGT,	<u> </u>
216724	Г	Г	Г	CAP1028 - FT/QC@50C	FQ	•	CAP1028 : FT@50C> QC@50	SIGT,	
216725	Г	Г	Г	CAP1066 - FT/QC050C	FQ	•	CAP1066 : FT050C> QC050	SIGT,	
216726	Г	Г	Г	CAP1088 - FT/QC050C	FQ	•	CAP1088 : FT050C> QC050	SIGT,	
216386	Г	Г	Г	NO INTERNAL TESI REQUIREMENT	Y	•	FOR FINAL TEST		
216387	Г		Г	NO TEST REQUIREMENT	Y	•	FOR DIE/WAFER SALE, BUY/RE:		
	Г	Г	Г				<u></u>		
	Г		Г						
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• Example : DFAW1 (Memory) Test Flow Groups.

pctfl0112s(🚥 pctfl0112s000 : Maintain Test Flows [101]						
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+ E	() Form 1 Form 2						
PSI M	PSI Mask DFAW1 Revision AS Version 0 Steps Test Location						
Flow	Туре	STD	2				
Flow		Config	STRIP		Copy Flow		
Number	Mont	Bank	Flow	Flow Group	Process Plan Name	Description	Test Location
179616	Г	Г	Г	COMM CAM3 AA	FQ	• СОММ САМЗ АА	MTAI,
166857	Г	Г	Г	COMM CAM3 AA STRIP	FQ	COMM CAM3 AA SINGULATED TE:	MTAI,
166858	Г	Г	V	COMM CAM3 AA STRIP	I	COMM CAM3 AA STRIP TEST	MTAI,
166860	₽	Г	Г	COMM CAM3 AA STRIP	FQQ	COMM CAM3 AA SINGULATED TE:	MTAI,
166874	1	Г	~	COMM CAM3 AA STRIP	IQ	COMM CAM3 AA STRIP TEST MOI	MTAI,
231761	Г	Г	₹	COMM CAM3 AA STRIP M1	I	COMM CAM3 AA STRIP M1	MTAI,
231762	Г	Г	Г	COMM CAM3 AA STRIP M1	FQ	COMM CAM3 AA SINGULATED M1	MTAI,
231763	₽	Г	$\overline{\mathbf{v}}$	COMM CAM3 AA STRIP M1	IQ	COMM CAM3 AA STRIP M1 MONI	MTAI,
231764	₹	Г	Г	COMM CAM3 AA STRIP M1	FQQ	COMM CAM3 AA SINGULATED M1	MTAI,
214637	Г	Г	Г	COMM CAM3 AA STRIP M2	FQ	COMM CAM3 AA SINGULATED M2	MTAI,
214638	₽	Г	Г	COMM CAM3 AA STRIP M2	FQQQ	COMM CAM3 AA SINGULATED M2	MTAI,
280242	Г	Г	$\overline{\mathbf{v}}$	COMM CAM3 AA STRIP M2	I	COMM CAM3 AA STRIP M2 - DE	MTAI,
•							•
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• Example : 34161 (MPU32) Test Flow Groups.

pctfl0112s0	000 : Ma	iintain Test F	lows [101]						
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888	N		A 🖌		<u>k?</u>				
	rm.1	Forn							
PSIM		341		Revision B Version 0	Steps		Test Location		
Flow		SID				-			
Flow	0000	Config	STRIP		Copy Flow		1		
Number	Mont	Bank	Flow	Flow Group	Process Plan Name		Description	Test Location	
296077	Г	Г	Г	IND_FFQ40C_85C_85C_S0101B-CU	FFQ	₽	34161FFQ-40C85C85CSINGLE	ASE9,	_
296076	Г	Г	Г	IND_FFQ40C_85C_85C_S051B+CU	FFQ	•	34161FFQ-40C85C85CSINGLE	ASE9,	
281415	Г	Г	Г	NO TEST REQUIREMENT	Y	Þ	WAFER DIE SALES		
282633	Г	Г	Г	NOT INTEGRATED - ATMEL	Y	Þ	FOR FINAL TEST	l.	
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• Example : 29657 (SCBU) Test Flow Groups.

pctfl0112s0	000 : Ma	intain Test F	lows [101]					
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- ↓ Eo	arm 1	Forn	n 2					
PSI M	1ask	296	10	Revision B Version 0	Steps	Test Location		
Flow		STD						
Flow		Config	STRIP		Copy Flow	1		
Number	Mont	Bank	Flow	Flow Group	Process Plan Name	Description	Test Location	
296444	Г	Г	~	IND_I_25C_150SOIC_STRIP	I	29657125CSTRIP	ASSH,	_
296445	Г		~	IND_I_25C_150SOIC_STRIP_13	I	29657125CSTRIP	ASSH,	
281373	Г	Г	Γ	NO TEST REQUIREMENT	Y	WAFER DIE SALES		
282591	Г	Г	Г	NOT INTEGRATED - ATMEL	Y)	FOR FINAL TEST		
	Г	Г	Г					
	Г		Г					
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•								F.



- Click "Save" button.
- Change PSI Test Revision Status from EDIT to PEND then click "Save" button.
- If there is missing/incomplete data, the system will not allow to change the status from EDIT to PEND.

🚥 pctfl0110m000 : Maintain PSI Test Revisions [101]								
<u>File Edit Group Options</u>	Or <u>d</u> er <u>T</u> ools <u>S</u> pecial <u>H</u> elp							
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PSI Mask LEAD1								
Revision AD	New Rev							
Version 1	New Ver Test Flows							
	HW Relations							
Status EDIT								
CN Number ACT								
Description INAC	dware id of 44L QFN 8x8							
Job Number EDIT PEND	Multi-Step Test Program 🔽							
PENDEA								
		modify enum						



 After the CN to release PSI Test Revision has been approved, MThai Document Control team will put CN Number and change Status from PEND to ACT.

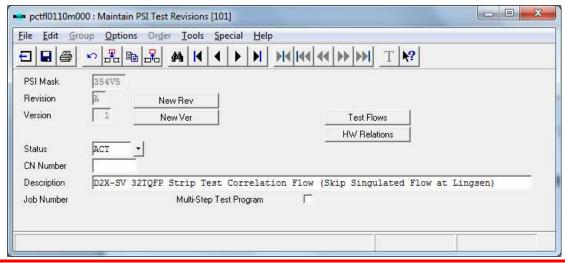
e <u>E</u> dit <u>G</u> r	000 : Maintain PSI Test Revisions [101]
PSI Mask Revision	LEAD1
revision /ersion	AD New Rev New Ver Test Flows
Citicit	HW Relations
Status	ACT •
CN Number	1701736
escription)	Change hardware id of 44L QFN 8x8
ob Number	Multi-Step Test Program 🛛 🗖

- When you need to release/revise the new Test Flow/Test Hardware to production, click "New Rev" button.
- Update Description then update Test Flows/HW Relations.



Create PSI Test Revision - 32

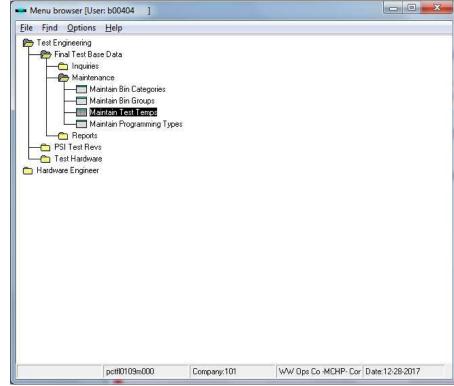
- When you need to create engineering Test Flow for correlation lot, data collection, etc. Click "New Ver" button.
- Update Description then click "Test Flows" button.
- Delete all Flow Groups that you don't use them from every Flow Type.
- Press "Shift" key and select all Flow Groups.
- Click "Delete" button.
- Start adding engineering flow.





Add New Test Temperature - 1

- In case, you need Test Temperature that does not exist in PDC, Here is the procedure to add it.
- Under "Test Engineering", Double clicks "Final Test Base Data".
- Double clicks "Maintenance" then "Maintain Test Temps".





- Click 🔠 insert button.
- Add new Test Temperature and select Temperature Type.
- Click Save & Exit button after finish adding it.

pctfl0109m0	000 : Maintain Test Temps	[101]	
<u>File E</u> dit <u>G</u> r	oup <u>O</u> ptions Or <u>d</u> er	<u>T</u> ools <u>S</u> pecial <u>H</u> elp	
E B	► 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1		
Test Temp	Тетр Туре		
+5C +88C	Cold - Hot -		-
-10C	Cold -		
-15C	Cold		
-20C	Cold •		
-25C	Cold •		
-300	Cold •		
-35C	Cold		
-40C	Cold •		
-41C	Cold ·		
-42C	Cold ·		
-43C	Cold •		
-45C	Cold 🔄		•

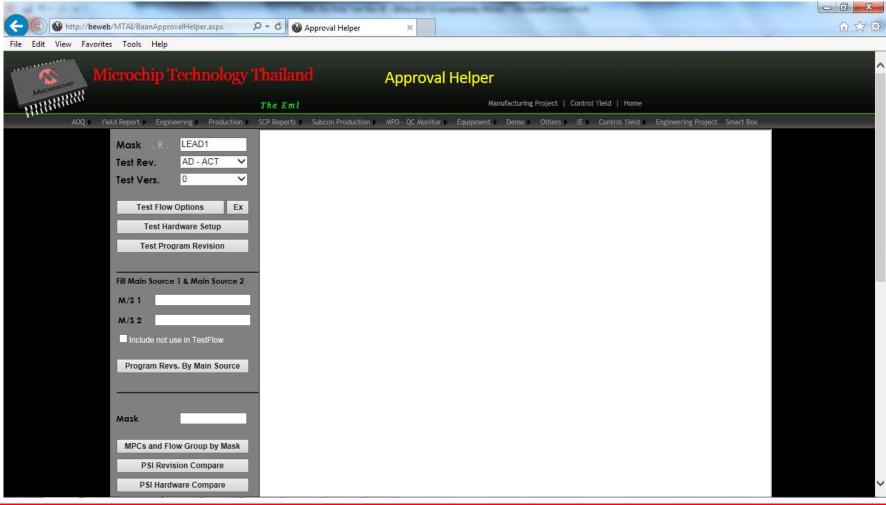


Tool for Reviewing PDC Data

- After finish creation relationship among Hardware Setups, Test Flows, Program IDs at PSI Test Revision, you can print out the reports to review.
- If find any error, you can go back to correct it.
 (Need to change Status from PEND to EDIT before make any change.)
- We also print out these reports to attach to the Test Change Notice (eCN).
- The tool is called "Approval Helper". http://beweb/MTAI/BaanApprovalHelper.aspx



• Input Mask number, select Test Rev. and Test Vers. then click Test Program Revision button.





• Select the Tester Type(s) (If there is >1 Tester Types).

Http://beweb/MTAI/BaanApprovalHelper.aspx	P - C M Approval Helper ×	- □ × A ☆ 8
File Edit View Favorites Tools Help	Theiland Approval Helper The Embo Manufacturing Project Control Yield Home SEP Reports - Subcon Production MPD - QC Monitor - Equipment - Demo - Others - IE - Control Yield - Engineering Project - Smart Box	^
Mask LEAD1 Test Rev. AD - ACT Test Vers. 0 Test Flow Options E Test Hardware Setup Test Program Revision Test Program Revision E Mask M/S 1 M/S 2 E Include not use in TestFlow Program Revs. By Main Source Mask Program Revs. By Main Source Mask PSI Revision Compare PSI Hardware Compare	Select Tester Type ✓ All Tester Type ✓ LTX_D2X ✓ J750 Continue	

MICROCHIP Print out the list of Program IDs - 3

• You can save the report in pdf, Word, Excel file to review.

e Edit View Favorites Tools	Help	_												
All and a second se	ip Tech	nolo	gy T	hailai	id	A	oproval	Helper						
WIIIRRR The	Embedded	Contr	ol Soli	utions C				Manufacturing Proj	ect Control Yield	Home				
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sk R LEAD1		-												
t Rev. AD - ACT V		((F F	1/2	_	Main Report		n 100% 🗸						
t Vers. 0 🗸	_													
	Prog	ran	n Re	vs b	y Mas	sk				LE	AD1			
Test Flow Options Ex	TESTER:	J750 Rev	Vora D	ata Mad	Time Mod	Main Courses		Executable	Checksum	PIN Crown	OS Varaian	Ctatus	Padaa	100
Test Hardware Setup	Program ID 113606	BC	Vers D	M06/2017	21:33:53	Main Source	Se VI S	FT-PRD-STD	Checksum 16540E2	BIN Group STANDARD	OS Version	Status ACT	Badge b01861	
	113607	BC		06/2017	21:34:05	LEARO FT A4		QC-PRD-STD	16540E2	STANDARD		ACT	b01861	
Test Program Revision	113720	BA		06/2017	21:35:13	LEARD FT A4		ft-res-ato	16540E2	STANDARD		ACT	b01861	
	113738	AZ		06/2017	21:35:34	LEAR0_FT_A4		FT-PRD-CTE	16540E2	STANDARD		ACT	b01861	
	113759	AZ		6/06/2017	21:36:03	LEARO FT A4		QC-PRD-CTE	16540E2	STANDARD		ACT	b01861	
	114107	BB		06/2017	21:36:24	LEARO FT A4		ft-prd-cte	16540E2	STANDARD		ACT	b01861	
Main Source 1 & Main Source 2	HW LIMITS:	Lead:	28 Config	: 300SOIC		Setup ID: 139		A4243 : LB Teradyne 18F25K20, 18	F24K20, 18F26K20 x32 2	BL SOIC 300 MCT-TAP	ESTRY			
						Handler: PH1		1896 : CT x32 28L SOIC 300 MCT		ChannelMap: x32m				
S 1	HW LIMITS:	Lead:	28 Config	: 300SOIC		Setup ID: 162		A4243 : LB Teradyne 18F25K20, 18	F24K20, 18F26K20 x32 2					
	HW LIMITS:	Lead:	28 Confir	CFN 6X6		Handler: SCH Setup ID: 91		A1896 : CT x32 28L SOIC 300 MCT A4244 : LB Teradyne 18F25K20, 18	E24K20 19E28K20 v21 2	ChannelMap: x32m				
S 2	The charto.	Leau.	20 0000	, ann_ono		Handler: SCH		A3974 : CT TERADYNE QFN 6X6 28		ChannelMap: x21m				
	HW LIMITS:	Lead:	28 Config	: SSOP		Setup ID: 77	Loadboard: 14-	A4122 : LB Teradyne PIC18F24K20	/25K20/26K20 x24 28L SS	OP 209 MCT-TAPEST	RY			
nclude not use in TestFlow						Handler: SCH		A3919 : CT 28L SSOP x 48 MCT-TA		ChannelMap: x24m	ct28ssop			
	HW LIMITS:	Lead:	44 Config	: 44TQFP_10		Setup ID: 63 Handler: SCH		A4254 : ST J750 18F45K20/18F46K A2834 : CONTACTOR 44TQFP STR		(1 MCT-TAPESTRY ChannelMap: x20m	atd dtafa			
ogram Revs. By Main Source	HW LIMITS:	Lead:	44 Confir	CFN_8X8		Setup ID: 105		A4838 : ST J750 PIC18F4XK20 x25			oraardib			
						Handler: SCH		4091 : CT X25 44L QFN 8X8 MCT		ChannelMap: x25m	ct44qfn			
	114137	AX		6/06/2017	21:37:03	LEAR0_FT_A4	8e.xls	ft-qtp-std	16540E2	STANDARD		ACT	b01861	
	114138	AX		6/06/2017	21:37:17	LEAR0_FT_A4	8e.xls	ft-sqtp-std	16540E2	STANDARD		ACT	b01861	
	114139	AX		6/06/2017	21:37:30	LEAR0_FT_A4		ft-res-sqtp	16540E2	STANDARD		ACT	b01861	
le .	114593	AU		06/2017	21:39:18	LEAR0_FT_A4		E2-QUAL-DLT-WR	16540E2	STANDARD		ACT	b01861	
:k	114594	AU		6/06/2017	21:39:35	LEAR0_FT_A4		E2-QUAL-DLT-POST	16540E2	STANDARD		ACT	b01861	
	116649	AL		06/2017	21:41:52	LEAR0_FT_A4		E2-QUAL-DLT2-POST	16540E2	STANDARD		ACT	b01861	
PCs and Flow Group by Mask	119277	z		6/06/2017	21:46:51	LEAR0_FT_A4		FT-SQTP-STD	16540E2	STANDARD		ACT	b01861	
	119278	Z		6/06/2017	21:47:06	LEAR0_FT_A4		FT-RES-SQTP	16540E2	STANDARD		ACT	b01861	
	121364	Y		6/06/2017	21:48:12	LEAR0_FT_A4		qc-prd-cte	16540E2	STANDARD		ACT	b01861	
PSI Revision Compare	HW LIMITS:	Lead:	20 00-5-	: SSOP		Setup ID: 77		A4122 : LB Teradyne PIC18F24K20						



• In case that TP supports >1 Masks.

Co (Martin://beweb/MTAI/Baan/	ApprovalHeine	er asny	D-0	Approval H	las	×	-	-	-	-		0 <mark>- x</mark> A A
File Edit View Favorites Tools H		eriesten.		M Approvaria	eipei							
	p Tech		y Thaila	nd	Ap	proval Helper Manufacturing	Project Control Yield H	lome				
A00 > Yield Report 1	Martin Million		on SCP Reports	Subcon Pre	oduction MPD -	QC Monitor Equipment Demo		With the second s	ng Project Smart	Box		
Mask R. C5035 Test Rev. AC - ACT V		<u>.</u>	₩ 1/1		Main Report V	₩ 100% ∨						
Test Vers. 0 🗸	March 1	ram	Revs by		Source	tite and the state of the						a l
Test Flow Options Ex	TESTER: Program ID		_TH /ers Date Mod	Time Mod	Main Source	Executable	Checksum	BIN Group	OS Version	Status	Badge	
Test Hardware Setup Test Program Revision	130972 HW LIMITS:	A 0	01/23/2018	0:53:24 Setup ID: 188	C5BH0_U5DPGX Handler: SCH	11 March (177) Decitive Contractor	483517451 OND D2X 16F1827 x168 20L SSO	STANDARD	3450	ACT	b01861	-
	130973 HW LIMITS:	A 0 Lead: 20	01/23/2018 Config: SSOP	0:54:01 Setup ID: 188	C5BH0_U5DPGX Handler: SCH		483517451 OND D2X 16F1827 x168 20L SSO	STANDARD		ACT	b01861	
III Main Source 1 & Main Source 2 M/S 1 C5BH0_U5DPGX_A0.una	130977 HW LIMITS:	A 0 Lead: 20	01/23/2018 Config: SSOP	0:54:40 Setup ID: 189	C5BH0_U5DPGX Handler: SCH	_A0.una f1-prd-std Loadboard: 14-A9864 : ST CREDENCE DIAM Contactor: 14-A9115 : CT x168 20L SSOP 209			ct20ssop	ACT	b01861	
A/S 2 C5BH0_U5DPGX_A0.una Include not use in TestFlow	130978 HW LIMITS:	A 0 Lead: 20	01/23/2018 Config: SSOP	0:55:47 Setup ID: 189	C5BH0_U5DPGX Handler: SCH	_A0.una f1-prd-std-lv Loadboard: 14-A9864 : ST CREDENCE DIAM Contactor: 14-A9115 : CT x188 20L SSOP 209			ct20ssop	ACT	b01861	
Program Revs. By Main Source	PSI Mask C5035 C5BH7 C5BH8	by Main	Source									-
Mask												
MPCs and Flow Group by Mask PSI Revision Compare												
PSI Hardware Compare												>



Print out the list of HW Setup IDs

- Input Mask number, select Test Rev. and Test Vers. then click Test Hardware Setup button.
- You can save the report in pdf, Word, Excel file to review.

Edit View Favorites Tools	Help									
Microch	in Tech	nology'	Thailan	a	Approva	Helper				
		av			Approva	rneiper				
HIRRR edded Contr	ol Solutio	ns Compan	u			Manufacturing P	roject Co	ntrol Yield Hom	ie -	
400 Vield Report			1. Sec. 10. 10. 10.	Subcon Produc	tion MPD - OC Monitor	Fautoment Demo	Others 1	IF Control Vield	d Engineering Project Smart Box	
	Engineeringer	Production	Der Kepola i	3000011110000		equipment benut		consorries	a engineering rojeee. amare box	
k R LEAD1	天									
Rev. AD - ACT 🗸	1 4	1 I F F	1/2		lain Report 🗸	100% 🗸				
Vers. 0 V		100000000								
	lest	lardwa	re Opti	ons						
Test Flow Options Ex	Mask:	LEAD1		CN#:	1701736	Date Mod:	10/16/2	2017		
Test Hardware Setup	Revision	: AD		Status:	ACT	Time Mod:	18:14:5	50		
	2010/02/02/02			Status.	ACT		0.7100.00	5073		
Test Program Revision	Version					User Mod:	b01861			
	Lead/Con	0								
ain Source 1 & Main Source 2	- Setup# 74	Tester J750	Handler DAY		14-A2991 : LB Teradyne J750 14-A1062 : STANDARD CONT	PIC18F48K20 x4 PDIP 600 DAY328 ACTOR	7	Dutboard : Channelmap :	x3day40dip	
				Cable Set :			_	Tester Interface :		
1	Setup# 76	J750	Handler DAY	Contactor :	14-A2991 : LB Teradyne J/50 14-A1062 : STANDARD CONT	PIC18F46K20 x4 PDIP 600 DAY328 ACTOR	(Dutboard : Channelmap :	x4day40dip_256p	
2				Cable Set :				Tester Interface :		
clude not use in TestFlow	Lead/Con	fia: 40 UQI	FN 5X5							
	Setup#	Tester	Handler	Loadboard :	14-A4384 : LB J750 PIC18FL1	8K20(LEAD1) x4 UQFN 5X5 40L RA	SCO SO2000	Dutboard :		
ogram Revs. By Main Source	2	J750	RA2	Contactor : Cable Set :	14-A4385 : CT 40L UQFP 5X5	RASCO		Channelmap : Tester Interface :	x4ras40uqfn_258p	
				Cable Set .				rester mendee .		
	- Lead/Con	fig: 44 44T	QFP_10							
	Setup#	Tester J750	Handler CAS	Loadboard : Contactor :	14-A3100 : ATQ10X10-44DC- 14-A1961 : CT 44LTQFP 10X1			Dutboard : Channelmap :	x3dlc44tqfp_128p	
k				Cable Set :				Tester Interface :		
Co. and Elaw Crown by Mask	Setup# 45	Tester J750	Handler CAS	Loadboard : Contactor :	14-A3100 : ATQ10X10-44DC- 14-A1961 : CT 44LTQFP 10X1			Dutboard : Channelmap :	x4dlc44tqfp_256p	
Cs and Flow Group by Mask				Cable Set :				Tester Interface :		
PSI Revision Compare	Setup# 63	Tester J750	Handler SCH	Loadboard : Contactor :	14-A4254 : ST J750 18F45K20 14-A2834 : CONTACTOR 44T	I/18F46K20 x20 44L TQFP 10X10X1 QFP STRIPTEST	MCT-TAPEST	Dutboard : Channelmap :	x20mct44tqfp	
		0100	0011	Cable Set :				Tester Interface :		



Print out Test Flow Options

- Input Mask number, select Test Rev. and Test Vers. then click Test Flow Options button.
- You can save the report in pdf, Word, Excel file to review.

Edit View Favorites Tools Help		
Microchip Technology	Thailand Approval Helper Manufacturing Project Control Yield Home	
AOQ > Yield Report > Engineering > Production >	SCP Reports Subcon Production MPD - QC Monitor Equipment Demo Others E Control Vield Engineering Project Smart Box	
Mask R LEAD1 Test Rev. AD - ACT V Test Vers. 0 V	Imain Report Imain Report	
	Test Flow Options CN#: 1701736 Date Mod: 10/16/2017 Mask: LEAD1 Revision: AD Version: 0 Status: ACT Time Mod: 18:14:59	
Test Hardware Setup	Change Comment: Change hardware id of 44L QFN 8x8 User Mod: b01861	
Test Program Revision	Flow Group: D2X-J750 STRIP EXTENDED OTP CERLER STD STD Flow # 260366 D2X STRIP EXTENDED OTP CERLER X56LF (SIQ) STD STD	
Fill Main Source 1 & Main Source 2 M/S 1	Strip Flow = 'Yes' Config Bank = 'No' Monitor = 'No' Test Location: MTAI, FS1@25C Checksum Rule: NA Programming Type: OTP Recipe Time: 0 Instr: Tester Prom ID Description NOTE: Please see "Program Revise by Mask" report for program details. Legacy Op Nbr LTX_D2X 126239 LEARD.STIR! D2X:FT-RPO-CERLER_RCOM-CONTACTOR Legacy Top Name	
M/S 2	IS1@125C Checksum Rule: N/A Programming Type: OTP Recipe Time: 0 Instr: Tester Program ID Description NOTE: Please see "Program Revs by Mask" report for program details. Legacy Op Nbr LTX_D2X 128240 LEAR0.STRIP.D2X.FT-PRD-CTE-APG_HOT-CONTACTOR LEAR0.STRIP.D2X.FT-PRD-CTE-APG_HOT-CONTACTOR Legacy Op Nbr	
Program Revs. By Main Source	QC1@-40C Checksum Rule: N/A Programming Type: OTP Recipe Time: 0 Instr: Tester Pram ID Description NOTE: Please see "Program Revs by Mask" report for program details. Legacy Op Nbr J750 122532 LEAR0. QC-PRD-CTE-APG Legacy Op Nor Instr:	
Mask	Flow # 260368 J750 STRIP EXTENDED OTP CERLER X20LF (SIQ) STD Strip Flow = 'Yes' Config Bank = 'No' Monitor = 'No' Test Location: MTAI,	
MPCs and Flow Group by Mask	FS1@25C Checksum Rule: N/A Programming Type: OTP Recipe Time: 0 Instr: Tester Prgm ID Description NOTE: Please see "Program Revs by Mask" report for program details. Legacy Op Nbr J750 122223 LEAR0_STRIP. FT-PRD-CERLER Legacy Op Nbr	
PSI Revision Compare	IS1@125C Checksum Rule: N/A Programming Type: OTP Recipe Time: 0 Instr: Tester Prom ID Description NOTE: Please see "Program Revs by Mask" report for program details. Legacy Op Nbr J750 122500 STRIP LEAR0. FT-PRD-CTE-APG	
PSI Hardware Compare	OC1@40C Checksum Rule: N/A Programming Type: OTP Recipe Time: 0 Instr:	



Assign Test Flow Group to the MPC -- 1

- After Test Flow Groups have been activated in PDC, need to request Engineering Planner to issue CN to assign Test Flow Group to each MPC.
- Each MPC can be linked to only one Test Flow Group.

MPC	Catalog Part Number	Flow_Group	Tester Type	Tester Device	Pattern	Restr Lvl	Rev	Stage	Qcode	Device Checksum	Blank Checksum	CP ON	CP OFF
LEAD14T4XC03	PIC18F46K20-E/PTC03	D2X-J750 STRIP EXTENDED OTP CERLER	LTX_D2X	18F46K20	OTP	CUSTOM	F	REL		0362	0362		
LEAD14T4XC03	PIC18F46K20-E/PTC03	D2X-J750 STRIP EXTENDED OTP CERLER	J750	18F46K20	OTP	CUSTOM	F	REL		0362	0362		
LEAD17T4XB04	PIC18F46K20-I/PT	D2X-J750 STRIP INDUSTRIAL OTP	J750	18F46K20	OTP	STANDARD	E	REL		0362	0362		
LEAD17T4XB04	PIC18F46K20-I/PT	D2X-J750 STRIP INDUSTRIAL OTP	LTX_D2X	18F46K20	OTP	STANDARD	E	REL		0362	0362		
LEAD17T4X022	PIC18F46K20-I/PT022	D2X-J750 STRIP INDUSTRIAL QTP	J750	18F46K20	QTP	CUSTOM	D	REL		0371	362	0371	F8F4
LEAD17T4X022	PIC18F46K20-I/PT022	D2X-J750 STRIP INDUSTRIAL QTP	LTX_D2X	18F46K20	QTP	CUSTOM	D	REL		0371	362	0371	F8F4
LEAD14S2XB04	PIC18F46K20-E/P	EXTENDED OTP	J750	18F46K20	OTP	STANDARD	С	REL		0362	0362		
LEAD17S2XB04	PIC18F46K20-I/P	INDUSTRIAL OTP	J750	18F46K20	OTP	STANDARD	С	REL		0362	0362		
LEAD14T4XB04	PIC18F46K20-E/PT	STRIP EXTENDED OTP	J750	18F46K20	OTP	STANDARD	D	REL		0362	0362		
LEAD17T3XV01	PIC18F46K20-VMLV01	UTAC 44QFN STRIP INDUSTRIAL OTP	J750	18F46K20	OTP	QS9000	D	REL		0362	0362		



Assign Test Flow Group to the MPC -- 2

МРС	Catalog Part Number	Flow_Group	Tester Type	Tester Device	Pattern	Restr Lvl	Rev	Stage	Qcode	Device Checksum	Blank Checksum	CP ON	CP OF
XG4017RKXABC	LAN8720AI-CP	LAN7820-OCTAL-FT/QC 100C	LTX_D10	LAN8720	N/A	CUSTOM	N	REL		N/A	N/A		
XG401SRKXA0C	LAN8720A-CP-TR	LAN7820-OCTAL-FT/QC 100C	LTX_D10	LAN8720	N/A	CUSTOM	L	PHAS		N/A	N/A		
													<u> </u>
XG4011RNXAB0	LAN8710A-EZK	LAN8710-FT/QC 100C	LTX_D10	LAN8710	N/A	CUSTOM	S	REL		N/A	N/A		<u> </u>
XG401SS8XA0C	LAN8710A-EZK-TR	LAN8710-FT/QC 100C	EX_DIGITAL	8710BS4QE3CCSTTR	N/A	CUSTOM	F	REL		N/A	N/A		<u> </u>
V0 (0 (7)0) (4 D0			170,000	1.4.110700.4		01107014		051					
XG4017J3XABC	LAN8720AI-CP-ABC	STRIP LAN8720-FT/QC 85C	LTX_D2X	LAN8720A	N/A	CUSTOM	L	REL REL		N/A	N/A		
XG4017J3XABC	LAN8720AI-CP-ABC	STRIP LAN8720-FT/QC 85C	LTX_D10	LAN8720A	N/A	CUSTOM	L	REL		N/A Device	N/A Blank		
MPC	Catalog Part Number	Flow_Group	Tester Type	Tester Device	Pattern	Restr Lvi	Rev	Stage	Qcode	Checksum	Checksum	CP ON	CP OF
DFAX71A3XC00	24VL024H/MS	COMM CAM3 VL	NEXTEST_PT	24VL024H	N/A	STANDARD	Α	REL		N/A	N/A		
DFAX7SC5XC00	24VL024HT/ST	COMM CAM3 VL AO	NEXTEST_PT	24VL024H	N/A	STANDARD	С	REL		N/A	N/A		
DFAX74A3XA00	24LC024H-E/MS	E-TEMP E1 LC	NEXTEST_PT	24LC024H	N/A	STANDARD	Α	REL		N/A	N/A		
DFAX74C5XA00	24LC024H-E/ST	E-TEMP E3 LC	NEXTEST_PT	24LC024H	N/A	STANDARD	Α	REL		N/A	N/A		
DFAX74C2XF00	24AA025E48-E/SN	E-TEMP EAM3 AA EUI48	NEXTEST_PT	24AA025E48	N/A	STANDARD	В	REL		N/A	N/A		
DFAX7YC8XF00	24AA025E48T-E/OT	E-TEMP EAM3 AA EUI48 AO	NEXTEST_PT	24AA025E48_SOT23	N/A	STANDARD	В	REL		N/A	N/A		
DFAX74C2XG00	24AA025E64-E/SN	E-TEMP EAM3 AA EUI64	NEXTEST_PT	24AA025E64	N/A	STANDARD	В	REL		N/A	N/A		
DFAX7YC8XG00	24AA025E64T-E/OT	E-TEMP EAM3 AA EUI64 AO	NEXTEST_PT	24AA025E64_SOT23	N/A	STANDARD	В	REL		N/A	N/A		
DFAX7Y5QXA00	24LC024HT-E/MNY	E-TEMP EAM3 LC STRIP M1 AO	NEXTEST_PT	24LC024H	N/A	STANDARD	E	REL		N/A	N/A		
DFAX77A3XB00	24AA024H-VMS	IND IAM3 AA	NEXTEST_PT	24AA024H	N/A	STANDARD	A	REL		N/A	N/A		
DFAX77C2XD00	24AA025E48-I/SN	IND IAM3 AA EUI48	NEXTEST_PT	24AA025E48	OTP	STANDARD	В	REL		N/A	N/A		
DFAX7TC8XD00	24AA025E48T-VOT	IND IAM3 AA EUI48 AO	NEXTEST_PT	24AA025E48_SOT23	OTP	STANDARD	С	REL		N/A	N/A		
DFAX77C2XG00	24AA025E64-I/SN	IND IAM3 AA EUI64	NEXTEST_PT	24AA025E64	OTP	STANDARD	Α	REL		N/A	N/A		
DFAX7TC8XG00	24AA025E64T-VOT	IND IAM3 AA EU164 AO	NEXTEST_PT	24AA025E64_SOT23	OTP	STANDARD	Α	REL		N/A	N/A		
													<u> </u>
DFAX77C2XB00	24AA024H-I/SN	IND IAM3 AA STRIP M1	NEXTEST_PT	24AA024H	N/A	STANDARD	E	REL		N/A	N/A		<u> </u>
DFAX77C2XB00	24AA024H-I/SN	IND IAM3 AA STRIP M1	NEXTEST_SSV	24AA024H	N/A	STANDARD	E	REL		N/A	N/A		<u> </u>
DFAX77C5XB00	24AA024H-VST	IND IAM3 AA STRIP M1 AO	NEXTEST_SSV2	24AA024H	N/A	STANDARD	E	REL		N/A	N/A		<u> </u>
DFAX77C5XB00	24AA024H-VST	IND IAM3 AA STRIP M1 AO	NEXTEST_PT	24AA024H	N/A	STANDARD	E	REL		N/A	N/A		<u> </u>
DFAX77C2XH00	24AA025UID-I/SN	IND IAM3 AA UID	NEXTEST_PT	24AA025UID	OTP	STANDARD	А	REL		N/A	N/A		<u> </u>
DFAX7TC8XH00	24AA025UIDT-I/OT	IND IAM3 AA UID AO	NEXTEST_PT	24AA025UID_SOT23	OTP	STANDARD	A	REL		N/A	N/A		<u> </u>
DFAX77A3XA00	24LC024H-VMS	IND IAM3 LC	NEXTEST_PT	24LC024H	N/A	STANDARD	Α	REL		N/A	N/A		
DFAX77C2XA00	24LC024H-VSN	IND IAM3 LC STRIP M1	NEXTEST_PT	24LC024H	N/A	STANDARD	с	REL		N/A	N/A		──
DFAX77C2XA00	24LC024H-VSN	IND IAM3 LC STRIP M1	NEXTEST SSV	24LC024H	N/A	STANDARD	c	REL		N/A	N/A		<u> </u>
DFAX77C5XA00	24LC024H-VST	IND IAM3 LC STRIP M1 AO	NEXTEST SSV2	24LC024H	N/A	STANDARD	E	REL		N/A N/A	N/A		<u> </u>
DFAX77C5XA00	24LC024H-VST	IND IAM3 LC STRIP M1 AO	NEXTEST_PT	24LC024H	N/A	STANDARD	E	REL		N/A N/A	N/A		<u> </u>
DI ANTI CONAU	24002411-031	IND IAMS LC STRIP INT AU	NEATEST_PT	2400240	00/4	JIANDARD	-	NLL		DVA	004		-
DFAX7TC5XC00	24VL024HT-VST		NEXTEST_PT	24VL024H	N/A	STANDARD	02	NREL		N/A	N/A		157



MPC Test Attributes - 1

• Use Tester Device as one of Test Program Options in order to reduce the number of Test Flow Groups.

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	Pattern CheckSum OFF CheckSum ON CheckSum ROM	030 AA8C 0339	SQTP Source Start Address Byte Count Promote Method	None 0 None			
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	Code Source	E:Mail	Serial Hex File				
	Promote Version	PM3V8.91	Status Register				
	MPC Test Attributes						
		Tester Type J750 LTX_D2X	18	t Device F46K20 F46K20			
	Essential Element Co	onstraints					
	NSCAR5						
	Wafer Map Commun Acquired Part Inforn						
	Assembly Instruction						
	Bill Of Material (BOM						
	Starting Material # Component Item LEAD1101XXXX	BOM level	CPN	Use Priority 10	Qty 1	Effective Date 02/21/2015	
		snapshot of the CPN information is s ue to contain references to CPN data	nown below. 9 which may no longer be pertinent to	the MPC revision whe	en looking at INACT	IVE MPC revs.	
	Catalog Part Number	PIC18F46K20T-I/PT030		10			
	CPN Stage	REL	Release to Buy- Microchip	lo			
	Web Page Part #	PIC18F46K20		lo			
	End Customer	TRIDONIC GmbH & Co. KG	Stop Orders	lo			



MPC Test Attributes - 2

): 114137 Rev: AX Main Source:	LEAR0 FT A48	: ACT e.xls	[
Checksum:	16540E2		
Executable Name:	ft-qtp-std		C
Part Number:	18F46K20		C
Temperature:	IN25C		C
Programming Type:	QTP		ſ
Bins:	2-PASS, 3-PA, 4	I-FU, 5-O/S	
Hardware Limits:			[
Correl. Process Code:]
Correlation Good Bin:	0		[
OS Version			C
Special Instructions:			[
CN Number:	1700910		
Comments:			



- Go to Approval Helper web.
- Input Mask number then click "MPCs and Flow Group by Mask".

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Microchip Technology HIIIIIIIIII	mpany	Approval H	Manufacturing Project	Control Yield Home		^
AQQ Yield Report) Engineering) Production) Mask R Test Rev Test Vers Test Flow Options Ex Test Hardware Setup Test Program Revision Fill Main Source 1 & Main Source 2 M/S 1 M/S 2 Include not use in TestFlow Program Revs. By Main Source Mask MPCs and Flow Group by Mask PSI Revision Compare PSI Hardware Compare		MPD - QC Monitor	Equipment () Demo () Others (IE Control Yield Englishing Control Yield Englishing Control Yield Englishing Control Yield Control Yield Englishing Control Yield Control	ineering Project Smart Box	



Print out the list of MPCs - 2

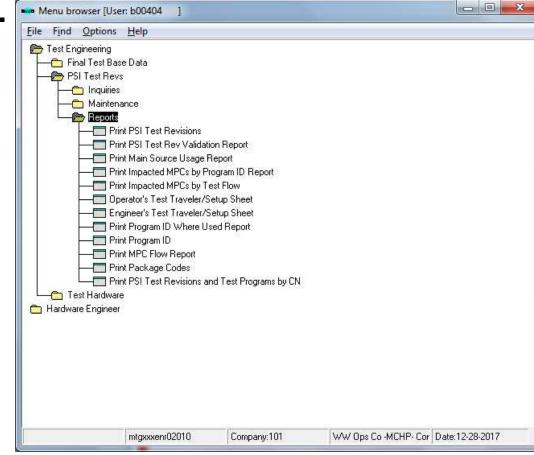
• You can export the report to Excel, Word, pdf file.

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Vers. 🗸 🗸		and Flow G	roups									
Test Flow Options Ex	MPC	Catalog Part Number	Flow_Group	Tester Type	Tester Device	Pattern	Restr Lvl	Rev Stage Qcode	Device Checksum	Blank Checksum	CP ON	C
Test Hardware Setup	LEAD14S2XB04	PIC18F46K20-E/P	EXTENDED OTP	J750	18F46K20	OTP	STANDARD	C REL	0362	0362		
rest hardware setup	LEAD14S2XREL	PIC18F46K20-E/PREL	RELIABILITY	SSD-AD-44	Second Cost Acro	OTP	STANDARD	02 NREL	0362	0362		
Test Program Revision	LEAD14S5XB04	PIC18F46K20-E/MV	EXTENDED OTP	J750	18F46K20	OTP	STANDARD	B REL	0362	0362		
Test Program Revision	LEAD14T3XB04	PIC18F48K20-E/ML	UTAC 44QFN STRIP EXTENDED OTP	J750	18F46K20	OTP	STANDARD	D REL	0362	0362		
	LEAD14T4XB04	PIC18F46K20-E/PT	STRIP EXTENDED OTP	J750	18F46K20	OTP	STANDARD	D REL	0362	0362		
	LEAD14T4XC03	PIC18F46K20-E/PTC03	D2X-J750 STRIP EXTENDED OTP CERLER	LTX_D2X	18F46K20	OTP	CUSTOM	F REL	0362	0362		
ain Source 1 & Main Source 2	LEAD14T4XC03	PIC18F46K20-E/PTC03	D2X-J750 STRIP EXTENDED OTP CERLER	J750	18F46K20	OTP	CUSTOM	F REL	0362	0362		
	LEAD17R7XB04					OTP	STANDARD	02 NREL	0362	0362		_
;1	LEAD17S2XB04	PIC18F46K20-I/P	INDUSTRIAL OTP	J750	18F46K20	OTP	STANDARD	C REL	0362	0362		_
	LEAD17S5XB04	PIC18F46K20-I/MV	INDUSTRIAL OTP	J750	18F46K20	OTP	STANDARD	B REL	0362	0362		
2	LEAD17T3XB04	PIC18F46K20-I/ML	UTAC 44QFN STRIP INDUSTRIAL OTP	J750	18F46K20	OTP	STANDARD	D REL	0362	0362		
	LEAD17T3XV01	PIC18F46K20-I/MLV01	UTAC 44QFN STRIP INDUSTRIAL OTP	J750	18F46K20	OTP	QS9000	D REL	0362	0362		
nclude not use in TestFlow	LEAD17T4X022	PIC18F46K20-I/PT022	D2X-J750 STRIP INDUSTRIAL QTP	J750	18F46K20	QTP	CUSTOM	D REL	0371	362	0371	F
	LEAD17T4X022	PIC18F46K20-I/PT022	D2X-J750 STRIP INDUSTRIAL QTP	LTX_D2X	18F46K20	QTP	CUSTOM	D REL	0371	362	0371	F
	LEAD17T4XB04	PIC18F46K20-I/PT	D2X-J750 STRIP INDUSTRIAL OTP	LTX_D2X	18F46K20	OTP	STANDARD	E REL	0362	0362		
ogram Revs. By Main Source	LEAD17T4XB04	PIC18F46K20-I/PT	D2X-J750 STRIP INDUSTRIAL OTP	J750	18F46K20	OTP	STANDARD	E REL	0362	0362		
	LEAD17T4XC05	PIC18F46K20-I/PTC05	D2X-J750 STRIP INDUSTRIAL OTP	J750	18F46K20	OTP	CUSTOM	F REL	0362	0362		
	LEAD17T4XC05	PIC18F46K20-I/PTC05	D2X-J750 STRIP INDUSTRIAL OTP	LTX_D2X	18F46K20	OTP	CUSTOM	F REL	0362	0362		
	LEAD17T4XC06	PIC18F46K20-I/PTC06	D2X-J750 STRIP INDUSTRIAL OTP	J750	18F46K20	OTP	CUSTOM	C REL	0362	0362		
	LEAD17T4XC06	PIC18F46K20-I/PTC06	D2X-J750 STRIP INDUSTRIAL OTP	LTX_D2X	18F46K20	OTP	CUSTOM	C REL	0362	0362		
k LEAD1	LEAD17T4XC07 LEAD17T4XC07	PIC18F46K20-I/PTC07	D2X-J750 STRIP INDUSTRIAL OTP	LTX_D2X	18F46K20	OTP OTP	CUSTOM	C REL	0362	0362		
	LEAD1714XC07	PIC18F48K20-I/PTC07 PIC18F48K20T-I/MV	D2X-J750 STRIP INDUSTRIAL OTP INDUSTRIAL OTP	J750 J750	18F46K20 18F46K20	OTP	CUSTOM STANDARD	C REL B REL	0362	0362		
				J750 J750		OTP	STANDARD	D REL	0362	0362		
PCs and Flow Group by Mask	LEAD1TT3XB04		UTAC 44QFN STRIP INDUSTRIAL OTP UTAC 44QFN STRIP INDUSTRIAL OTP	J750 J750	18F46K20 18F46K20							
	LEAD1TT3XNSB		UTAC 44QEN STRIP INDUSTRIAL OTP UTAC 44QEN STRIP INDUSTRIAL OTP	J750 J750	18F46K20 18F46K20	OTP OTP	CUSTOM QS9000	A REL D REL	0362	0362		
	EAD1TT2Y1/04											
PSI Revision Compare	LEAD1TT3XV01 LEAD1TT3XVB1	PIC18F48K20T-I/MLV01 PIC18F48K20T-I/MLVAO	UTAC 44QEN STRIP INDUSTRIAL OTP	J750	18F46K20	OTP	QS9000	F REL	0362	0362		



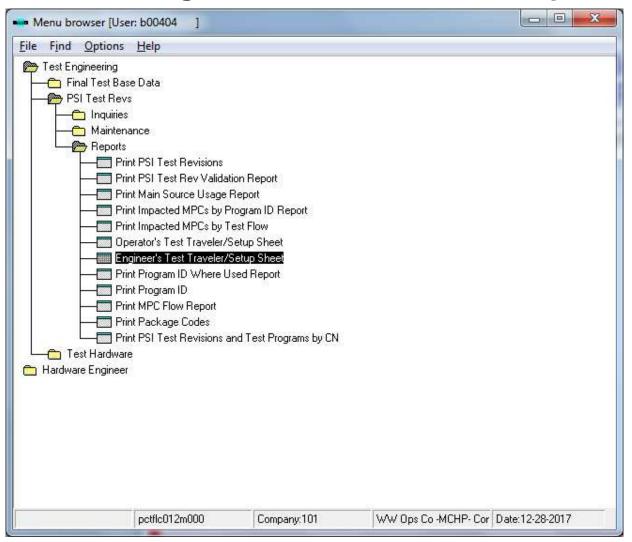
- Able to print out Test Traveler/Setup Sheet after Test Flow Group has been assigned to the MPC.
- Under Test Engineering, Double clicks PSI Test Revs, Double







• Double clicks at Engineer's Test Traveler/Setup Sheet.





- Input MPC then press Tab key.
- Select Test Flow.
- Put "-" at Mask Call Rev, Assy Lot no and Wafer Lot 1.
- Click Setup Sheet button.

	Engineer's Test Trave			
Eile Edit Group	The transfer of the transfer o	I shall be the	<u>telp</u> 1)14 144 44	>> >> T > ?
MPC LE	EAD1TT4X030	PSI Rev Lead/Config	M 44/44TQFP_	10
Flow Group	D2X-J750 ST	RIP INDUSTRIAL	QTP	•
Flow Number	245849	PSI Test Rev	leadi Rev ad	Ver 0
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Assy Lot Wafer Lot 1	-	_	Setu	p Sheet
Wafer Lot 2 Trace Code			Tra	aveler
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• Select printer by using magnifier at Device.

🚥 ttstpsplopen : Select D	evice [000]			
<u>File Edit Group Opti</u>	ions Or <u>d</u> er <u>T</u> ools S	pecial <u>H</u> elp		
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Paper Type	1FN3031-L	Direct	Finance Admin Landscape	▲ Cancel
Font	1FN3031-P	Direct	Finance Admin Bldg Portrait	Find
Paper Width	1FNOFF22	Direct	Ricoh in MTAI Finance off 22(A4 Portrait)	Help
Margin	1FNOFF22_L	Direct	Ricoh in MTAI Finance off 22 (A4 Landscap	
1	1PUR3032-L	Direct	Purchase Admin Landscape	
Report pctflc0071100s	1PUR3032-P 20FFFAC39	Direct Direct	Purchase Admin Bldg Portrait 2-OFF-FAC-39 MMT Printer (Portrait)	*1
			,	<u> </u>



Click Find. At Device, input "FILE" then click OK.
Select FILEONLY-SS Device and click OK.

•••• tt	aad3500s000 : Display	/ Device Data [000]				9
	Device	Device Type	Description		<u> </u>	Display Device Data - Find
	1FN3031-L	Direct	Finance Admin Landscape	<u> </u>	Cancel	
	1FN3031-P	Direct	Finance Admin Bldg Portrait		Find	Device: FILE OK
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	1FNOFF22_L	Direct	Ricoh in MTAI Finance off 22 (A4 Landscap			
	1PUR3032-L	Direct	Purchase Admin Landscape			
	1PUR3032-P	Direct	Purchase Admin Bldg Portrait			
	20FFFAC39	Direct	2-OFF-FAC-39 MMI Printer (Portrait)	•		

53 ttaad3500s000 : Display Device Data [000] Device Device Type Description ÖK . Cancel FILEONLY-SS Rewrite file StreamServer printer to produce files onl FILEONLYD-SS Rewrite file StreamServer printer to produce files onl Find... FINGOOD Rewrite file Print to Finished Goods Share Help FINLSR1 Direct C3 Second Floor Finance C3 Second Floor Finance - Landscape FINLSR1-L Direct FINLSR1-SS Rewrite file Finance Laser #1 StreamServe FINLSR2 Direct Finance Laser #2 -



• Click Continue button.

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🚺 Downloads		TestSetup_LEAD1TT4X030_245849_20171228_021029	12/28/2017 4:10 PM	Adobe Acrobat D	28	КВ	
💹 Recent Places		TestSetup_LEAD1TT4X030_245849_20171228_021005	12/28/2017 4:10 PM	Adobe Acrobat D	28	КΒ	
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Documents		TestSetup_TCB01TRSXVC3_243072_20171227_230539	12/28/2017 1:05 PM	Adobe Acrobat D	28	КВ	
J Music		TestSetup_TCB01TRSXVC2_240491_20171227_230517	12/28/2017 1:05 PM	Adobe Acrobat D	28	KB	
E Pictures		TestSetup_TCB01TRSXVC1_240489_20171227_230500	12/28/2017 1:05 PM	Adobe Acrobat D	29	КВ	
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		12 TestSetup_TCB017RSXVC8_240584_20171227_230401	12/28/2017 1:04 PM	Adobe Acrobat D	33	KB	
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🏭 SYS (C:)		TestSetup_TCB017RSXVC5_240584_20171227_230321	12/28/2017 1:03 PM	Adobe Acrobat D	33	KB	
👝 DATA (D:)		TestSetup_TCB017RSXVC3_243072_20171227_230301	12/28/2017 1:03 PM	Adobe Acrobat D	28	KВ	
🗣 GROUPS (\\B1-1-S1) (G:)		TestSetup_TCB017RSXVC2_240491_20171227_230242	12/28/2017 1:02 PM	Adobe Acrobat D	28	КΒ	
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- Note : For subcontractor testing, you can send him Test
 Setup Sheet as a reference test specification.
- If you need Test Traveler, click Traveler button.







• **PSI/Test Program CN :** <u>http://mchpweb/dms/testprogram/default.aspx</u>

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Test Data Template		
Error Code Glossary		



• 3 Change Types

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Insert item				
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	Impact (to customer)	O High	O Med	O Low
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Are Datasheets Affected	i? O No O Yes			



• Attachments

Home	Attachments	Approver History		
	visions Attachments	ate attachments to Test Flows and/or Test Hardware	Options section(s) below:	
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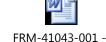
• **Reviewer/Approver** – Contact DocControlHELP to add name.

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- Test Program Verification and Peer Review Checklist :
 - FRM-41043-001





Example

- Action :
 - NA Not Applicable. Does not apply to this verification. No action was taken for this verification.
 - NV Not Verified. This section applies to the change being made, but a verification of this section was not done.
 - RV Reviewed and actions taken
- **SPI-41043-003 -** Test Verification, Review, and Release Procedure
- **SPI-41043-004 -** Test Program Release Criteria-Procedures



Appendix – Baan Notes

- Press "Tab" button to move the cursor to the next field.
- See the list of options by moving mouse to the triangle sign. Click mouse when it changes to magnifier.
- Can open several windows as needed.
- When update PSI Test Revision, always start from production active version (Version : 0).
- Document Control Team always verify if PDC Time Stamp matches the time stamp in eCN.

After submitted eCN, DO NOT make any change on PDC otherwise DCC will reject your eCN.



Appendix - Acronyms & Terms

 ROM : Read Only Memory Program customer code during wafer fabrication

(Very old Product only)

- OTP : One-Time-Programmable

 No customer code / Blank memory
 (EPROM / EEPROM / FLASH)
 No memory
 Default for Strip Test Product Configuration
- QTP : Quick-Turnaround-Production Program customer code during Final Test
- SQTP : Serialized Quick Turnaround Production Program customer code with serialized number during Final Test



Appendix – Links

- MCHPWEB : <u>http://microchipweb/</u>
- DMS: <u>http://microchipweb/pages/dms/Document%20Management%20System/toolsandresources</u>
- **Specification Index :** <u>http://mchpweb/dms/specindex/default.aspx</u>
- MPC Lookup : <u>http://mchpweb-pps/dms/pdcn/Pages/MPC%20Lookup.aspx</u>
- Hardware Database System : <u>http://hwdb/MainPage.aspx</u>
- Hardware Specification (14-Axxxx): http://mchpweb/dms/specindex/Pages/SpecDatabaseSearch.aspx?linkCat=NUMERIC
- PDC Approval Helper : http://beweb/MTAI/BaanApprovalHelper.aspx
- PSI/Test Program CN : <u>http://mchpweb/dms/testprogram/default.aspx</u>
- Tool to find lot information in MES :
 <u>http://mth-vm-shareweb/Mtaiisdev/dataservices_lotproperties.aspx?lotid=MTAI183702430.000</u>
- Tool to find MES lot# from YYWWNNN trace code : http://t04ftsrv/cgi-bin/mes/mtaiTraceCode.pl?TC=17494TS&B1=Fetch
- Acronyms and Terms : <u>http://microchipweb/search/acronyms</u>
- Application Request (iTURF) : <u>http://mchpweb/corporate/UserRequestForm/default.aspx</u>