



MICROCHIP

PDC for Final Test

Terdsak Somboonchan
MThai Project Engineering
2 February, 2018



Agenda

- **PDC - Overview – The 1st Day (3 hours)**
- **PDC – Hand on – The 2nd Day (3 hours)**
- **PDC – Change Notice – The 3rd Day (3 hours)**





Table of Contents - 1

Contents	Page
• <u>Final Test Setup Data</u>	5
• <u>What is Baan ?</u>	6
• <u>Request for Baan PDC User Account</u>	7
• <u>What is PDC ?</u>	8
• <u>What is MPC ?</u>	12
• <u>Process Step Characters</u>	19
• <u>Process Step Names</u>	20
• <u>Test Program Revision</u>	21
• <u>PSI Test Revision</u>	22
• <u>Tester Type / Handler Type</u>	32
• <u>Load Board / Contactor Data</u>	36
• <u>Hardware Setup</u>	40
• <u>Final Test Data Collection for PDC Creation</u>	42
• <u>Login to Baan PDC</u>	43
• <u>Exit from Baan PDC</u>	44
• <u>Baan Buttons</u>	46
• <u>Add Loadboard Type</u>	48
• <u>Add Contactor Type</u>	54





Table of Contents - 2

Contents	Page
• <u>Add Tester Interface Board</u>	58
• <u>Add Cable set usage</u>	60
• <u>Create Hardware Setup ID</u>	62
• <u>Add New Bin Groups</u>	68
• <u>Create Test Program ID</u>	78
• <u>Create Test Program ID Description</u>	91
• <u>Create Test Flow Group Name</u>	102
• <u>Process Plans</u>	112
• <u>Create PSI Test Revision</u>	115
• <u>Add New Test Temperature</u>	147
• <u>Tool for Reviewing PDC Data</u>	149
• <u>Assign Test Flow Group to the MPC</u>	156
• <u>MPC Test Attributes</u>	158
• <u>Print out the list of MPCs</u>	160
• <u>Print out Test Traveler / Setup Sheet from Baan PDC</u>	162
• <u>PSI/Test Program eCN</u>	170
• <u>Appendix</u>	175





Final Test Setup Data - 1

- **Baan PDC** : Engineering Lot, Test setup data for subcontractor

- Special Test Setup Options



Baan-TSO

- Special Test Traveler



Baan-TT

- **MES** : In house Production Lot (MTAI, MMT, MPHL)

- Test Setup Options : FRM-95002-031



MES-TSO

- Singulated Test Traveler : FRM-95002-014



MES-STT

- MTAI Strip Test Traveler : FRM-95002-030



MES-MTAI-STT



MES-YGAY2TV2X
031

- Subcon Strip Test Traveler : FRM-95002-032



MES-SUB-STT



MES-LEAR4YM4X
V03





What is Baan ?

- **Baan was a vendor of Enterprise Resource Planning (ERP) software that is now owned by Infor Global Solutions.**

- **History :**

The Baan Corporation was created by Jan Baan in 1978 in Barneveld, Netherlands, to provide financial and administrative consulting services.

The Baan company focused on the creation of ERP software.





Request for Baan PDC User Account

- Go to : <http://microchipweb/>
 - Tools & Resources → Information Services
 - Application Request (iTURF) → Create New Request
 - Fill in iTURF and have your manager approve it.
- [Fill in **BAAN/MySo** section per below example. Change role only. If you have already had Baan user account for another function, Please check Account Type as Change.]

BAAN/MySO		
<input checked="" type="checkbox"/> Production <input type="checkbox"/> Development (IS Only)	Account Type: <input checked="" type="radio"/> New <input type="radio"/> Change <input type="radio"/> MySO	*Baan User Copy ID: B00404
*Baan Company: (e.g. 101) 101		*List all functional roles: (e.g. Marketing, Finance, etc) Test Engineer

- You will be notified via email when the approval is completed and when the Baan changes have been completed.
- IS team will provide you user account and install Baan PDC software on your PC.
- **Note : iTURF = Information Technology User Request Form**





What is PDC ? - 1

- **PDC : Product Data Characteristics**
 - Microchip has started using it since year 2003.
 - It contains data used to define how Microchip products are made.
 - The data structure exists in the Baan database as a separate module.
 - It's a control database that requires CN to activate the data.





What is PDC ? - 2

- Reference Documents :
 - [BPC-00005](#) : PDC Test Process Guidelines
 - [BPC-00006](#) : PDC User's Guide – Test Hardware
 - [PI-70009](#) : PSI Test Revision
 - [PI-70010](#) : PDC Test Programs
 - [PI-70011](#) : PDC Test Hardware
 - [PI-70012](#) : Process Plan Maintenance
- Find the above specification at <http://microchipweb/>
 - MCHPWEB → Tools & Resources
 - Tools by popular Category → Document Management System (DMS)
 - DMS SharePoint Applications → Controlled Document Libraries
 - Specification Index → PI → Search



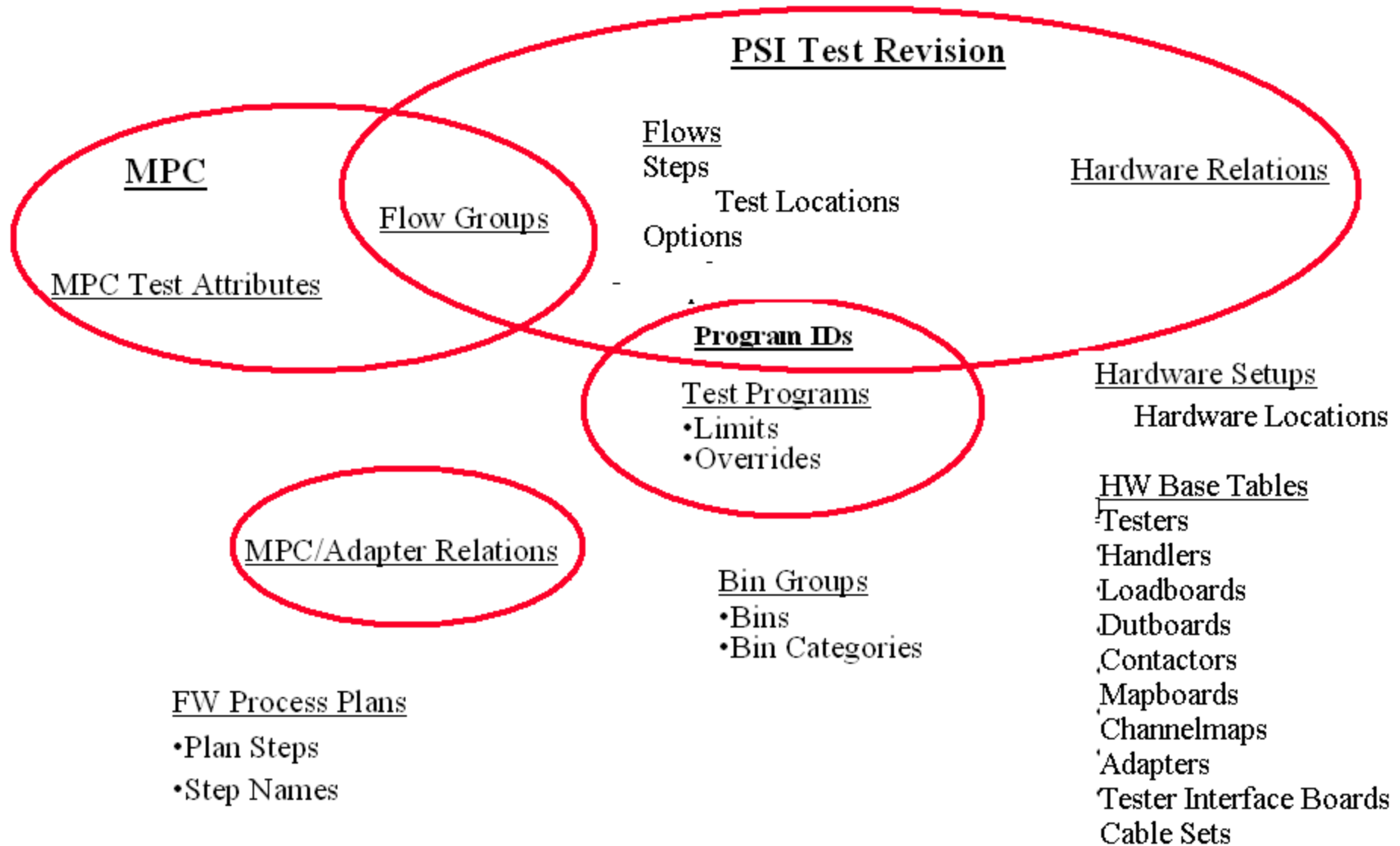


What is PDC ? - 3

- **Content (Focus on the data for Final Test only) :**
 - **Tester Type**
 - **Handler Type**
 - **Test Hardware (Load Board, DUT Board, Contactor, etc.)**
 - **Test Flow**
 - **Test Program**
 - **Handler Bins (Hardware Bins Per Test Program Assignment)**
 - **MPC Test Attributes**
[**Tester Device (Part Number for Test Program Loading)]**



What is PDC ? - 4



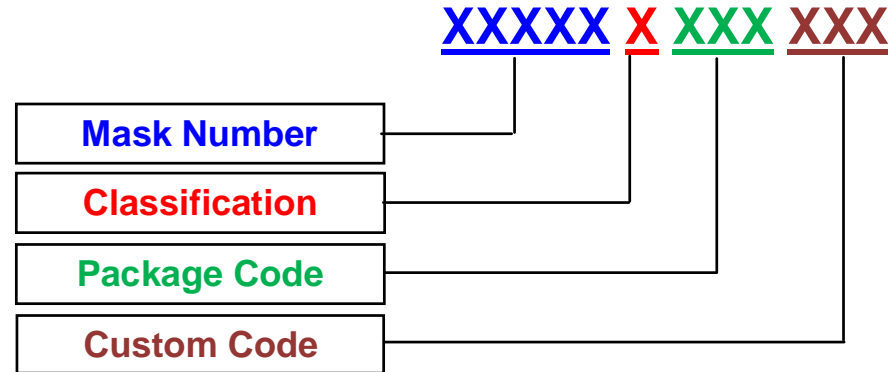


What is MPC ? - 1

- **MPC : Manufacturing Product Code**
Note : The MPC can be linked to only one Flow Group.
- **Consists of a 12-digit alphanumeric code**
 - **1st - 5th Characters : Mask number**
 - **6th Character : Classification**
 - **7th - 9th Characters : Package Code**
 - **10th - 12th Characters : Custom Code**
- **See more detail from :**
 - **[SPI-43506](#) : Product Identification System**
 - **[MS-14000-001](#) : Package Code Tables**



What is MPC ? - 2



Product Class Code	Description	Temperature Level	Temperature Range (degrees)
1	Commercial Temperature	Commercial	0°C to +70°C
4	Extended Temperature (E)	Extended	-40°C to +125°C
7	Industrial Temperature (I)	Industrial	-40°C to +85°C
J	High Temperature Range, Tape & Reel	High	-40°C to >+125°C
S	Commercial Temperature Tape & Reel	Commercial	0°C to +70°C
T	Industrial Temperature (I) Tape & Reel	Industrial	-40°C to +85°C
Y	Extended Temperature (E) Tape & Reel	Extended	-40°C to +125°C





What is MPC ? - 3

Package Code	Package Code Description	Package Type (8 Char Max.)	Lead Count	Industry Standard	Package Width Or Size	Pkg Otlm Dwg No.	Solder Composition (Terminal Finish)
2EX	Very Thin Quad Flatpack No-Leads	VQFN	028	JEDEC	5x5x0.9mm	C04-239	Matte Tin
2JX	Low-profile Plastic Quad Flat Pack	LQFP	176	JEDEC	20x20x1.4mm	C04-367	Matte Tin
2MX	Very, Very Thin Profile Fine Pitch Ball Grid Array	WFBGA	034	JEDEC	4x6x0.73mm	C04-164	SAC
2NX	Ultra Thin Quad Flatpack No-Leads	UQFN	028	JEDEC	6x6x0.55mm	C04-385	Matte Tin
2TX	Extremely-Thin Profile Fine Pitch Land Grid Array	XFLGA	048	JEDEC	4x6x0.6mm	C04-163	SAC
2WX	Very Thin Fine-Pitch Land Grid Array	VFLGA	068	JEDEC	8x8x1.0mm	C04-373	NiAu
2XX	Thin Profile Square Ball Grid Array	TFBGA	121	JEDEC	10x10x1.20mm	C04-148	SAC
3JX	Low Profile Fine Pitch Ball Grid Array	LFBGA	256	JEDEC	14x14x1.14mm	C04-369	SAC105
3RX	Very Thin Dual Flatpack No-Lead	VDFN	008	JEDEC	4x4x1.0mm	C04-285	NiPdAu
3XX	System In Package	SiP	121	-	8x8x1.08mm	C04-212	SAC
4AX	Super-thin Dual Flatpack No-Lead	X2DFN	006	JEDEC	1.5x1.8x0.33mm	C04-441	Au
4DX	Thin Ball Grid Array	TBGA	024	JEDEC	6x8x1.2mm	C04-199	SAC
4GX	Very Thin Fine Pitch Ball Grid Array	VFBGA	064	JEDEC	7x7x1.0mm	C04-370	SAC125+Ni
4QX	Thin Dual Flatpack No-Lead	TDFN	006	JEDEC	2x2x0.8mm	C04-078	NiPdAu
4XX	PCB Module	MODULE	043	-	32x15x2.5mm	C04-10041	NiAu
5SX	Very, Very Thin Leadless Array Package	WTLA	020	-	3x3x0.7mm	C04-184	NiPdAu
6QX	Ultra Thin Dual Flatpack No-Lead	UDFN	008	JEDEC	2x3x0.5mm	C04-136	NiPdAu
6SX	Very Thin Leadless Array Package	VTLA	044	-	6x6x0.9mm	C04-157	NiPdAu
7BX	Micro Small Outline Package	MSOP	008	JEDEC	3x3mm	C04-111	NiPdAu
8PX	Leadless Land Grid Array	LLGA	026	-	6x6x0.60mm	C04-277	NiPdAu
8XX	Super Thin Dual Flatpack No-Leads	X2SON	008	JEDEC	1.5x1.5x0.40mm	C04-338	Matte Tin
9AX	Dual Flatpack No-Leads Simulator	TDFN-S	008	JEDEC	6x5x0.8mm	C04-169	NiPdAu
9HX	Quarter Size Outline Package	QSOP	044	JEDEC	7.5mm(.300in)	C04-282	Matte Tin
9XX	Super Thin Quad Flatpack No-Leads	X2QFN	010	JEDEC	1.5x1.5x0.40mm	C04-378	Matte Tin
A4X	Thin Shrink Small Outline Package	TSSOP	008	JEDEC	4.4mm	C04-086	Matte Tin
A6X	Dual Flatpack No-lead	DFN-S	008	JEDEC	6x5x0.9mm	C04-122	Matte Tin
A7X	Dual Flatpack No-lead	DFN	008	JEDEC	3x3x0.9mm	C04-062	Matte Tin
ACA	Plastic Ball Grid Array	PBGA	289	JEDEC	19x19x2.36mm	C04-1085	SAC
ALA	Very Thin Land Grid Array	VLGA	006	JEDEC	3.2x2.5x0.9mm	C04-1215	NiAu
ASX	High-Power Dual Flatpack No-Lead	PDFN	008	JEDEC	5x6x0.9mm	C04-188	Matte Tin
B9X	Metric Plastic Quad Flat Pack	MQFP	100	JEDEC	14x20x2.7mm	C04-050	Matte Tin
C2X	Plastic Small Outline Integrated Circuit	SOIC	008	JEDEC	.150in(3.90mm)	C04-057	Matte Tin
C3X	Plastic Small Outline IC	SOIJ	008	EIAJ	.208in	C04-056	Matte Tin
C4X	Plastic Dual-In-Line Package	PDIP	008	JEDEC	.300in	C04-018	Matte Tin
FDX	Ultra Thin Fine Pitch Ball Grid Array	UFBGA	025	JEDEC	3x3x0.6	C04-412	SAC
G3X	Shrink Small Outline Package	SSOP	020	JEDEC	.209in	C04-072	Matte Tin
L4X	Plastic J-Leaded Chip Carrier	PLCC	028	JEDEC	11.5x11.5x4.3	C04-026	Matte Tin
LFX	Very-Thin Quad Flatpack No-Lead, Routable (HLA™)	VQFN-R	025	JEDEC	6x6x0.9mm	C04-331	Cu
M2X	Quad Flatpack No-Leads	QFN-S	028	JEDEC	6x6x0.9mm	C04-124	Matte Tin
M3X	Skinny Plastic Dual-In-Line Package	SPDIP	028	JEDEC	.300in	C04-070	Matte Tin
M4X	Quad Flatpack No-Leads	QFN	028	JEDEC	6x6x0.9mm	C04-105	Matte Tin
N6X	Thin Small Outline Transistor	TSOT	006	JEDEC	2.9x1.6x1.0mm	C04-141	Matte Tin
NAX	Dual Row Quad Flatpack No-Lead	DQFN	124	JEDEC	10x10x0.85mm	C04-395	Matte Tin
Q3X	Ultra Thin Small Outline No-Leads (SON or DFN)	USON	008	JEDEC	2x3x0.6mm	C04-203	Matte Tin
Q7X	Extremely Thin Dual Flatpack No-Leads	XSON	008	JEDEC	2x2x0.45mm	C04-205	Matte Tin
RWX	Very, Very Thin Dual Flatpack No-Leads	WDFN	008	JEDEC	2x2x0.8mm	C04-261	Matte Tin
T4X	Thin Plastic Quad Flat Pack	TQFP	044	JEDEC	10x10x1mm	C04-076	Matte Tin
THX	Flip Chip Ball Grid Array	FCBGA	169	JEDEC	11x11mm	C04-309	SnPb
TJX	Fine Pitch Ball Grid Array	FPBGA	025	JEDEC	5.7x5mm	C04-310	SnPb
TVX	Plastic Metric Quad Flatpack	PQFP	044	JEDEC	10x10x2mm	C04-119	SnPb
UDX	Very Thin Quad Flatpack No Lead	SQFN	036	JEDEC	6x6x1.0mm	C04-272	Matte Tin
W6X	Thin Small Outline Package	TSOP	032	JEDEC	8x14mm	C04-174	Matte Tin



What is MPC ? - 4

- There are 4 MPC Stages.
 - **NREL** : Non-Release / Engineering
 - **REL** : Released / Production Released
 - **PHAS** : Phase out / Last Time Buy
 - **EOL** : End of Life





What is MPC ? - 5

- [MPC Lookup](#)
- Go to <http://microchipweb/>
 - MCHPWEB → Tools & Resources
 - Tools by popular Category → Document Management System (DMS)
 - DMS SharePoint Applications → Lookup Applications
 - MPC Lookup
 - Search by Mask, MPC, Division, CPN, Customer, etc.
 - Click “View Detail”





What is MPC ? - 6

- LEAD1TT4X030

MPC Viewer

mchpweb-netapps/Dataviewer/MPC?MPC=LEAD1TT4X030&revision=A

MPC Viewer Status: **ACTIVE**

MPC: Revision: Rev Date: CN: CN Date:

MPC

General Attributes

Mask	<input type="text" value="LEAD1"/>	Product Type	<input type="text" value="D63"/>	Product Revision	<input type="text"/>
Product Class	<input type="text" value="T"/>	Restriction Level	<input type="text" value="CUSTOM"/>	QA/QB Code	<input type="text" value="QA"/>
Package Code	<input type="text" value="T4X"/>	Acquisition Code	<input type="text"/>	Base Qty Multiple	<input type="text" value="1200"/>
Custom Code	<input type="text" value="030"/>	Sub-Cons BD	<input type="text" value="No"/>	Emulator MPC	<input type="text" value="No"/>
MPC Stage	<input type="text" value="REL"/>	BD Part No	<input type="text" value="PIC18F46K20"/>	Device Checksum	<input type="text" value="0339"/>
BOM Level Code	<input type="text" value="FGM"/>	Packing Media	<input type="text" value="T/R"/>	Blank Checksum	<input type="text" value="0362"/>
BAS/DES Where Used	<input type="text" value="N/A"/>	Pattern Type	<input type="text" value="QTP"/>	UL Certified	<input type="text" value="No"/>
Catalog Part Number	<input type="text" value="*see below"/>	Pattern Number	<input type="text" value="030"/>	Firm ware Rev	<input type="text"/>
Catalog Part Primary	<input type="text" value="Yes"/>	UV Sensitive	<input type="text" value="No"/>	Buy/Resale/Foundry	<input type="text" value="No"/>
Functional Unit	<input type="text" value="MCHP"/>	Test Flow Group	<input type="text" value="*see below"/>	No Build	<input type="text" value="No"/>
Config Level	<input type="text" value="N/A"/>	SDP MPC	<input type="text"/>	Late Marking	<input type="text" value="No"/>
Default Warehouse	<input type="text" value="FB1"/>	Temp Range	<input type="text"/>	Pin Orientation	<input type="text" value="Quadrant 2"/>

Catalog Part Number:
 Test Flow Group:

Marking

Location	Line	Value
FRONT	1	@
FRONT	2	05514827
FRONT	3	YYWWNNN

Comments



What is MPC ? - 7

- LEAD1TT4X030**

MPC Viewer

mchpweb-netapps/Dataviewer/MPC?MPC=LEAD1TT4X030&revision=A

Mask Pattern Attributes

Pattern	030	SQTP Source	None
Checksum OFF	AA8C	Start Address	
Checksum ON	0339	Byte Count	0
Checksum ROM		Promote Method	None
QCode		Start Value	
Cust Hex ID	FFFFFFF	Increment	0
SDP Prod ID		Hex File	
SDP Cust ID		SQTP File	
Code Source	E:Mail	Serial Hex File	
Promote Version	PM3V8.91	Status Register	

MPC Test Attributes

Tester Type	Test Device
J750	18F46K20
LTX_D2X	18F46K20

Essential Element Constraints

NSCARs

Wafer Map Communication

Acquired Part Information

Assembly Instructions

Bill Of Material (BOM)

Starting Material # 1

Component Item	BOM level	CPN	Use Priority	Qty	Effective Date
LEAD1101XXXX	DIS		10	1	02/21/2015

CPN

Note:

- For convenience, a snapshot of the CPN information is shown below.
- MPC data will continue to contain references to CPN data which may no longer be pertinent to the MPC revision when looking at INACTIVE MPC revs.

Catalog Part Number	PIC18F46K20T-1/PT030	Release to Sample	No
CPN Stage	REL	Release to Buy-Microchip	No
Web Page Part #	PIC18F46K20	NCNR Flag	No
End Customer	TRIDONIC GmbH & Co. KG	Stop Orders	No



Process Step Characters

(PI-70012)

Step Character	Description
B	Burn-in
C	Temperature Cycles
E	Endurance Test (Singulated)
F	Final Test (Singulated)
I	100% Integrated Final Test (Strip Final Test + Inline QC)
J	100% Integrated (Singulated Final Test + Inline QC)
K	Bake and 24 Hour Hold Bake at Room Temperature
Q	QC Sampling (Singulated) per LTPD Table
R	Rescreen (Singulated) – 100% of the lot
S	100% Final Test (Strip)
T	QC Sampling (Strip) per LTPD Table
U	Rescreen (Strip)
V	Rescreen all QC samples from previous step (QC(n)) – QC Sample only
Y	No Final Test required step





Process Step Names

(PI-70012)

Step Character	Step Name	Test Method	Description
B	BI(n)	Singulated	Where (n) is the number of Burn - IN Test being performed
E	ET(n)	Singulated	Where (n) is the number of Endurance Test being performed
S	FS(n)	Strip	Where (n) is the number of Final Test being performed
F	FT(n)	Singulated	Where (n) is the number of Final Test being performed
I	IS(n)	Strip	Where (n) is the number of Integrated Final Test being performed
J	IT(n)	Singulated	Where (n) is the number of Integrated Final Test being performed
Q	QC(n)	Singulated	Where (n) is the number of Quality Sampling Test being performed
T	QS(n)	Strip	Where (n) is the number of QC Sampling Test being performed
K	RB(n)	Singulated	Where (n) is the number of Retention Bake being performed
U	RS(n)	Strip	Where (n) is the number of Re-screen (100% Test) being performed
R	RT(n)	Singulated	Where (n) is the number of Re-screen (100% Test) being performed
C	TC(n)	Singulated	Where (n) is the number of the Temperature Cycle being performed
Y	YY1	Singulated	Used for MPCs where no final test steps are necessary. Allows print out SCAN and QC FOI lot traveler.
V	VT(n)	Singulated	Where (n) is the number of Re-screen all QC Sample being performed



Test Program Revision (PI-70010)

pctf10120m000 : Maintain Test Program Revisions [101]

File Edit Group Options Order Tools Special Help

Tester Type J750 Teradyne J750 (all models)

Program ID 122222 LEAR0_FT. FT-PRD-CERLER

Revision 0

Version 0

Status ACT

Test Program LEAR0_FT_A48e.XLS

Job Name FT-PRD-CERLER

Checksum 16540E2

Bin Group STANDARD Bins 2 - 5

Correlation Process Code

Correlation Good Bin 0

Special Instructions

J750 OI Ver

CN Number 1700910

Comments

RMA Disposition No Special Requirements

OS Version Job Number



PSI Test Revision – 1

(PI-70009)

- **PSI : Product Specification Index**
- **The combination of Test Flow, Test Hardware and Test Program defined for use in the production for each product (Mask Number).**

pctf0110m000 : Maintain PSI Test Revisions [101]

File Edit Group Options Order Tools Special Help

PSI Mask: LEAD1

Revision: AD [New Rev]

Version: 0 [New Ver]

Status: ACT

CN Number: 1701736

Description: Change hardware id of 44L QFN 8x8

Job Number: Multi-Step Test Program

Test Flows

HW Relations

- **Note :**
- **Job Number** - When a mass load is used to add new PSI Test Revision data to the tables, a job number is assigned by the data loader process.
- **Multi-Step Test Program** – Set to YES if the mask may be using an ‘operation number’ at each test step when testing products.





PSI Test Revision – 2

(PI-70009)

- **Revision is defined with letter designation.**
[A,B,C,...., AA,AB,AC,....,ZZ] Except I,O
- **Version is defined with number designation.**
Version : 0 is the production version.
Version : 1,2,3,....,99 is engineering version
(Engineering Sample Test Flow, Correlation Test Flow, Special rescreen flow for quality issue lot, etc.).
- **Note : When update production test flow, should start from version 0.**
When create engineering version, should have only the flow you created, delete all other flows.



PSI Test Revision – 3

(PI-70009)

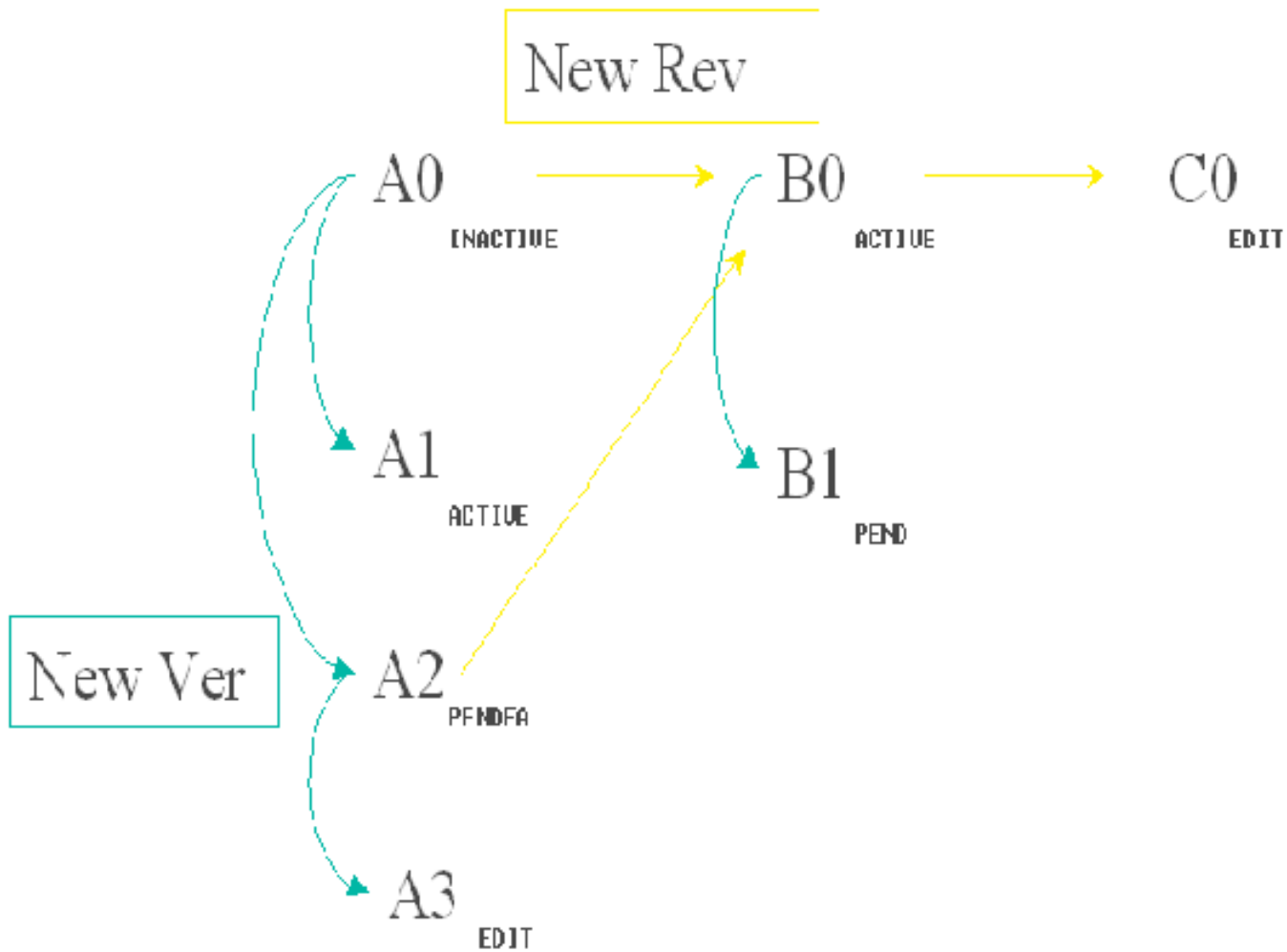
- **Status**
- **EDIT : Add, Modify, Delete Data**
Default state for new revision/version
- **PEND : Pending Approval (Will get all approvals before start using it in production)**
- **PENDEA : Pending Engineering Approval**
(In urgent case, production will proceed before getting all approvals but the lots will be placed on hold in manufacturing before they are moved to Finished Goods inventory.)
- **ACT : Active. Data in this status is available for use by manufacturing.**
- **INACT : Inactive. Data in this status is no longer available for use by manufacturing.**





PSI Test Revision – 4

(PI-70009)





PSI Test Revision – 5

(PI-70009)

- Flow Type – STD (Standard – Production Test Flow)

pctf0112s000 : Maintain Test Flows [101]

File Edit Group Options Order Tools Special Help

Form 1 Form 2

PSI Mask LEAD1 Revision AD Version 0 Steps Test Location

Flow Type STD

Flow Config STRIP

Copy Flow

Flow Number	Mont	Bank	Flow	Flow Group	Process Plan Name	Description	Test Location
260366	<input type="checkbox"/>	<input type="checkbox"/>	<input checked="" type="checkbox"/>	D2X-J750 STRIP EXTENDED OTP CERLEI	SIQ	D2X STRIP EXTENDED OTP CER	MTAI,
260368	<input type="checkbox"/>	<input type="checkbox"/>	<input checked="" type="checkbox"/>	D2X-J750 STRIP EXTENDED OTP CERLEI	SIQ	J750 STRIP EXTENDED OTP CEI	MTAI,
260374	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	D2X-J750 STRIP EXTENDED OTP CERLEI	FFQQ	EXTENDED OTP CERLER	MTAI,
260452	<input type="checkbox"/>	<input type="checkbox"/>	<input checked="" type="checkbox"/>	D2X-J750 STRIP EXTENDED OTP CERLEI	SIT	J750 STRIP EXTENDED OTP CEI	MTAI,
245844	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	D2X-J750 STRIP INDUSTRIAL OTP	FFQQ	SINGULATED INDUSTRIAL OTP	MTAI,
245845	<input type="checkbox"/>	<input type="checkbox"/>	<input checked="" type="checkbox"/>	D2X-J750 STRIP INDUSTRIAL OTP	SIQ	J750 STRIP INDUSTRIAL OTP :	MTAI,
245846	<input type="checkbox"/>	<input type="checkbox"/>	<input checked="" type="checkbox"/>	D2X-J750 STRIP INDUSTRIAL OTP	SIT	J750 STRIP INDUSTRIAL OTP :	MTAI,
245847	<input type="checkbox"/>	<input type="checkbox"/>	<input checked="" type="checkbox"/>	D2X-J750 STRIP INDUSTRIAL OTP	SIQ	D2X STRIP INDUSTRIAL OTP X	MTAI,
245848	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	D2X-J750 STRIP INDUSTRIAL QTP	FFQQFR	SINGULATED INDUSTRIAL QTP	MTAI,
245849	<input type="checkbox"/>	<input type="checkbox"/>	<input checked="" type="checkbox"/>	D2X-J750 STRIP INDUSTRIAL QTP	SIQFR	J750 STRIP INDUSTRIAL QTP :	MTAI,
245850	<input type="checkbox"/>	<input type="checkbox"/>	<input checked="" type="checkbox"/>	D2X-J750 STRIP INDUSTRIAL QTP	SITFR	J750 STRIP INDUSTRIAL QTP :	MTAI,
245851	<input type="checkbox"/>	<input type="checkbox"/>	<input checked="" type="checkbox"/>	D2X-J750 STRIP INDUSTRIAL QTP	SIQFR	D2X STRIP INDUSTRIAL QTP X	MTAI,



PSI Test Revision – 6

(PI-70009)

- Flow Type – REL (Reliability Test Flow)

pctf0112s000 : Maintain Test Flows [101]

File Edit Group Options Order Tools Special Help

Form 1 Form 2

PSI Mask LEAD1 Revision AD Version 0 Steps Test Location

Flow Type REL

Flow Config STRIP Copy Flow

Flow Number	Mont	Bank	Flow	Flow Group	Process Plan Name	Description	Test Location
179187	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	RELIABILITY	FFFFFFF	PRE DLI	MTAI,
179207	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	RELIABILITY	BFFFFFFBFFF	DLI MONITOR	MTAI,
255713	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	RELIABILITY	FFFFFFBRRRBRR	PRE+DLI MONITOR	MTAI,
	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>				
	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>				
	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>				
	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>				
	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>				
	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>				
	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>				
	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>				



PSI Test Revision – 7

(PI-70009)

- Flow Type – RSN (Rescreen - Test Flow for QC Rejected Lot)

pctf0112s000 : Maintain Test Flows [101]

File Edit Group Options Order Tools Special Help

Form 1 Form 2

PSI Mask LEAD1 Revision AD Version 0 Steps Test Location

Flow Type RSN

Flow Config STRIP Copy Flow

Flow Number	Mont	Bank	Flow	Flow Group	Process Plan Name	Description	Test Location
260377	<input type="checkbox"/>	<input type="checkbox"/>	<input checked="" type="checkbox"/>	D2X-J750 STRIP EXTENDED OTP CERLER	U	D2X STRIP EXTENDED OTP CERLER	MTAI,
260378	<input type="checkbox"/>	<input type="checkbox"/>	<input checked="" type="checkbox"/>	D2X-J750 STRIP EXTENDED OTP CERLER	U	J750 STRIP EXTENDED OTP CERLER	MTAI,
260379	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	D2X-J750 STRIP EXTENDED OTP CERLER	FQ	EXTENDED OTP CERLER [QC@12]	MTAI,
260380	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	D2X-J750 STRIP EXTENDED OTP CERLER	RQ	EXTENDED OTP CERLER [QC@12]	MTAI,
260381	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	D2X-J750 STRIP EXTENDED OTP CERLER	FQ	EXTENDED OTP CERLER [QC@-4]	MTAI,
260382	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	D2X-J750 STRIP EXTENDED OTP CERLER	RQ	EXTENDED OTP CERLER [QC@-4]	MTAI,
260453	<input type="checkbox"/>	<input type="checkbox"/>	<input checked="" type="checkbox"/>	D2X-J750 STRIP EXTENDED OTP CERLER	U	J750 STRIP EXTENDED OTP CERLER	MTAI,
245852	<input type="checkbox"/>	<input type="checkbox"/>	<input checked="" type="checkbox"/>	D2X-J750 STRIP INDUSTRIAL OTP	U	J750 STRIP INDUSTRIAL OTP	MTAI,
245853	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	D2X-J750 STRIP INDUSTRIAL OTP	FQ	SINGULATED INDUSTRIAL OTP	MTAI,
245854	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	D2X-J750 STRIP INDUSTRIAL OTP	RQ	SINGULATED INDUSTRIAL OTP	MTAI,
245855	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	D2X-J750 STRIP INDUSTRIAL OTP	FQ	SINGULATED INDUSTRIAL OTP	MTAI,
245856	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	D2X-J750 STRIP INDUSTRIAL OTP	RQ	SINGULATED INDUSTRIAL OTP	MTAI,



PSI Test Revision – 8

(PI-70009)

- HW Relations - Relations

The screenshot shows a software window titled "pctf0111s000 : Maintain HW Setup Relations [101]". The window has a menu bar (File, Edit, Group, Options, Order, Tools, Special, Help) and a toolbar with various icons. Below the toolbar, there are tabs for "Relations", "Contact/Channel", and "Locations". The "Relations" tab is active, displaying a table with columns: Lead Count, Configuration, Setup ID, Tester, Handler, Loadboard, Dutboard, and Mapboard. A "Copy Relations" button is located in the top right of the table area.

Lead Count	Configuration	Setup ID	Tester	Handler	Loadboard	Dutboard	Mapboard
40	600DIP	74	J750	DAY	14-A2991		
40	600DIP	76	J750	DAY	14-A2991		
40	UQFN_5X5	2	J750	RA2	14-A4384		
44	44TQFP_10	44	J750	CAS	14-A3100		
44	44TQFP_10	45	J750	CAS	14-A3100		
44	44TQFP_10	63	J750	SCH	14-A4254		
44	44TQFP_10	122	LTX_D2X	SCH	14-A6940		
44	44TQFP_10	125	LTX_D2X	SCH	14-A6940		
44	QFN_8X8	105	J750	SCH	14-A4838		
44	QFN_8X8	183	J750	MUT	14-A3112		
44	QFN_8X8	184	J750	MUT	14-A3112		
48	UQFN_6X6	1	J750	RAS	14-A4110		



PSI Test Revision – 9

(PI-70009)

- HW Relations – Contact/Channelmap

The screenshot shows a software window titled "pctf0111s000 : Maintain HW Setup Relations [101]". The window has a menu bar with "File", "Edit", "Group", "Options", "Order", "Tools", "Special", and "Help". Below the menu bar is a toolbar with various icons for file operations and navigation. The main area of the window is divided into tabs: "Relations", "Contact/Channelmap", and "Locations". The "Contact/Channelmap" tab is active, displaying a table with the following columns: "PSI Mask", "LEAD1", "Revision", "AD", "Version", "0", "Lead", "Setup", "Tester Interface", and "Cable Set". The table contains 13 rows of data, each representing a different hardware configuration.

PSI Mask	LEAD1	Revision	AD	Version	0	Lead	Setup	Tester Interface	Cable Set
Count	Configuration	ID	Contact	Channelmap					
40	600DIP	74	14-A1062	x3day40dip					
40	600DIP	76	14-A1062	x4day40dip_256p					
40	UQFN_SX5	2	14-A4385	x4ras40uqfn_256p					
44	44TQFP_10	44	14-A1961	x3dlc44tqfp_128p					
44	44TQFP_10	45	14-A1961	x4dlc44tqfp_256p					
44	44TQFP_10	63	14-A2834	x20mct44tqfp					
44	44TQFP_10	122	14-A6962	x56mct44tqfp					
44	44TQFP_10	125	14-A7951	x56mct44tqfp					
44	QFN_8X8	105	14-A4091	x25mct44qfn					
44	QFN_8X8	183	14-A2683	x8mult44lqfn_512					
44	QFN_8X8	184	14-A2683	x6mult44lqfn_256					
48	UQFN_6X6	1	14-A1062	2ras48uqfn_128p					



PSI Test Revision – 10

(PI-70009)

- HW Relations – Locations

The screenshot shows a software window titled "pctf0111s000 : Maintain HW Setup Relations [101]". The window has a menu bar with "File", "Edit", "Group", "Options", "Order", "Tools", "Special", and "Help". Below the menu bar is a toolbar with various icons for file operations and navigation. The main area of the window displays a table with the following data:

PSI Mask	LEAD1	Revision	AD	Version	0
Lead	Setup				
Count	Configuration	ID	Test Plant(s)		
40	600DIP	74	MTAI,		
40	600DIP	76	MTAI,		
40	UQFN_5X5	2	MTAI,		
44	44TQFP_10	44	MTAI,		
44	44TQFP_10	45	MTAI,		
44	44TQFP_10	63	MTAI,		
44	44TQFP_10	122	MTAI,		
44	44TQFP_10	125	MTAI,		
44	QFN_8X8	105	MTAI,		
44	QFN_8X8	183	MTAI,		
44	QFN_8X8	184	MTAI,		
48	UQFN_6X6	1	MTAI,		



Tester Type / Handler Type - 1

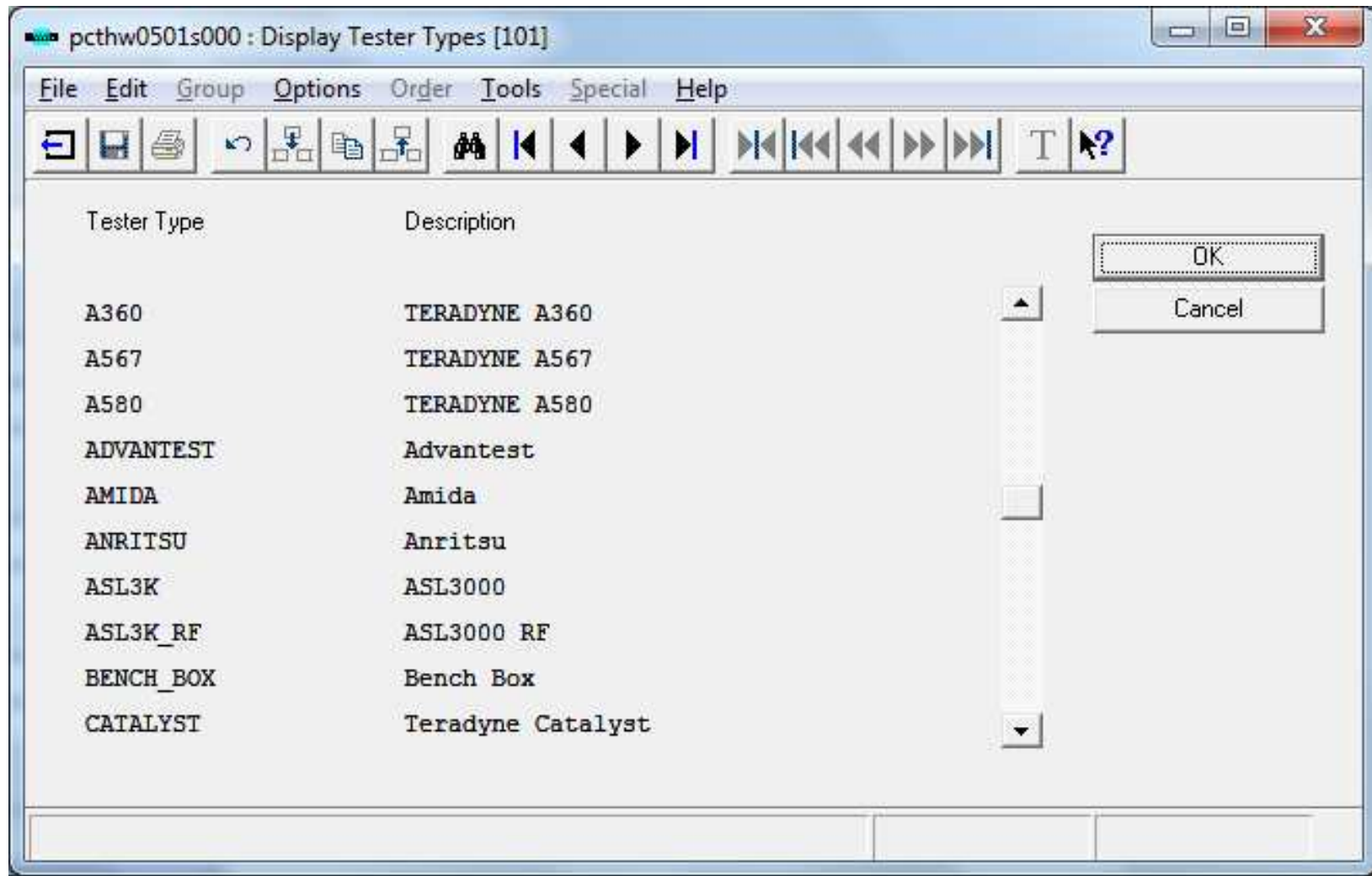
- **Tester Type : 20-Character Maximum**
141 Tester Types as of Dec 7, 2017
- **Handler Type : 20-Character Maximum**
96 Handler Types as of Dec 7, 2017



Tester-Handler

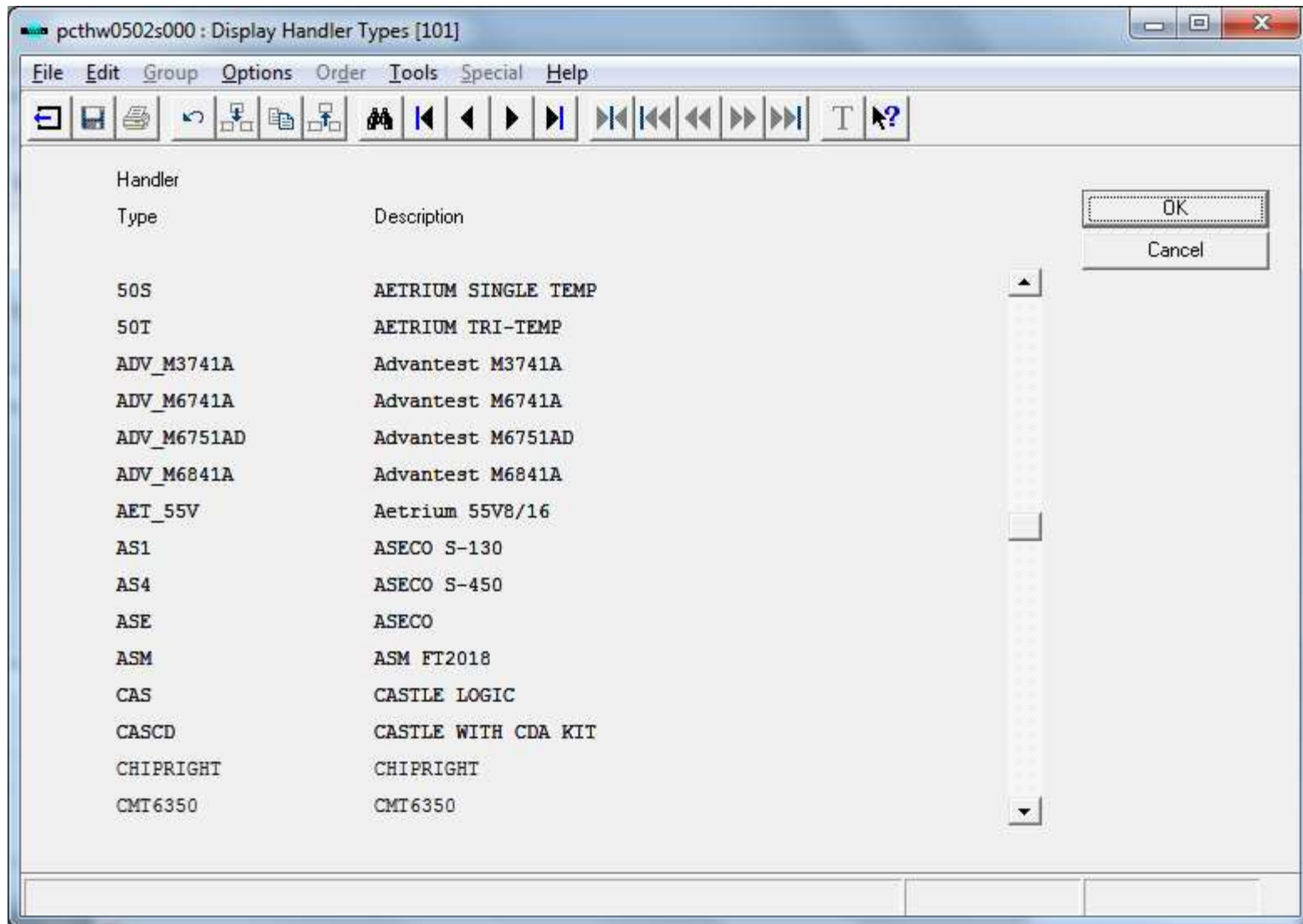


Tester Type / Handler Type - 2





Tester Type / Handler Type - 3





Tester Type / Handler Type - 4

- **Add New Tester / Handler Type**
- Discuss with **Soravorn Pochpring - B00022** (MThai IE) on the Tester/Handler name then submit a request.
- Go to : <http://mth-sv-qaapp/ApproveCenter/equipname>
- Login by using your PC login User name and Password.
- Select “Equipment Name”.
- Choose “New Tester” or “New Handler”.
- Input the purpose.
- Select “Add Tester” or “Add Handler”, Fill in the data then click “Submit”.
- Click “Create Approval Loop” and leave all default approvers.
- Click “Submit to Approval”.
- You will get a notification email once your request got all approvals or rejected.





Load Board / Contactor Data - 1

- Load Board Design files on DMS.
- Go to <http://microchipweb/>
 - MCHPWEB → Tools & Resources
 - Tools by popular Category → Document Management System (DMS)
 - DMS SharePoint Applications → Controlled Document Libraries
 - Specification Index → 14-A Hardware Schematics
 - Search by Spec Num
 - Click “View Detail” to get .zip file.

Drag a column header and drop it here to group by that column

Detail	Type	Status	Spec Num	Revision	Title	Originator	Owner	Part Number	Incorporated CN
	<input type="text"/>	<input type="text"/>	14-A6940	<input type="text"/>	<input type="text"/>	<input type="text"/>	<input type="text"/>	<input type="text"/>	<input type="text"/>
View Detail	14	Current	14-A6940	A	ST CREDENCE DIAMOND 2X PIC18F46K20 x56 44L TQFP 10X10 MCT-TAPESTRY	Ponthanathorn Udomrattanasiriporn - B09836	Valen Burd - C10844		CN_14001845





Load Board / Contactor Data - 2

- Load Board Data in Baan PDC.

pcthw0103m000 : Maintain Loadboard Types [101]

File Edit Group Options Order Tools Special Help

Loadboard Type: I4-A6940 Status: ACTIVE Tester Types

Loadboard Name: ST D2X PIC18F46K20 X56 44L TQFP 10X10 MCT-TAPESTRY

Site Count: 56

Mfg Part No.

pcthw0113s000 : Maintain Loadboard Type/Tester Types Relations [101]

File Edit Group Options Order Tools Special Help

Loadboard Type: I4-A6940 ST D2X PIC18F46K20 X56 44L TQFP 10X10 MCT-TAPESTRY

Tester Type	Description
LTX_D2X	Dual-chassis Diamond-X



Load Board / Contactor Data - 3

- Contactor Design files on DMS.
- Go to <http://microchipweb/>
 - MCHPWEB → Tools & Resources
 - Tools by popular Category → Document Management System (DMS)
 - DMS SharePoint Applications → Controlled Document Libraries
 - Specification Index → 14-A Hardware Schematics
 - Search by Spec Num
 - Click “View Detail” to get .zip file.

Drag a column header and drop it here to group by that column

Detail	Type	Status	Spec Num	Revision	Title	Originator	Owner	Part Number	Incorporated CN
	<input type="text"/>	<input type="text"/>	<input type="text" value="14-A6747"/>	<input type="text"/>	<input type="text"/>	<input type="text"/>	<input type="text"/>	<input type="text"/>	<input type="text"/>
View Detail	14	Current	14-A6747	A	CT x32 6L SOT23 MCT-TAPESTRY	Noppakun Permkhuer - B02110	Valen Burd - C10844	SPC0158A-T01	B1401A





Load Board / Contactor Data - 4

- Contactor Data in Baan PDC.

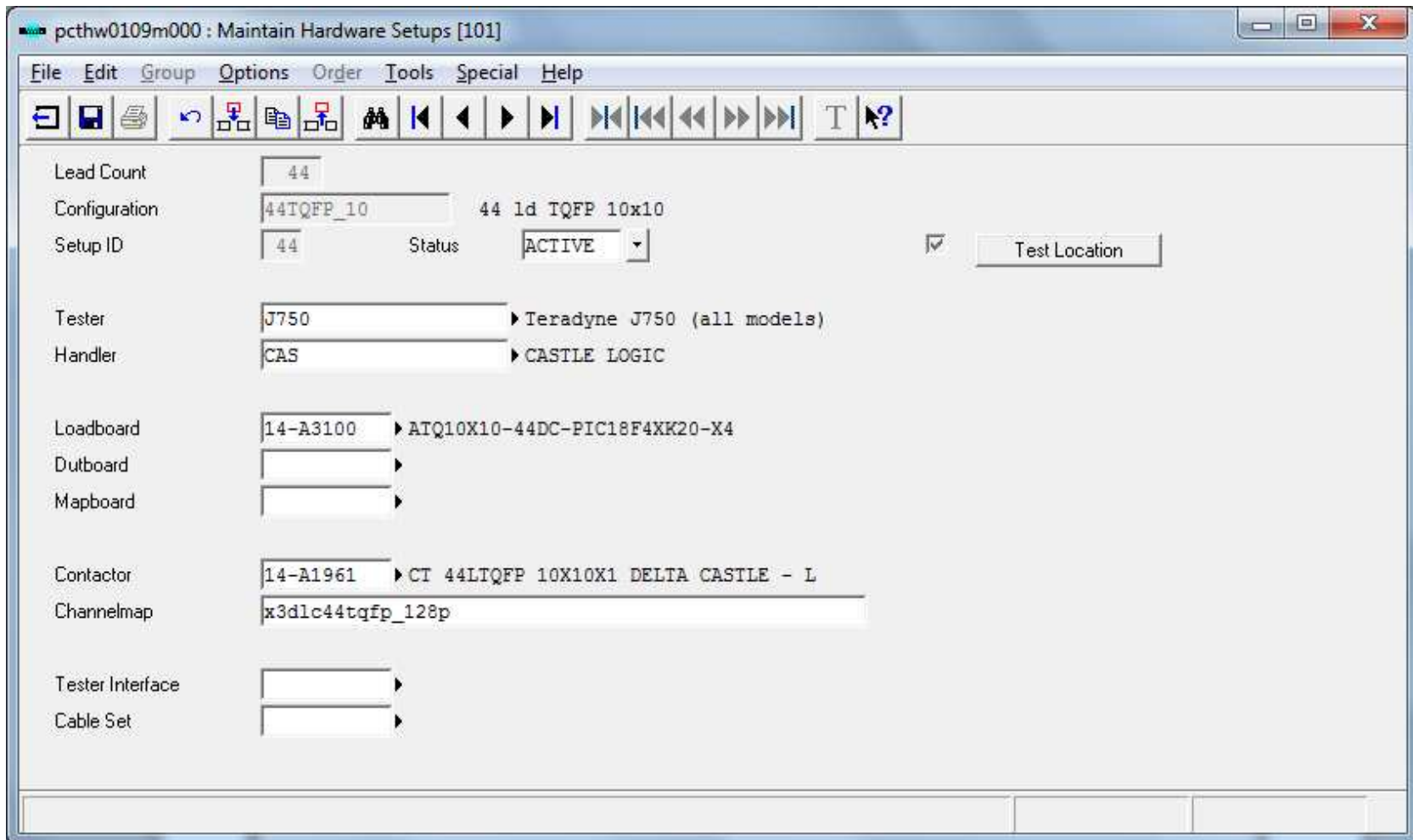
The screenshot shows a software window titled "pcthw0105m000 : Maintain Contactor Types [101]". The window has a menu bar with "File", "Edit", "Group", "Options", "Order", "Tools", "Special", and "Help". Below the menu bar is a toolbar with various icons for file operations and navigation. The main area contains a table with three columns: "Contactor Type", "Contactor Name", and "Status". The table lists 15 contactor entries, each with a unique ID, a detailed name, and a status of "ACTIVE".

Contactor Type	Contactor Name	Status
14-A6618	CT STE X1 16L/24L QSOP 150 Delta717	ACTIVE
14-A6620	CT X2 132L DQFN 11X11 NS-8080	ACTIVE
14-A6636	CT STE X1 128L MQFP 14X20X2.7 CASTL	ACTIVE
14-A6637	CT STE X1 100L QFP 14X20X2.7 CASTLE	ACTIVE
14-A6638	CT STE X1 100L TQFP 14X14X1.4 CASTL	ACTIVE
14-A6640	CT X1 196L CABGA 12X12X1.7 CASTLE	ACTIVE
14-A6647	CT X1 100L TQFP 14X14X1 DELTA CASTL	ACTIVE
14-A6648	CT X1 128L QFP 14X20X2.7 CASTLE ECT	ACTIVE
14-A6649	CT X1 128L QFP 14X20X2.7 CASTLE	ACTIVE
14-A6747	CT x32 6L SOT23 MCT-TAPESTRY	ACTIVE
14-A6767	CT X1 40L QFN 6X6X0.9 CASTLE	ACTIVE
14-A6768	CT X1 208L QFP 28X28X3.4 CASTLE	ACTIVE
14-A6769	CT X1 40L VFBGA 4X4X0.8 DELTA EDGE	ACTIVE



Hardware Setup - 1

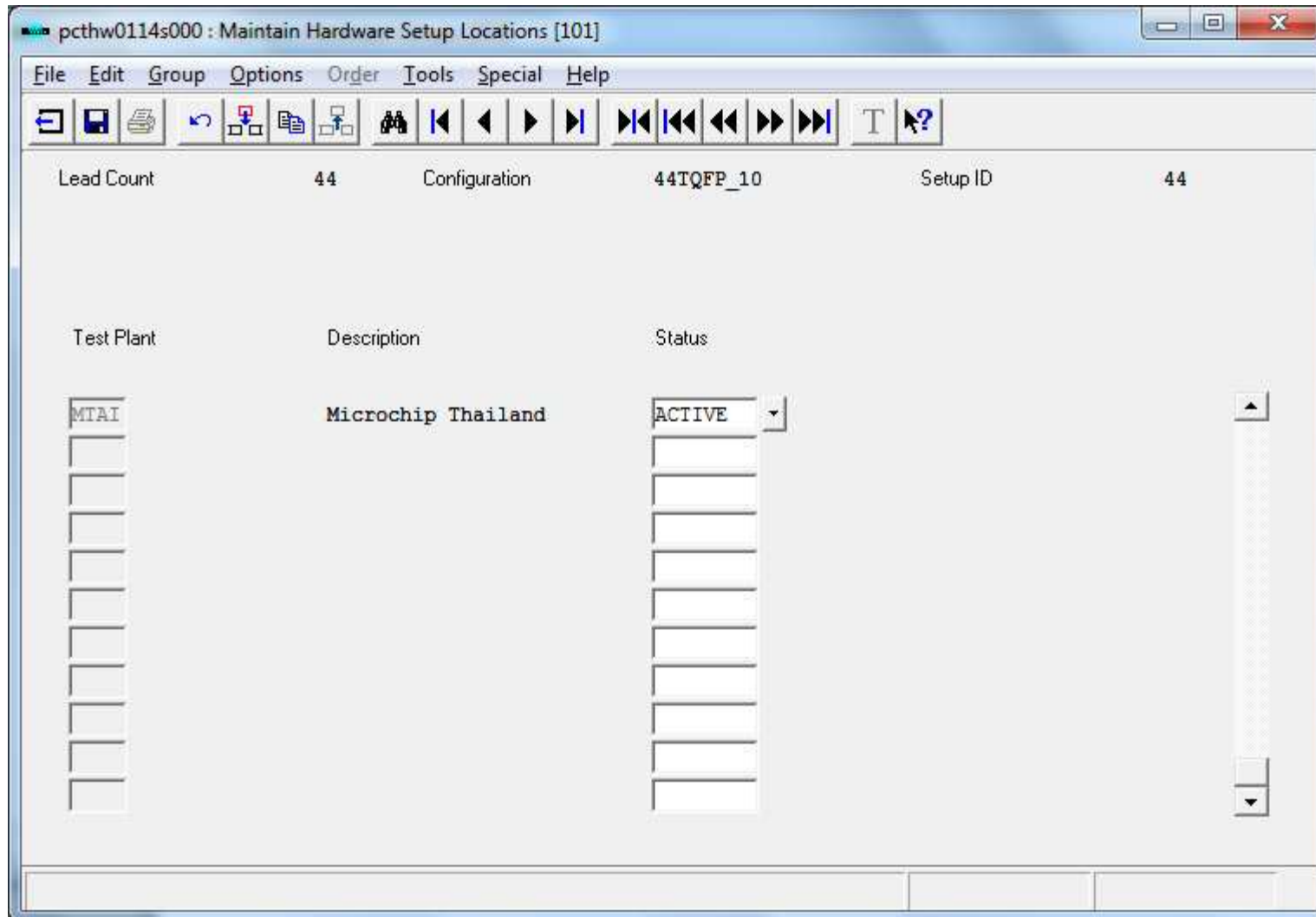
- HW Setup is consist of Tester, Handler, Loadboard, Dutboard, Mapboard, Contactor, Channelmap, Tester Interface, Cable Set,





Hardware Setup - 2

- And Test Location.





Final Test Data Collection for PDC Creation

- **Data for PDC Creation :**
 - **Mask / MPC**
 - **Package / Package Code**
 - **Tester / Handler**
 - **Test Hardware (Loadboard, Contactor,..) Spec no.(14-Axxxxx)**
 - **Test Flow & Test Temperature**
 - **Test Program Name and Test Program Option for each Test Step**
 - **Test Program Channel Map Name (if any)**
(Mapping DUT pins with Tester Pin Electronics)
 - **Handler Binning (Good/Reject, Define in the test program)**
 - **Operating System (OS) if require special version**
 - **Special Instruction (if any)**





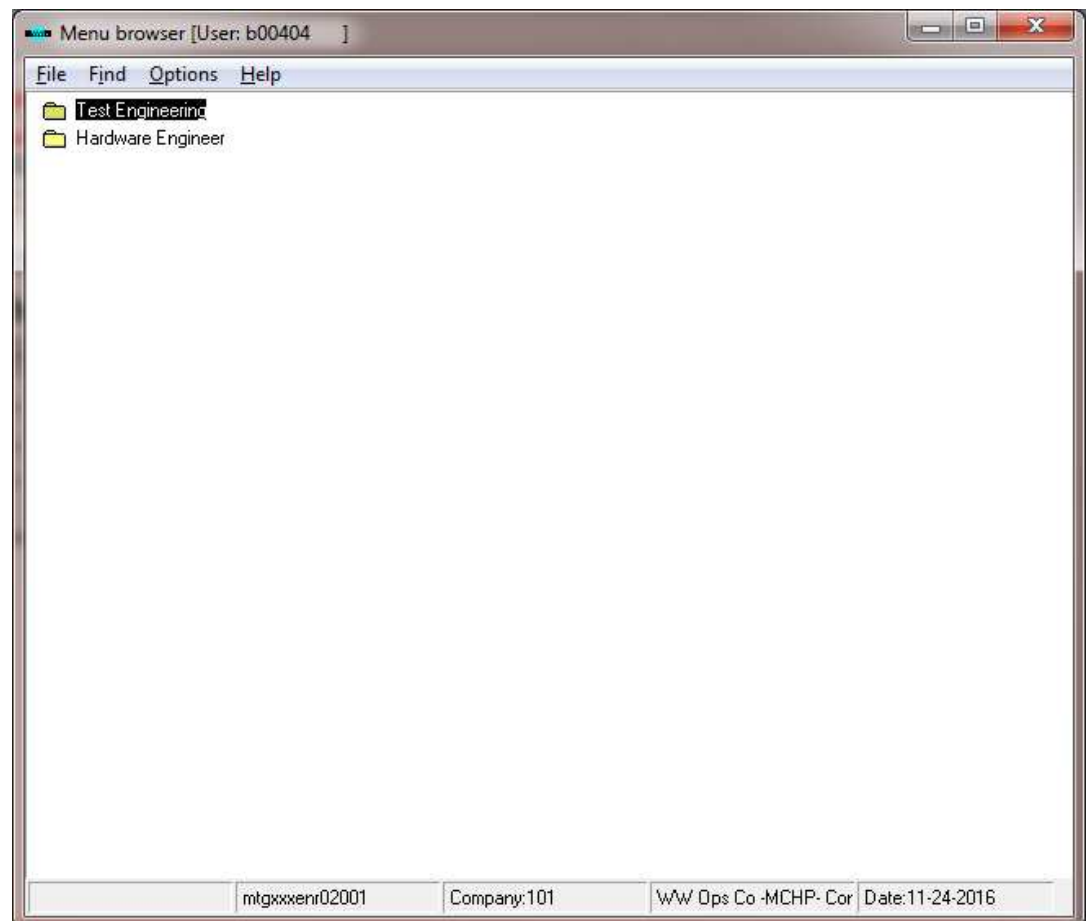
Login to Baan PDC

- Login by using PC login User name and Password.
- Then press “Enter” button or click “Connect” button.



A dialog box titled "Login for baanprod" with a close button (X) in the top right corner. It contains three input fields and three buttons:


Hostname:	baanprod	Connect
Username:	b00404	Cancel
Password:	xxxxxxxxxx	Configure





Exit From Baan PDC - 1

Caution: Do not exit by clicking "X" box at the upper right hand corner of the screen.

Exit all screens via File menu or Save and Exit button  (the button right under File menu with the blue arrow pointing the left inside a black box) to prevent license issue.

The image displays two screenshots of the Baan PDC software interface. The left screenshot shows a 'File' menu with the 'Exit' option highlighted, accompanied by a green 'Do' callout. The right screenshot shows a 'Save and Exit' button (a black box with a blue arrow pointing left) highlighted, also with a green 'Do' callout. Both screenshots have yellow 'Don't' callouts pointing to the 'X' button in the top right corner of the window.



Exit From Baan PDC - 2

From: Microchip Information Systems
Sent: Sunday, January 15, 2017 10:27 PM
To: #All WorldWide Employees
Subject: Baan licenses - please read

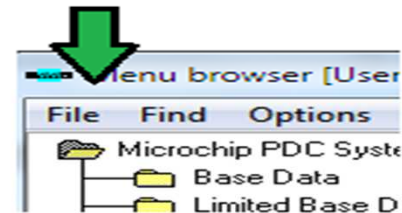
If you are not a Baan user, you may disregard this message.

Our Baan licenses are a precious and costly resource which Microchip owns. In the past few days, we have had issues with the number of users who are able to log onto the system, especially in the heavily used Asia region. Frustrations rise quickly if you need to log in and you get a message saying that the user limit has been reached.

We have enough licenses to support everyone's work if we are careful in how we manage our Baan log in activity.

Please be aware of 2 key rules for using your Baan account

1. If you are not actively working in the system, LOG OFF!
2. When you do log off, DO NOT use the red "X" in the upper right corner to close your account
 - a. ALWAYS use the File > Exit path to leave the application



We are monitoring system usage and we do have some clean-up tools for helping to remove stale accounts, but it is better to develop good Baan habits from the beginning than to chase these problems. Thank you for your help in keeping our Baan system available for everyone.

Please do not reply to this email address. Please send any questions to the Helpdesk at helpdesk@microchip.com or call 480.792.7600.





Baan Buttons

- **Frequently use buttons :**



Find/Search Function



Insert Function



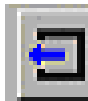
Delete Function



Bowtie Function (Select the option)



Save Function



Save and Exit Function



Copy Function



Next Function

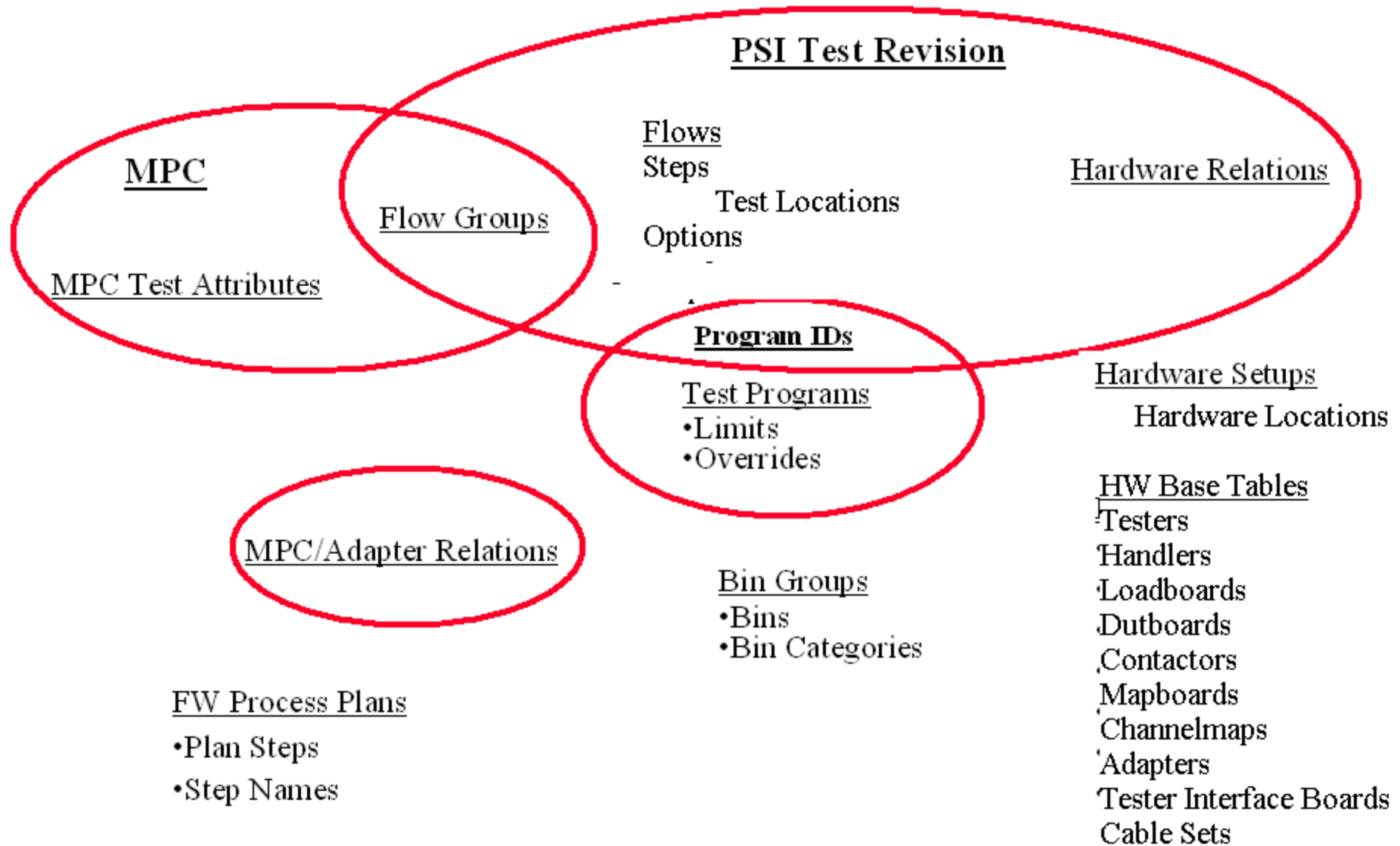


Prior Function





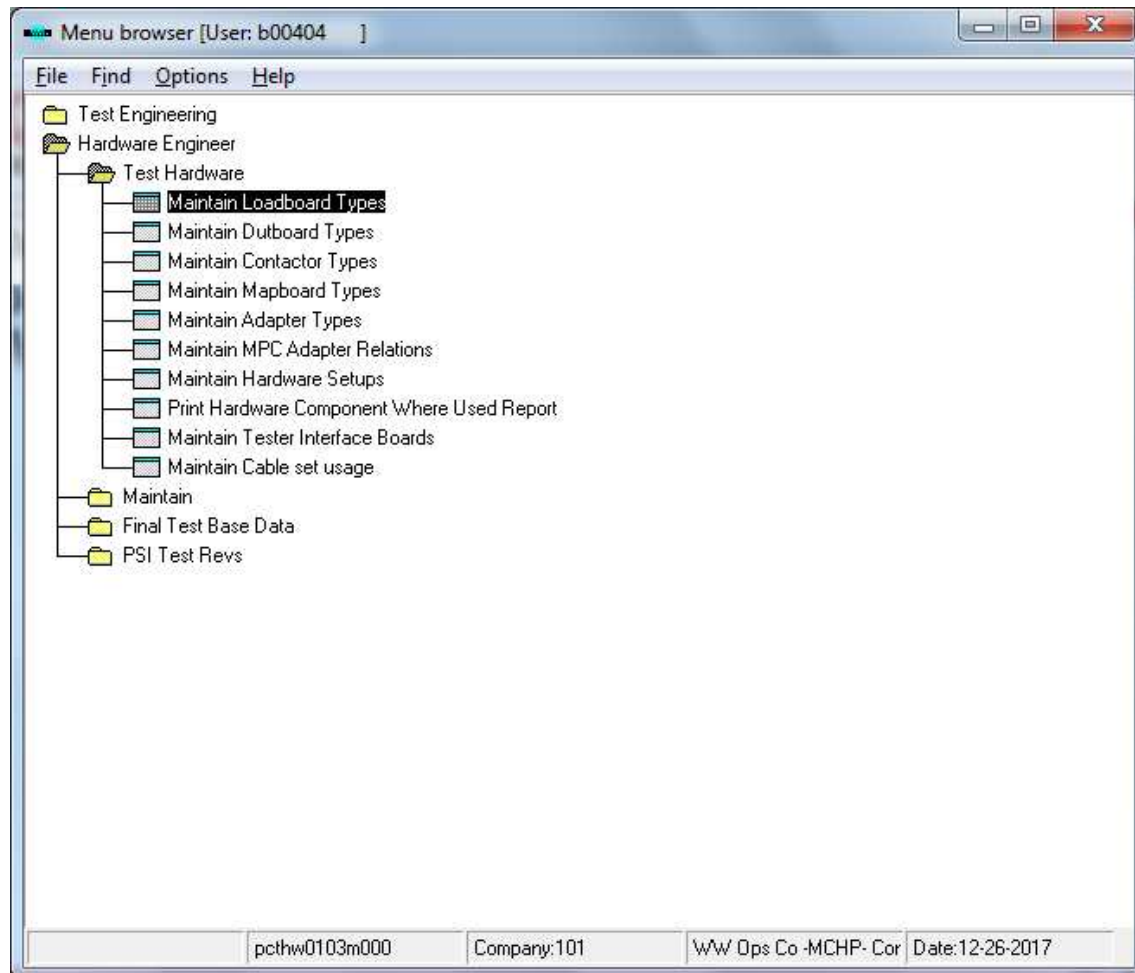
PDC Data Structure






Add Loadboard Type - 1

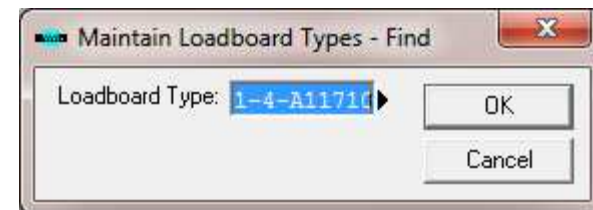
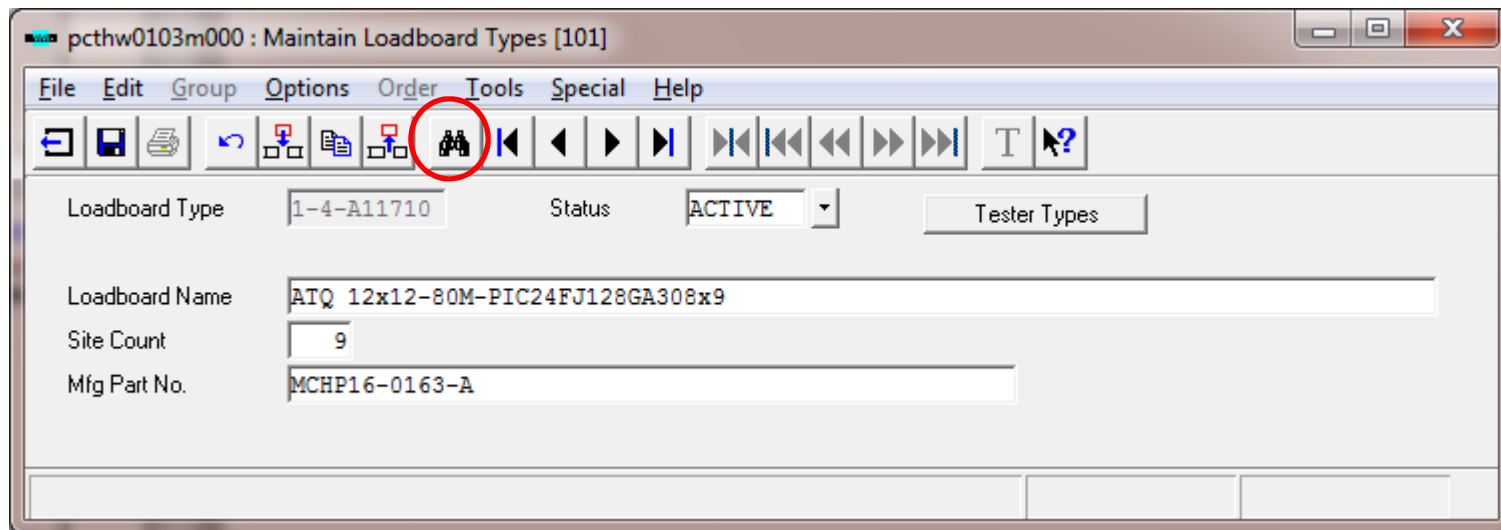
- Double clicks “Hardware Engineer”.
- Double clicks “Test Hardware” then “Maintain Loadboard Types”.





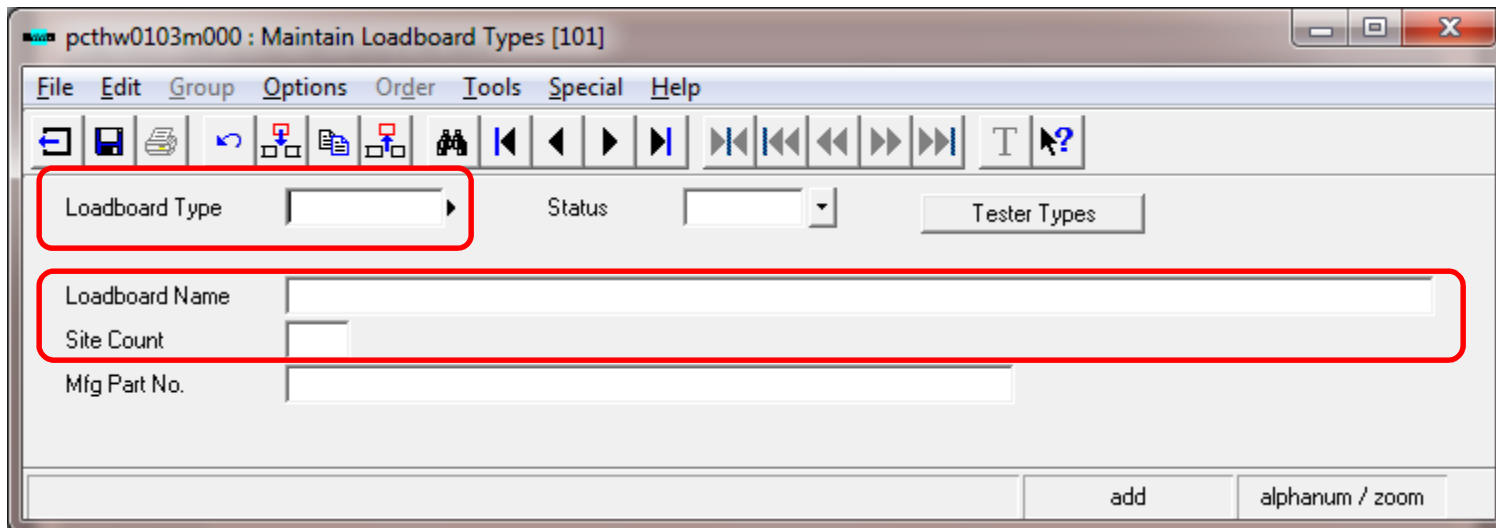
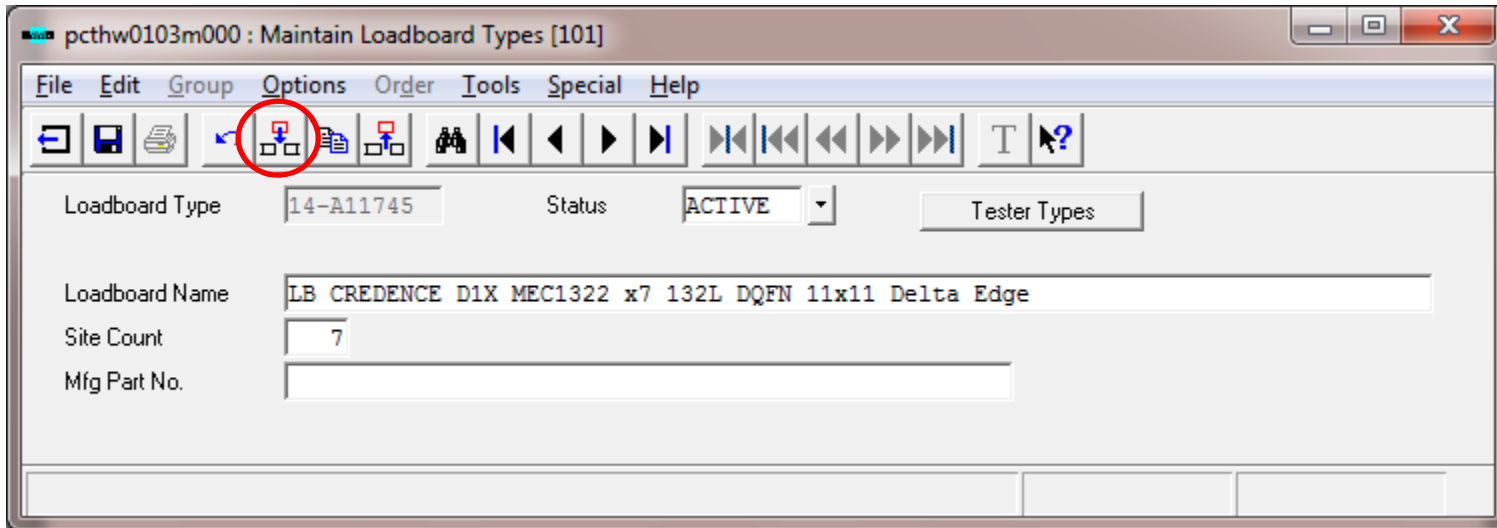
Add Loadboard Type - 2

- Click Find button ( binoculars symbol)
- Input 14-Axxxxx Loadboard spec no. then press “Enter” button or click “OK” to verify if it has already existed or not. If not, it will go to the nearest Loadboard spec no.






Add Loadboard Type - 3





Add Loadboard Type - 4

- Click  Insert button.
- Input 14-Axxxxx Loadboard spec no. at Loadboard Type field.
- Input Loadboard Name (120 Char. Max) per Title of DMS data or Description of Hardware Database System (HDS).

DMS Data

Drag a column header and drop it here to group by that column

Detail	Type	Status	Spec Num	Revision	Title	Originator	Owner	Part Number	Incorporated CN
	<input type="text"/>	<input type="text"/>	<input type="text" value="14-A11742"/>	<input type="text"/>	<input type="text"/>	<input type="text"/>	<input type="text"/>	<input type="text"/>	<input type="text"/>
View Detail	14	Current	14-A11742	A	ST CREDENCE DIAMOND D2X PIC18F66J16/67J11 x56 64L TQFP 10x10x1 MCT-TAPESTRY	Siam Pungmaneesakul - B04711	Siam Pungmaneesakul - B04711	MCHP16-0199-A	CN_14006870

HDS Data

Drag a column header and drop it here to group by that column

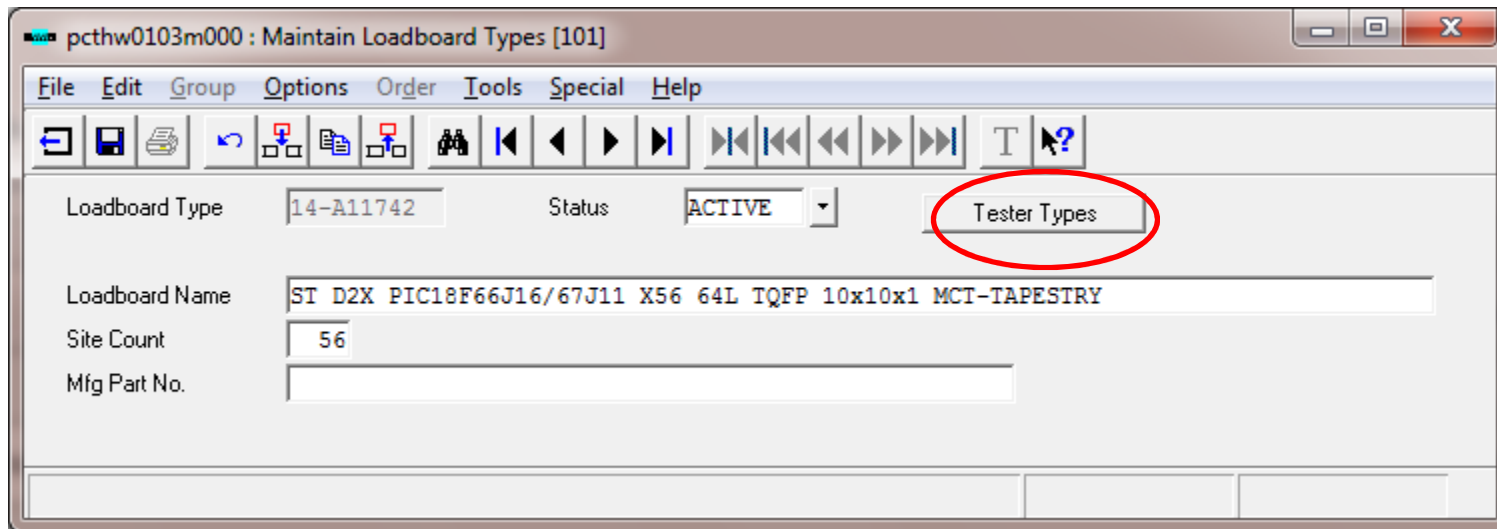
Job Number	Spec Number	Description	Board Type	Priority	Job Type	Status
<input type="text"/>	<input type="text" value="14-A11742"/>	<input type="text"/>	<input type="text"/>	<input type="text"/>	<input type="text"/>	<input type="text"/>
<u>ST-18399</u>	14-A11742	ST CREDENCE DIAMOND D2X PIC18F66J16/67J11 x56 64L TQFP 10x10x1 MCT-TAPESTRY	ST	2	StripTest	RTE/Complete








Add Loadboard Type - 5

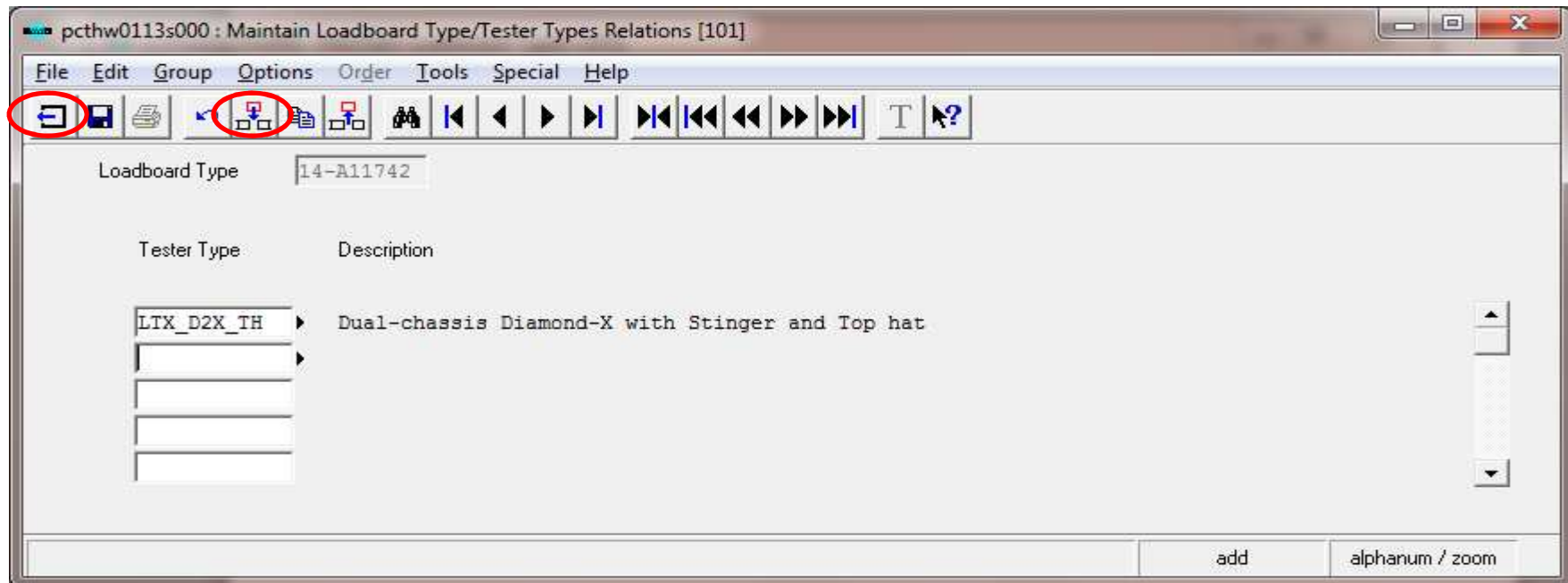
- Input Site Count (Number of maximum sites of the Loadboard).
- Click “Tester Types” button.





Add Loadboard Type - 6

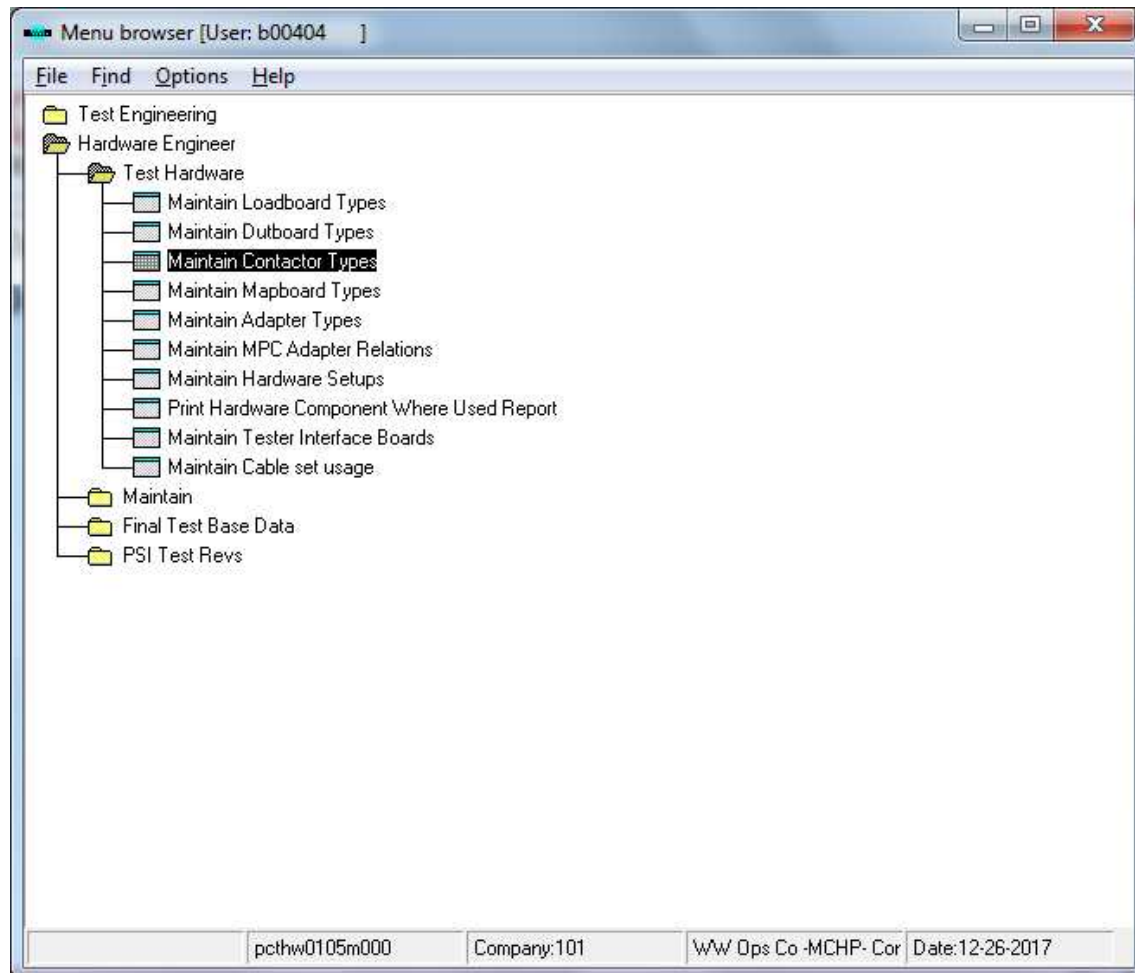
- Click  Insert button. Select Tester Type or Type it in.
Note : See the list of Tester Types by moving mouse to the triangle sign. Click mouse when it changes to magnifier.
- Click  “Save & Exit” button to go back to the 1st page.
- Click  “Save & Exit” button again to go back to “Hardware Engineer” screen.






Add Contactor Type - 1

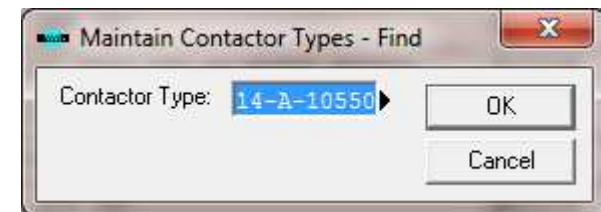
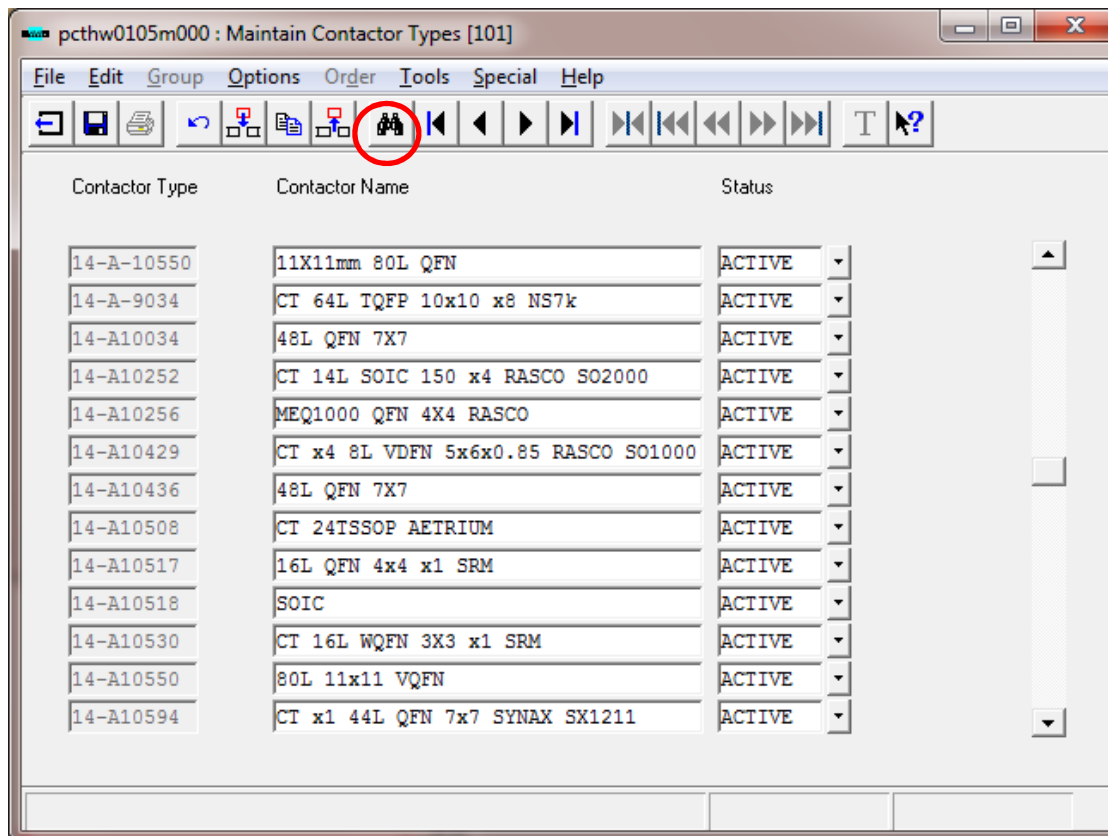
- Double clicks “Hardware Engineer”.
- Double clicks “Test Hardware” then “Maintain Contactor Types”.






Add Contactor Type - 2

- Click Find button ( binoculars symbol)
- Input 14-Axxxxx Contactor spec no. then press “Enter” button or click “OK” to verify if it has already existed or not.
If not, it will go to the nearest Contactor spec no.





Add Contactor Type - 3

- Click  Insert button.
- Input 14-Axxxxx Contactor spec no. at “Contactor Type” field.
- Input Contactor Name (120 Char. Max) per Title of DMS data or Description of Hardware Database System (HDS).

DMS Data

Drag a column header and drop it here to group by that column

Detail	Type	Status	Spec Num	Revision	Title	Originator	Owner	Part Number	Incorporated CN
	<input type="text"/>	<input type="text"/>	<input type="text" value="14-A7303"/>	<input type="text"/>	<input type="text"/>	<input type="text"/>	<input type="text"/>	<input type="text"/>	<input type="text"/>
View Detail	14	Current	14-A7303	A	CT x56 64L TQFP 10X10 MCT-TAPESTRY	Murriel Natiola - A04734	Valen Burd - C10844		CN_14002107

HDS Data


Drag a column header and drop it here to group by that column

Job Number	Spec Number	Description	Board Type	Priority	Job Type	Status
<input type="text"/>	<input type="text" value="14-A7303"/>	<input type="text"/>	<input type="text"/>	<input type="text"/>	<input type="text"/>	<input type="text"/>
<u>CT-13938</u>	14-A7303	CT x56 64L TQFP 10x10x1 MCT-TAPESTRY	CT	0	Contactor	Closed





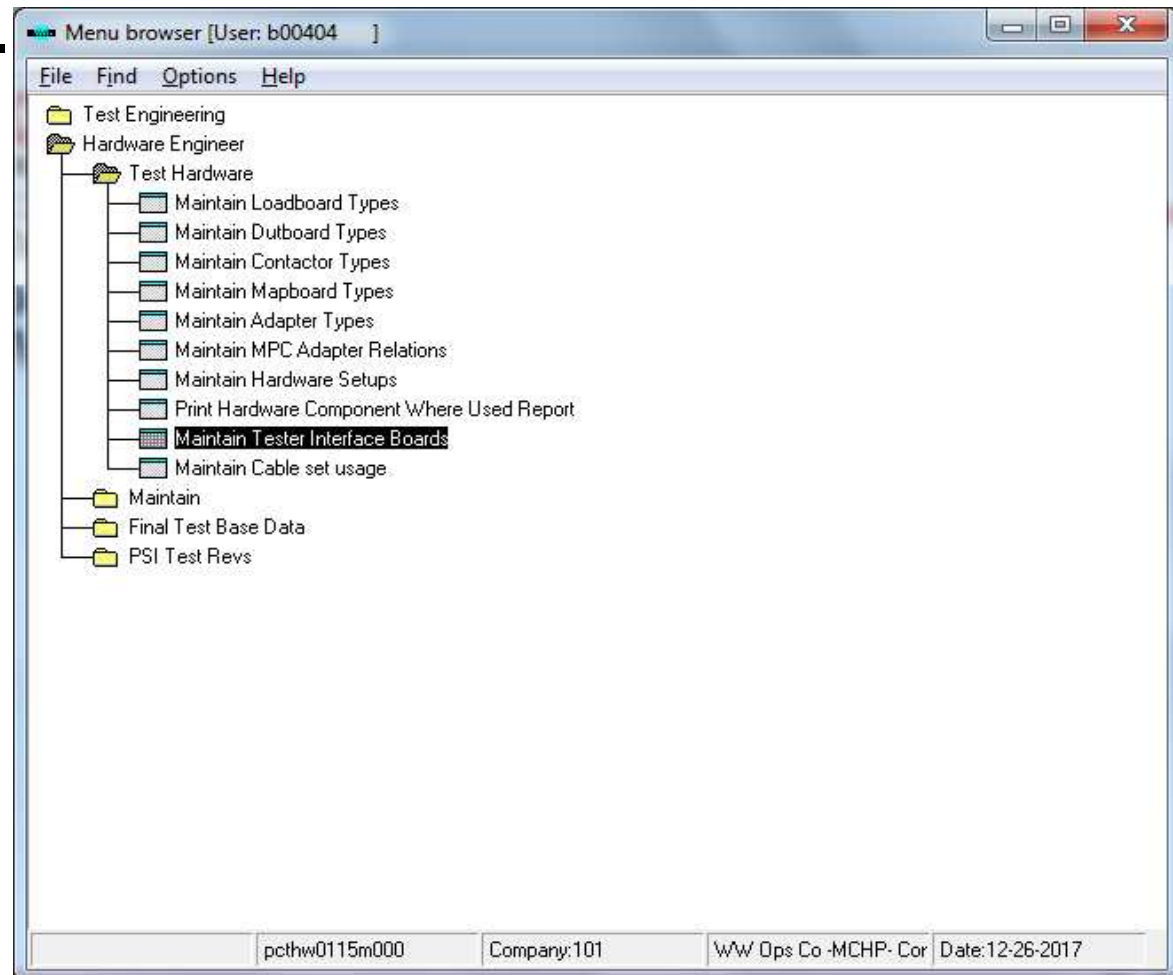
Add Contactor Type - 4

- Press “Tab” button to move the cursor to “Status” field.
The default value is ACTIVE.
- Click  “Save & Exit” button to go back to “Hardware Engineer” screen.



Add Tester Interface Board - 1

- Double clicks “Hardware Engineer”.
- Double clicks “Test Hardware” then “Maintain Tester Interface Boards”.





Add Tester Interface Board - 2

- Input Tester Type, Tester Interface (14-Axxxxx) and Description (120 Char. Max) per Title of DMS data.

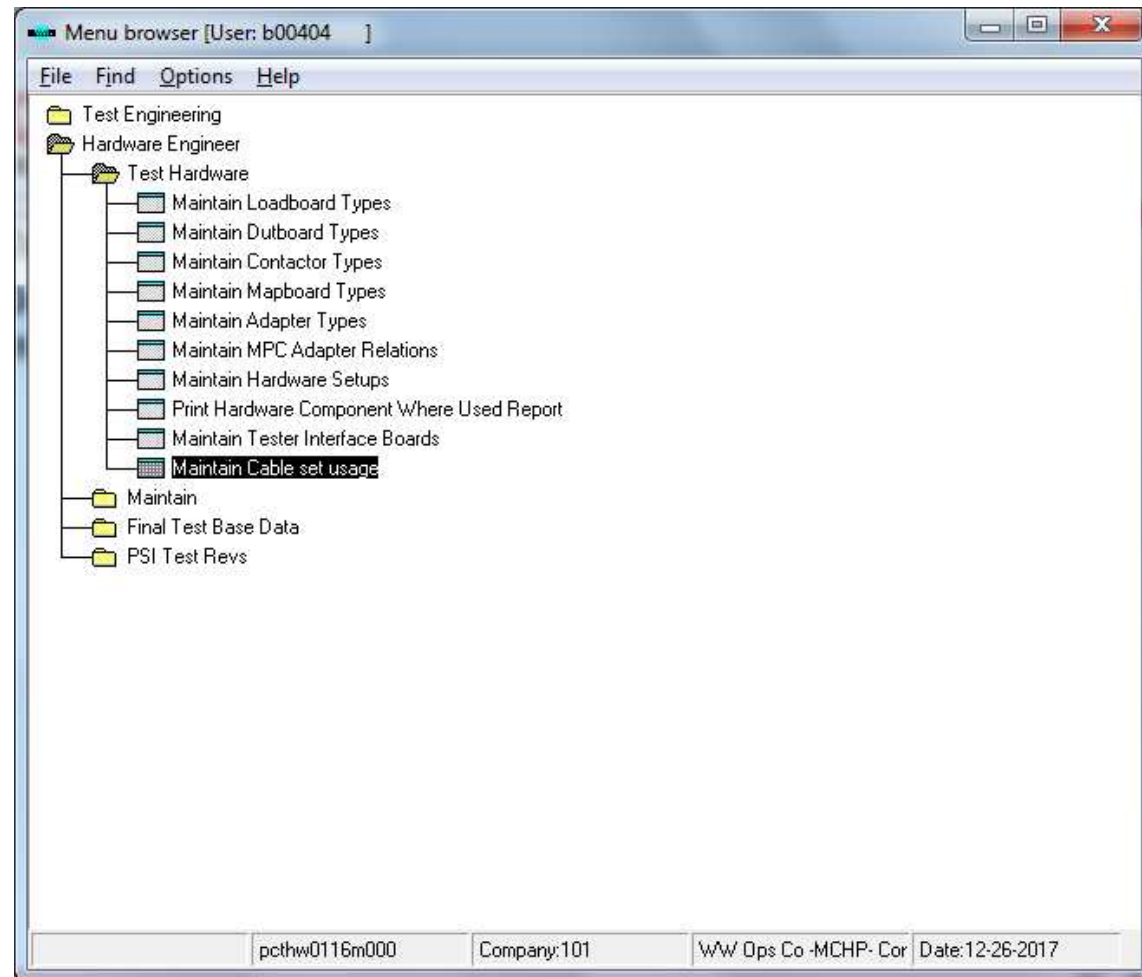
The screenshot shows a software window titled "pcthw0115m000 : Maintain Tester Interface Boards [101]". The window has a menu bar with "File", "Edit", "Group", "Options", "Order", "Tools", "Special", and "Help". Below the menu bar is a toolbar with various icons for file operations and navigation. The main area of the window is a table with the following columns: "Tester Type", "Tester Interface", "Description", and "Status". The table is currently empty, with 10 rows visible. A vertical scrollbar is on the right side of the table. At the bottom of the window, there is a "first" button.

Tester Type	Tester Interface	Description	Status



Add Cable set usage - 1

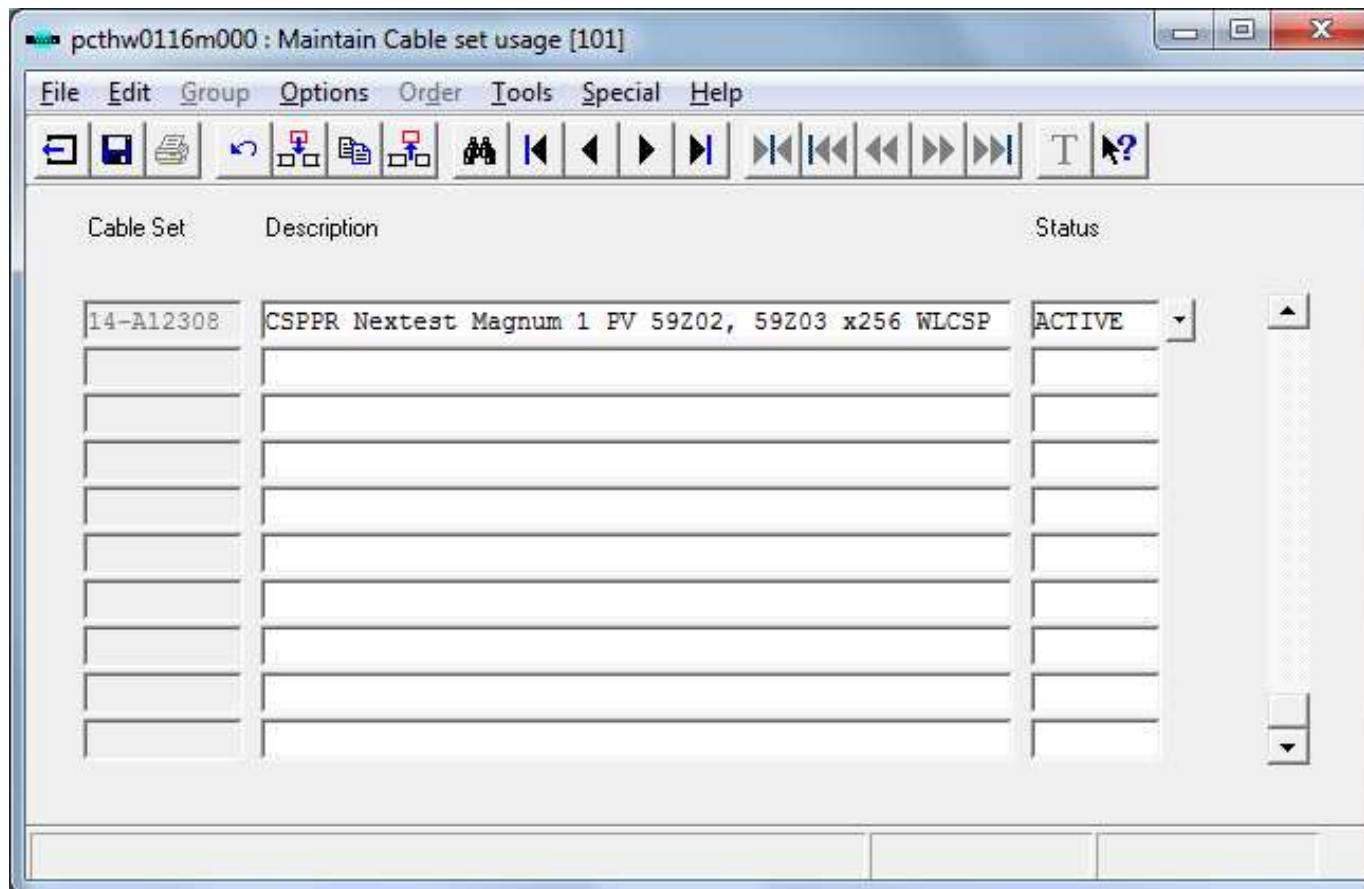
- Double clicks “Hardware Engineer”.
- Double clicks “Test Hardware” then “Maintain Cable set usage”.





Add Cable set usage - 2

- Input Cable Set (14-Axxxxx) and Description (120 Char. Max) per Title of DMS data.





Create Hardware Setup ID - 1

- Hardware Setup data in PDC will be shown at each test step of Test Setup Options (Per package code of the MPC).
- If “Prog HW Limits” of the Program ID has been used, it will show only the assigned Setup ID.

FT1@IN25C

Tester Type: J750

Program ID:114137

Legacy Op Num:

1) Tester Models

- J750_1024
- J750_128
- J750_256
- J750_512
- J750_640
- J750_832
- J750_MSO_128
- J750_MSO_256
- J750_SCAN
- J750_SCAN_448
- J750_SCAN_512

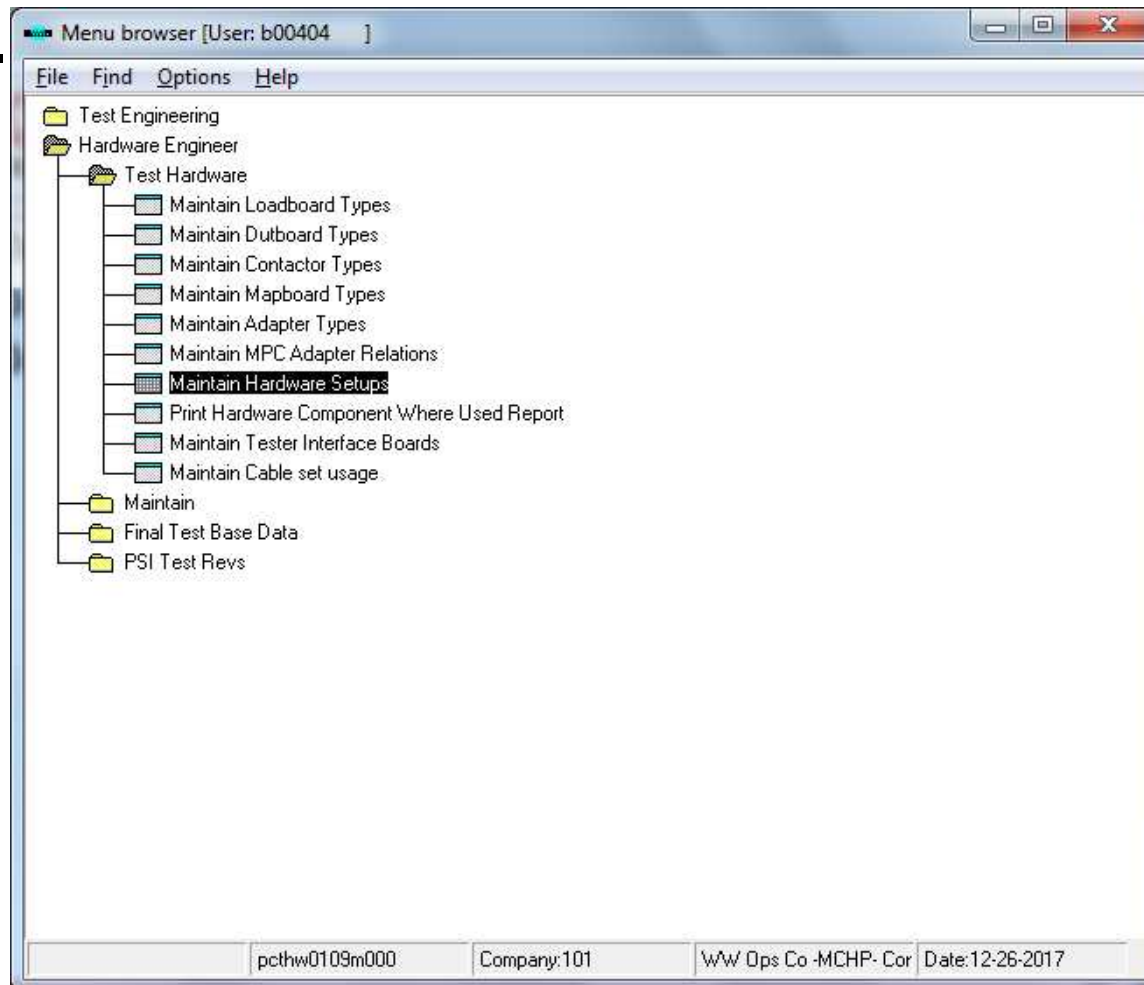
2) Hardware Setups for 44/44TQFP_10 (T4X)

	SETUP ID	HANDLR. CHANNELMAP	LOADBOARD ADAPTER	CONTACTOR TESTER INTERFACE BOARD	DUTBOARD CABLE SET	MAPBOARD H/W LOCATION
<input type="checkbox"/>	44	CAS x3dlc44tqfp_128p	14-A3100	14-A1961		MTAI,
<input type="checkbox"/>	45	CAS x4dlc44tqfp_256p	14-A3100	14-A1961		MTAI,
<input type="checkbox"/>	63	SCH x20mct44tqfp	14-A4254	14-A2834		MTAI,




Create Hardware Setup ID - 2

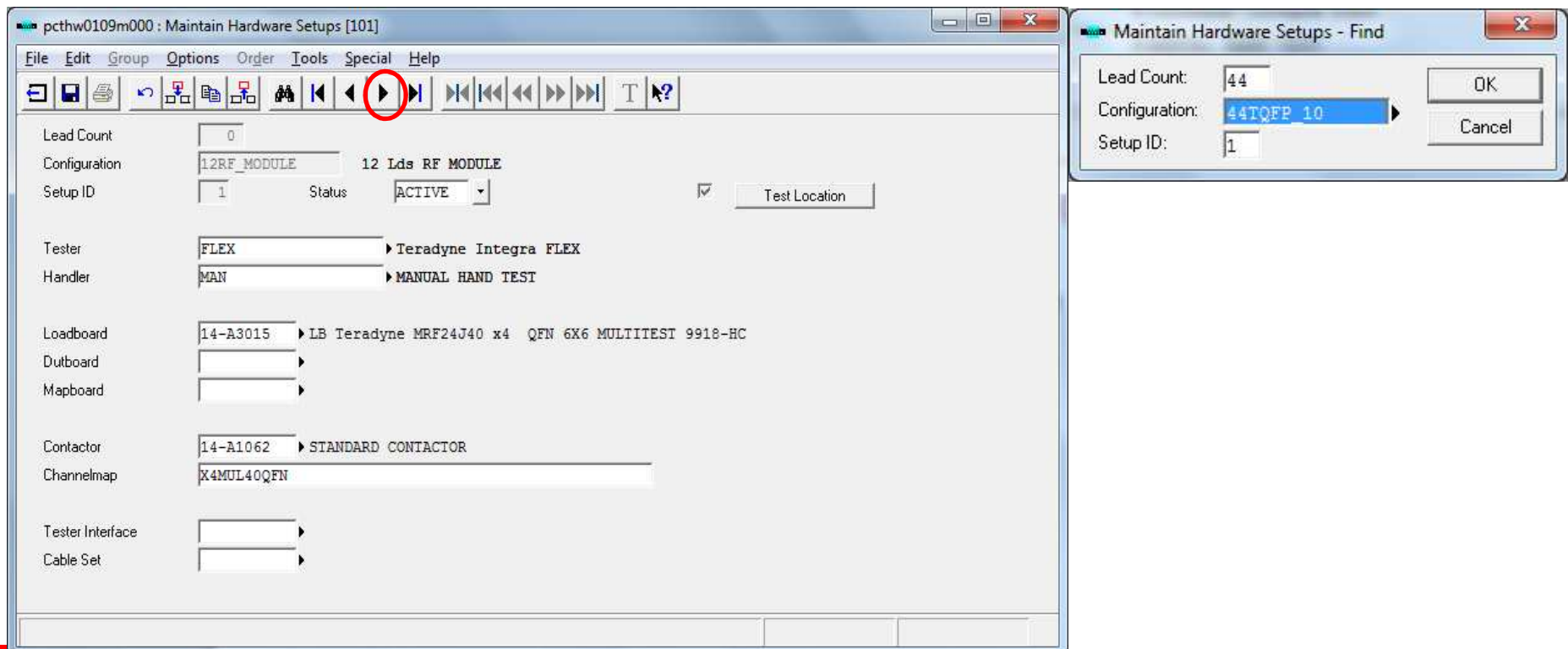
- Double clicks “Hardware Engineer”.
- Double clicks “Test Hardware” then “Maintain Hardware Setups”.






Create Hardware Setup ID - 3

- Click Find button ( binoculars symbol).
- Input Lead Count and Configuration then press “Enter” button or click “OK” button.
- It will go to the 1st Setup ID. Click Next button to see if there is the Hardware Setup ID you needed.





Create Hardware Setup ID - 4

- If there is no Hardware Setup ID you needed.
- Click  Insert button to create new Hardware Setup ID.
- Input Lead Count, Configuration (handler kit), Tester, Handler.



Configuration

pcthw0109m000 : Maintain Hardware Setups [101]

File Edit Group Options Order Tools Special Help

Lead Count 0

Configuration

Setup ID 999 Status ACTIVE

Tester

Handler

Loadboard

Dutboard

Mapboard

Contactor

Channelmap

Tester Interface

Cable Set

Test Location

add numeric



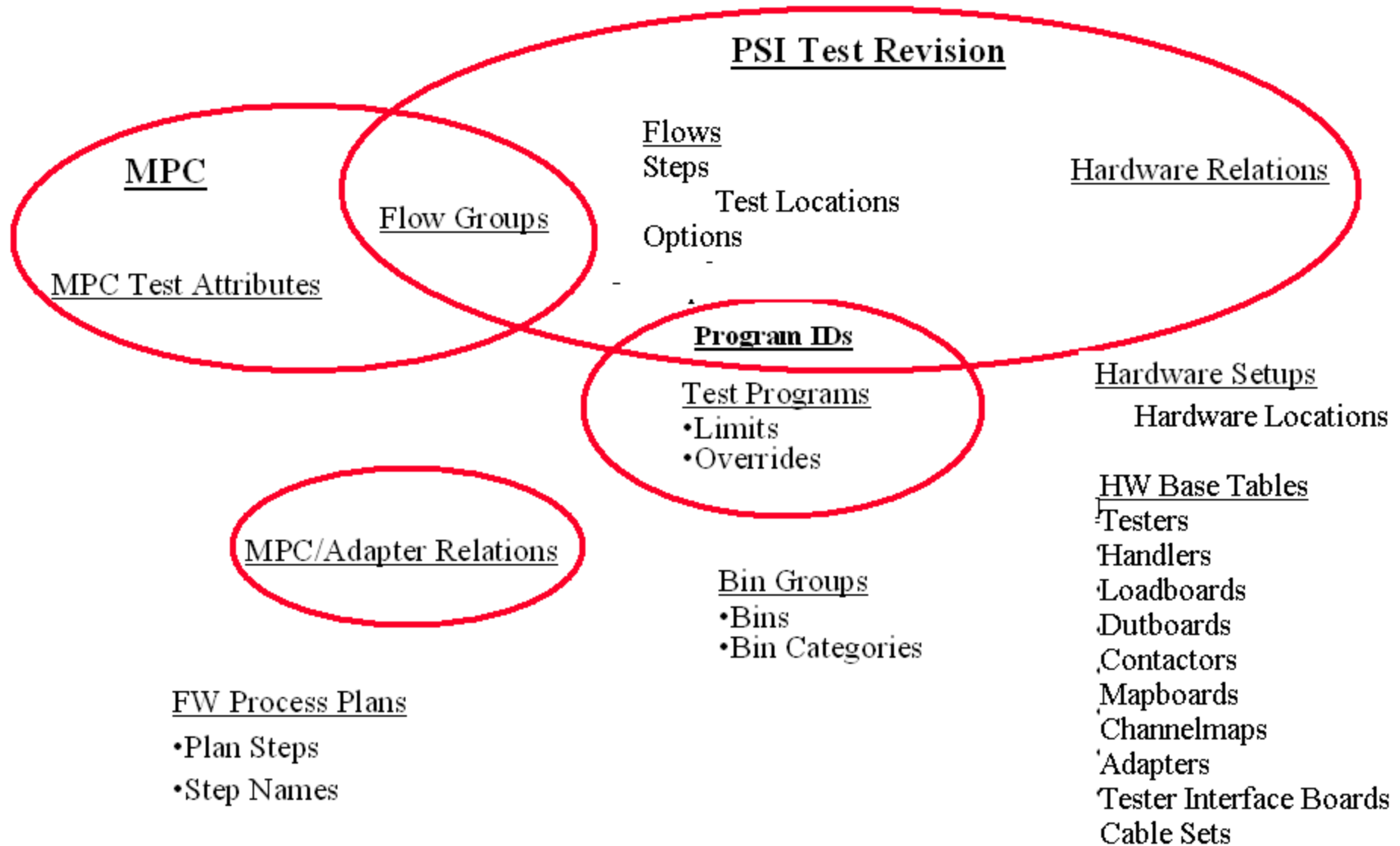
Create Hardware Setup ID - 5

- Input Load Board, DUT Board, Map Board, Contactor, Channel Map, Tester Interface Board, Cable Set.  Tester Type
- Note : **Not need to input all of them, depends on Tester Type.**
- Click “Test Location” button to add Test Plant.  Test Plant
- Click  “Save & Exit” button after finished inputting all necessary data to new Hardware Setup ID.
- In case that you can't find Configuration (Package) you needed, please inform **Soravorn Pochpring - B00022** (MThai IE) to create it.





PDC Data Structure





Add New Bin Groups - 1

- List of available Bin Groups as of Jan 4, 2018



Bin Groups

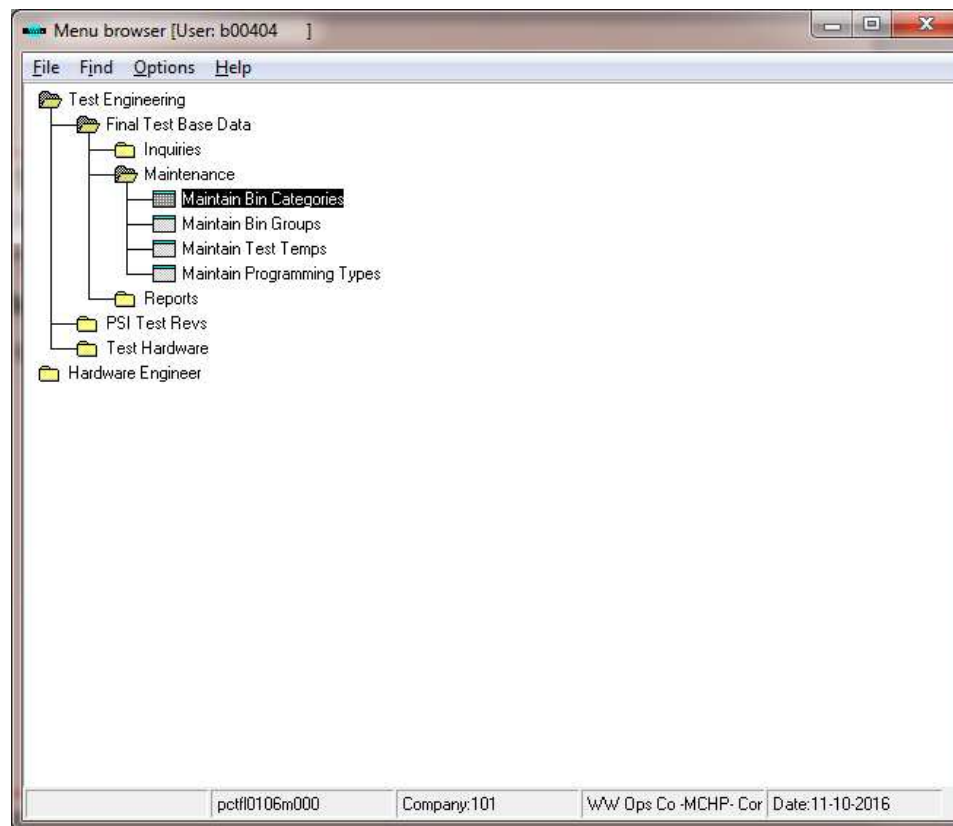
The screenshot shows a software window titled "pctf0104m000 : Maintain Bin Groups [101]". The window has a menu bar with "File", "Edit", "Group", "Options", "Order", "Tools", "Special", and "Help". Below the menu bar is a toolbar with various icons for file operations and navigation. The main area contains a table with the following columns: "Bin Group", "Description", "Bin Group Type", and "Status". A "Bin Details" button is located to the right of the table. The table lists 15 bin groups, all with a "PHYSICAL" type and "ACTIVE" status.

Bin Group	Description	Bin Group Type	Status
ATML ARM-NT01	1-GOOD, 5-O/S, 6-PA/FU	PHYSICAL	ACTIVE
ATML ARM-RF01	1-GOOD, 2-PA, 3-FU, 4-O/S	PHYSICAL	ACTIVE
ATML ARM-RF02	1-GOOD, 2-PA, 3-FU, 4-O/S, 5-Analog	PHYSICAL	ACTIVE
ATML ARM-RF03	1-GOOD, 2-PA, 3-FU, 4-O/S, 5-Analog,	PHYSICAL	ACTIVE
ATML ARM-RF04	1-GOOD, 2-PA, 3-FU, 4-O/S, 5-Analog,	PHYSICAL	ACTIVE
ATML ARM-RF05	1-GOOD, 2-PA, 3-FU, 4-IDD, 5-EEPROM	PHYSICAL	ACTIVE
ATML CPLD1	1-GOOD, 5-PA/FU, 6-CONT/LEAK	PHYSICAL	ACTIVE
ATML CRYPTO1	1-GOOD, 2-O/L VOL/H, 3-PA/FU, 4-	PHYSICAL	ACTIVE
ATML NTO NPI1	1-GOOD, 2-GOOD-BIN2, 3-GOOD-BIN3	PHYSICAL	ACTIVE
ATML NT01	1-GOOD, 2-GOOD-B2, 3-GOOD-B3, 4-GOO	PHYSICAL	ACTIVE
ATML RFA1	2-GOOD, 1-PA/FU, 3-NON RETEST, 4-	PHYSICAL	ACTIVE
ATML SEEPROM 1	1-GOOD, 2-GOOD Normal W, 5-O/S,	PHYSICAL	ACTIVE
ATML SEEPROM 2	1-GOOD, 7-O/S, 8-OTHER	PHYSICAL	ACTIVE




Add New Bin Groups - 2

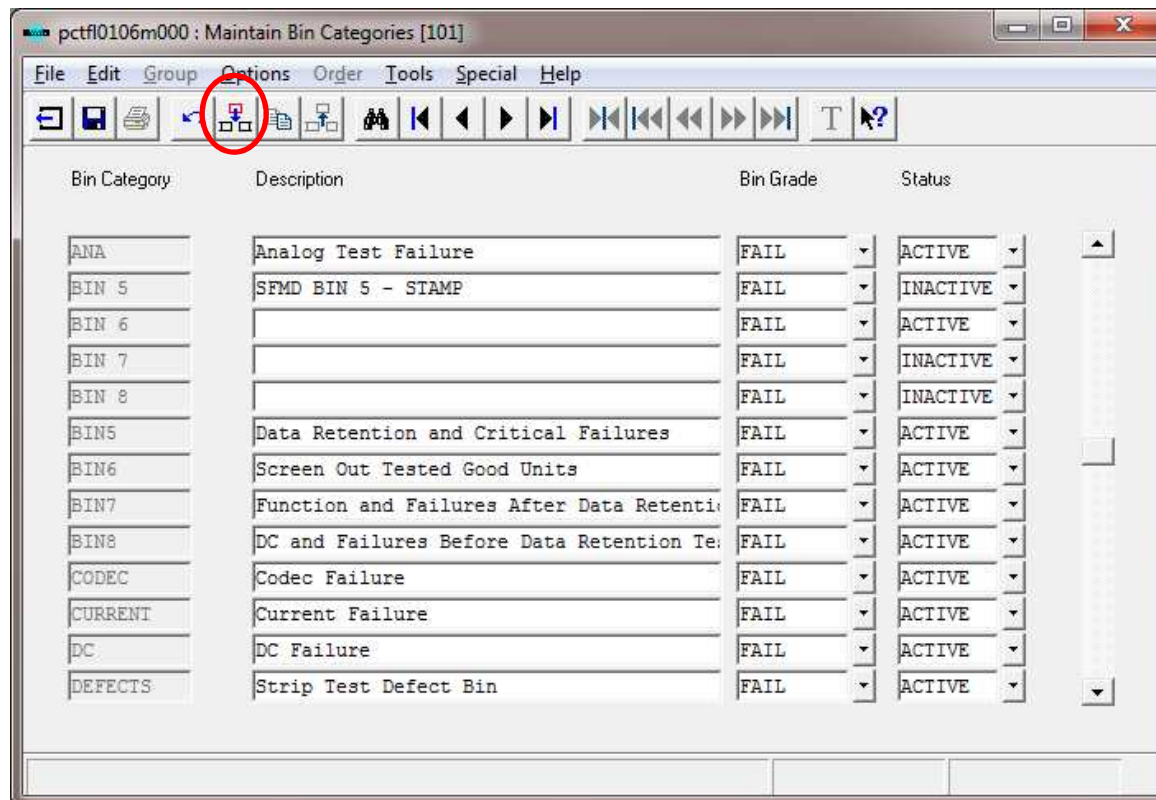
- If no existing Bin Group you can use.
- Under “Test Engineering”, Double clicks “Final Test Base Data” then Double clicks “Maintenance”.
- Double clicks “Maintain Bin Categories”.





Add New Bin Groups - 3

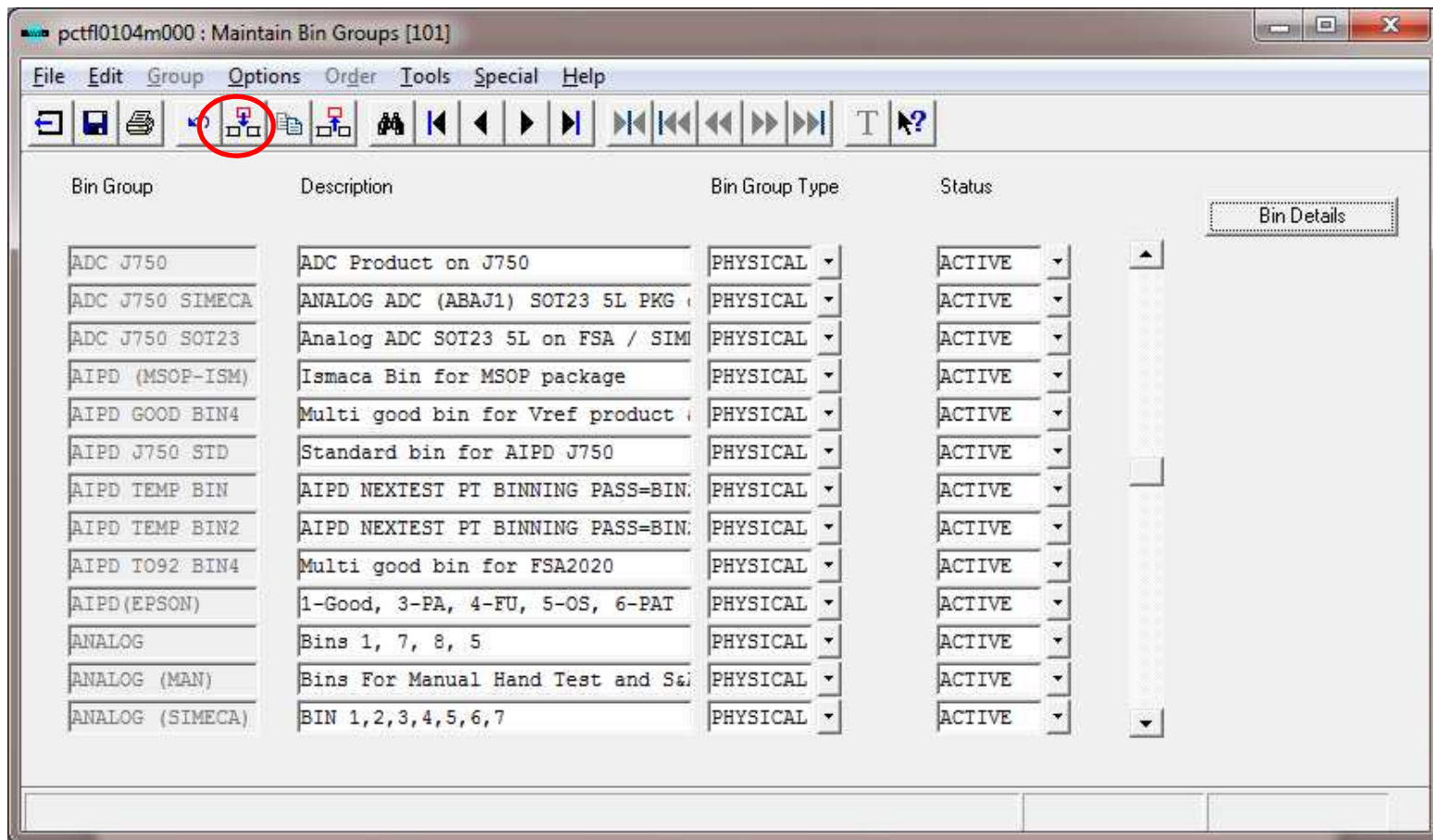
- Look for Bin Categories you needed.
- If you can't find the one you needed, click  button to add new Bin Category.
- Note : **Can't delete any Bin Category, only change it to be INACTIVE.**






Add New Bin Groups - 5

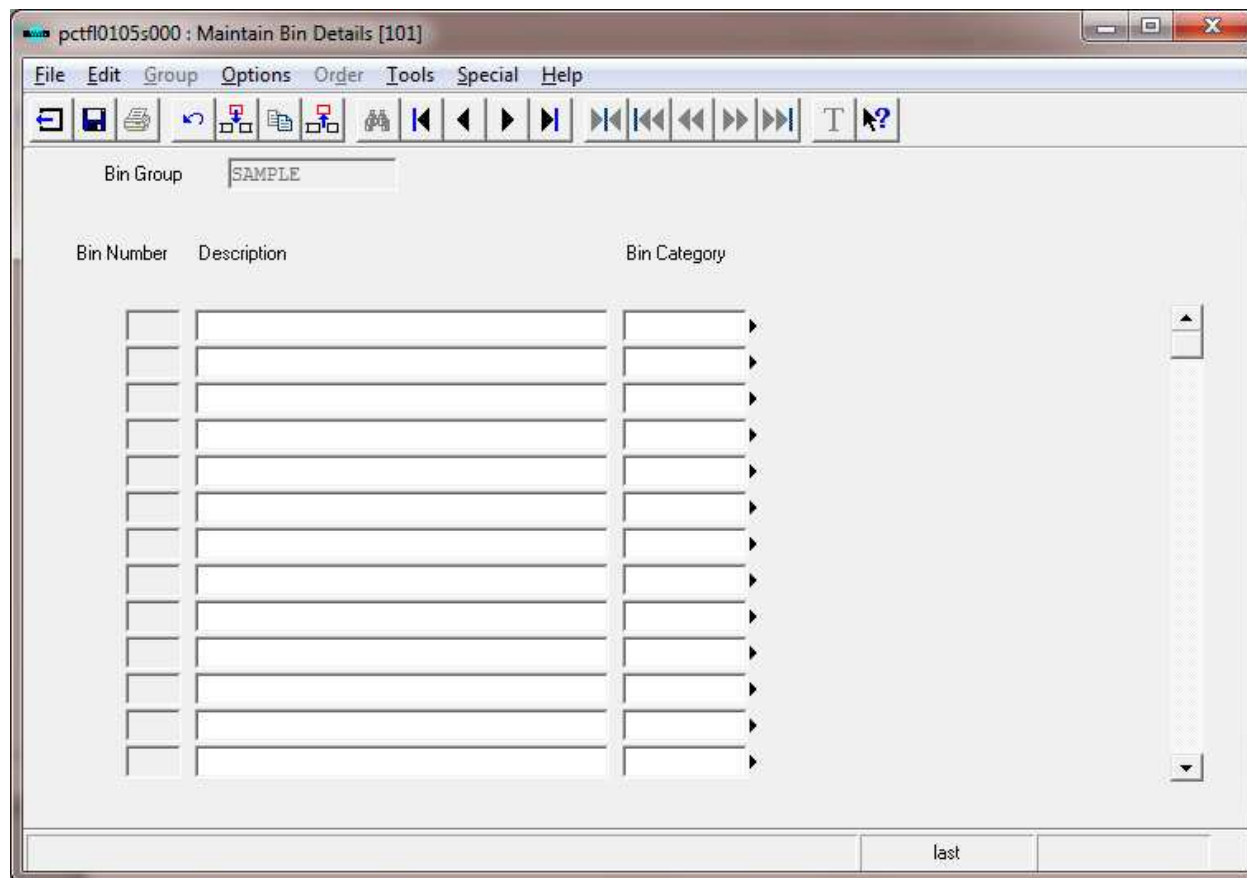
- Double clicks “Maintain Bin Groups”.
- Click  button to add new Bin Group.





Add New Bin Groups - 8

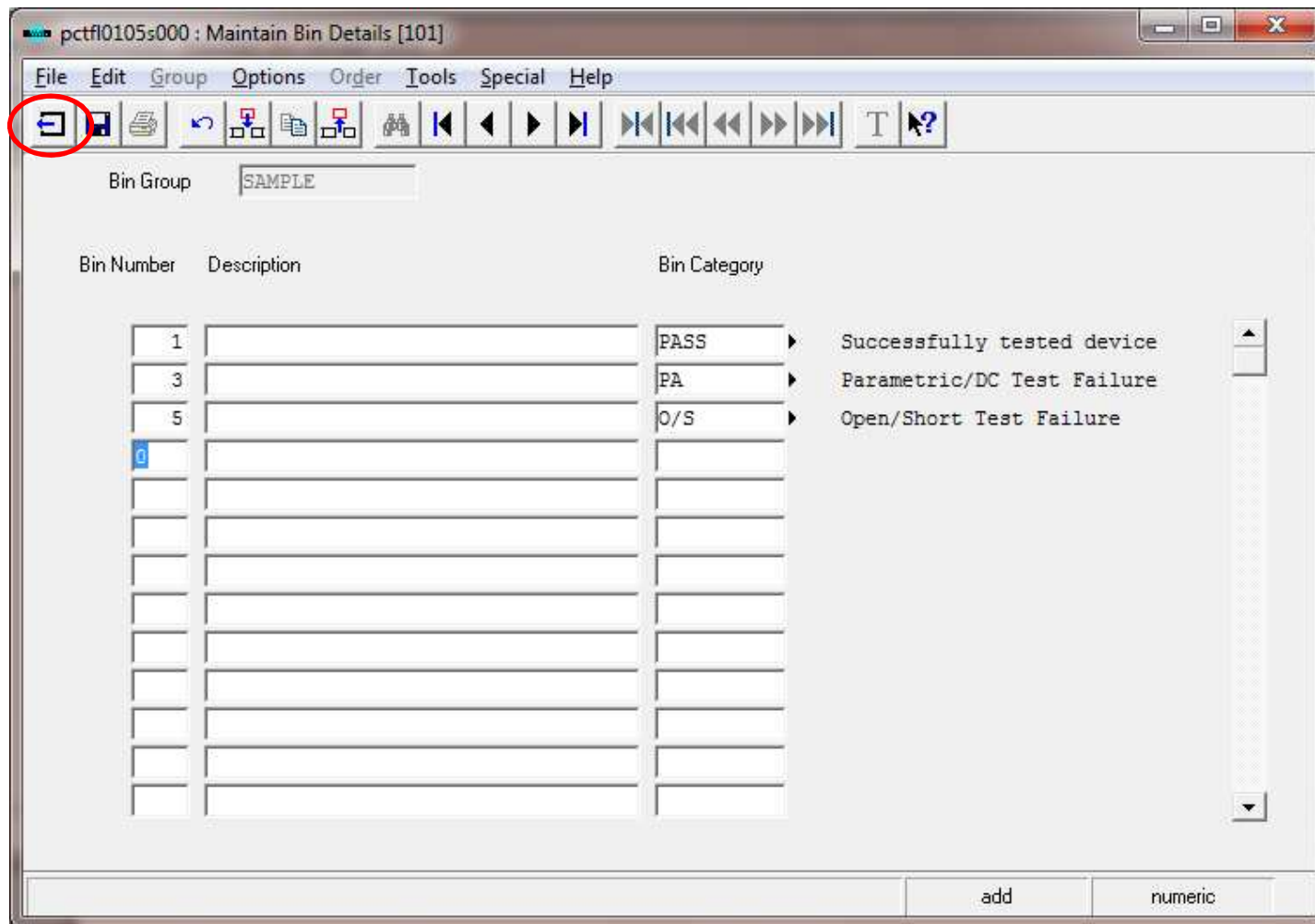
- Click  Insert button to add Bin Number
- Press Tab button twice and select Bin Category.
- Add next Bin Number until complete all of them.





Add New Bin Groups - 9

- Click  Save and Exit button.









Create Test Program ID - 1

- Test Program ID data in PDC will be shown at Program Options of Test Setup Options.
- Those data will be shown at Main Source, Checksum, Executable Name, Bins, OS, Special Instructions, Comments.

ID: 114137 Rev: AX Ver: 0 Status: ACT

		Verified
Main Source:	LEAR0_FT_A48e.xls	<input type="checkbox"/>
Checksum:	16540E2	<input type="checkbox"/>
Executable Name:	ft-qtp-std 	<input type="checkbox"/>
Part Number:	18F46K20 	<input type="checkbox"/>
Temperature:	IN25C 	<input type="checkbox"/>
Programming Type:	QTP 	<input type="checkbox"/>
Bins:	2-PASS, 3-PA, 4-FU, 5-O/S	<input type="checkbox"/>
Hardware Limits:		<input type="checkbox"/>
Correl. Process Code:		<input type="checkbox"/>
Correlation Good Bin:	0	<input type="checkbox"/>
OS Version		<input type="checkbox"/>
Special Instructions:		<input type="checkbox"/>
CN Number:	1700910	
Comments:		





Create Test Program ID - 2

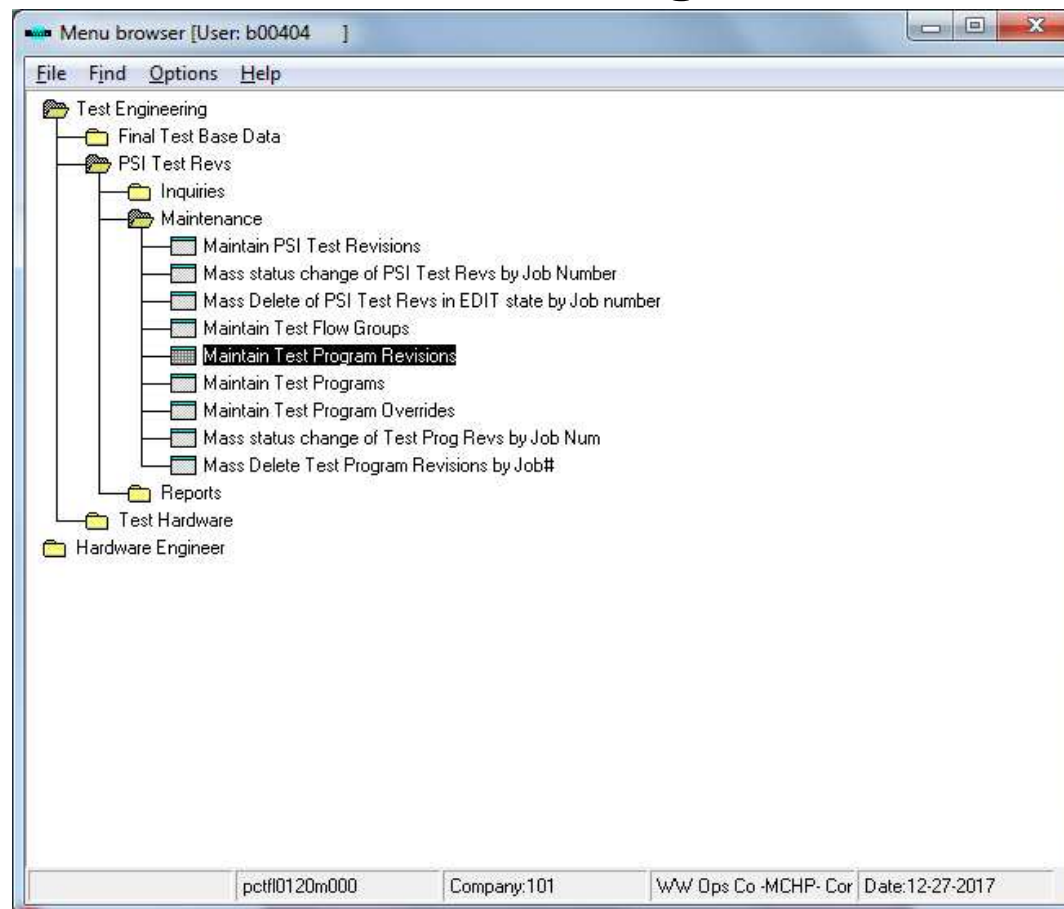
- Review Test Program Name and Test Program Option of each test step to minimize the number of Program IDs to be created.
- **Example : One Mask has 2 Part Numbers (A & B)**
 - Test Flow for Part A : FT 110C → FT 25C → QC 25C
 - FT 110C & FT 25C : Use the same TP & TP Option
Need only 1 Program ID for these 2 steps ([Program ID1](#)).
 - QC 25C uses the same TP but different TP Option
Need another Program ID for this step ([Program ID2](#)).
- Test Flow for Part B : FT 110C → FT 25C → QC 25C
FT 110C uses the same TP & TP Option as FT 110C of Part A so, can use [Program ID1](#).
- FT 25C, QC 25C use the same TP as Part A but different TP Option.
Need to create 1 Program ID for FT 25C ([Program ID3](#))
Need to create 1 Program ID for QC 25C ([Program ID4](#))





Create Test Program ID - 3

- Under “Test Engineering”, Double clicks “PSI Test Revs”.
- Double clicks “Maintenance”.
- Double clicks “Maintain Test Program Revisions”.





Create Test Program ID - 4

- Click  Insert button to create new Program ID.

pctf0120m000 : Maintain Test Program Revisions [101]

File Edit Group Options Order Tools Special Help

Tester Type: ADVANTEST Advantest

Program ID: 115676 FT: FTRKGD (GLOB11=0)

Revision: D New Rev

Version: 0 New Ver

Status: ACT

Test Program: S25VRVIA

Job Name: FTRKGD

Checksum: NA

Bin Group: STD SFMD BIN1 PASS BIN1, FAIL BIN5/6/7/8

Correlation Good Bin: 0

CN Number: 1401974

Comments: GLOB11=0; FT, RETEST BIN8

RMA Disposition: No Special Requirements

OS Version: Job Number



Create Test Program ID - 5

- Select Tester Type then click OK button.

The screenshot shows two overlapping windows from the Microchip software. The background window is titled 'pctf0120m000 : Maintain Test Program Revisions [101]'. It has a menu bar (File, Edit, Group, Options, Order, Tools, Special, Help) and a toolbar. A red box highlights the 'Tester Type' dropdown menu, which is currently empty. The foreground window is titled 'pcthw0501s000 : Display Tester Types [101]'. It also has a menu bar and toolbar. It displays a list of tester types with their descriptions. The 'J750' entry is selected and highlighted in black. To the right of the list are 'OK' and 'Cancel' buttons.

Tester Type	Description
ICTS	ICTS
IHT	IHT
IMPACT	Impact
IIS9K	XCERRA SCHLUMBERGER
✓ J750	Teradyne J750 (all models)
J750_AERO	Teradyne J750 with AeroFlex test option
J750_HD	Teradyne J750HD
J750_LITE	Teradyne J750 with Litepoint option
J750_MSO	Teradyne J750s with MSO Option
J750_SCAN	Teradyne J750 with Scan option



Create Test Program ID - 6

- Input Test Program name (60 characters maximum ; Main Source on Test Setup Options).

pctf0120m000 : Maintain Test Program Revisions [101]

File Edit Group Options Order Tools Special Help

Tester Type: J750

Program ID: 999999

Revision: A [New Rev]

Version: 0 [New Ver]

Prog Overrides

Prog HW Limits

Model Limit

Status: EDIT

Test Program: TEST1

Job Name: []

Checksum: []

Bin Group: []

Correlation Process Code: []

Correlation Good Bin: []

Special Instructions: []

J750 OI Ver: []

CN Number: []

Comments: []

RMA Disposition: Contact Engineering

OS Version: [] Job Number

add alphanumeric



Create Test Program ID - 7

- Input Test Program Option (Job Name for below example, **60 characters maximum** ; Executable Name on Test Setup Options). Can leave it blank for some Tester Types.
- Test Program Checksum is an option, can leave it blank.
- Select Bin Group.

pctf0120m000 : Maintain Test Program Revisions [101]

File Edit Group Options Order Tools Special Help

Tester Type: J750

Program ID: 999999

Revision: A [New Rev]

Version: 0 [New Ver]

Status: EDIT

Test Program: TEST1

Job Name: OPTION1

Checksum:

Bin Group: STANDARD Bins 2 - 5

Correlation Process Code:

Correlation Good Bin:

Special Instructions:

J750 DI Ver:

CN Number:

Comments:

RMA Disposition: Contact Engineering

OS Version: Job Number

Prog Overrides

Prog HW Limits

Model Limit

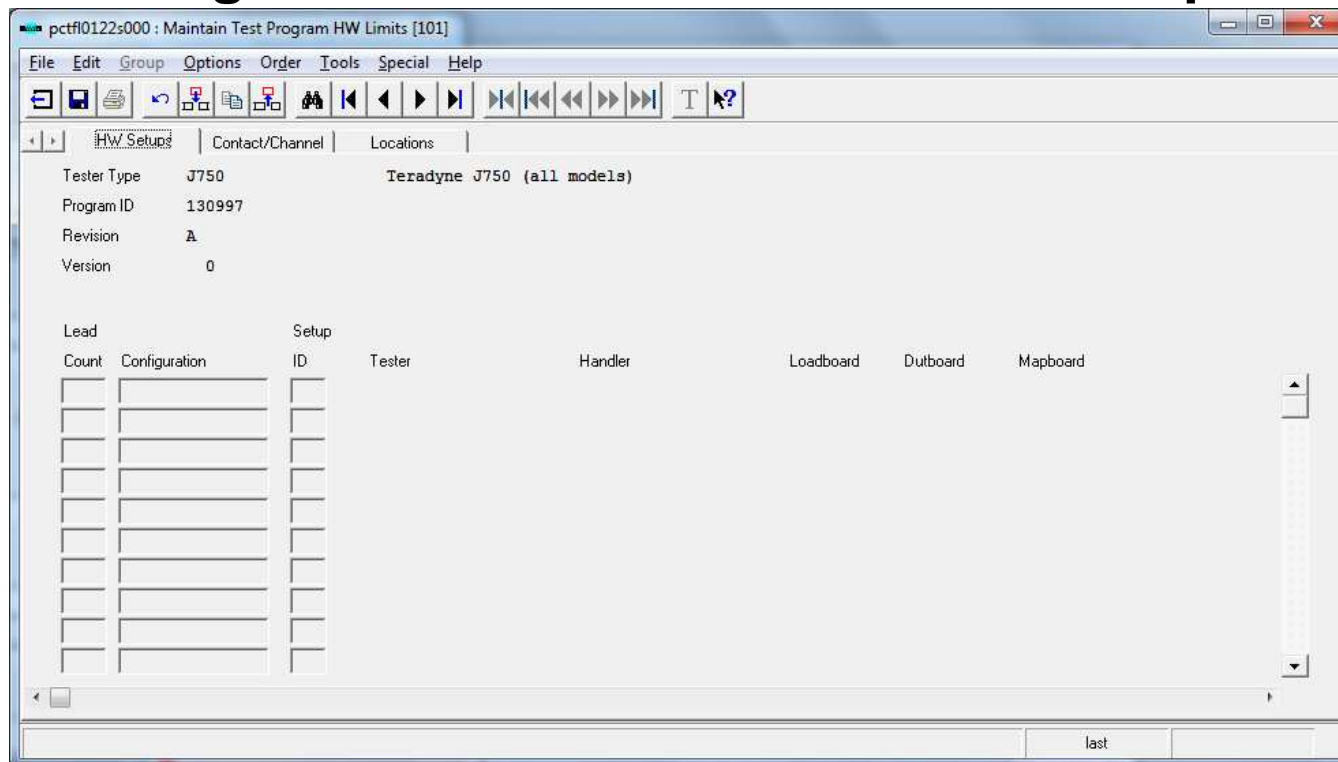
add alphanum









Create Test Program ID - 8

- Put Special Instructions (**120 Characters maximum**) if needed.
- Put Comment (**200 Characters maximum**) if needed.
- Put OS Version (**25 Characters maximum**) if needed.
- If you want to limit Test HW that can be used for this program ID, click “Prog HW Limits” button then add Setup ID.





Create Test Program ID - 9

- Click  “Save” button.
- Change Status from EDIT to PEND (Pending Approval).
- Click  “Save” button.
- Click  Insert button to add the other new Program ID(s).
- Click  “Save & Exit” button after finished creating new Program ID(s).





Create Test Program ID - 12

- After the CN to release the Test Program has been approved, MThai Document Control Team will change its Status from PENDING to ACT.

The screenshot shows a software window titled "pctf10120m000 : Maintain Test Program Revisions [101]". The interface includes a menu bar (File, Edit, Group, Options, Order, Tools, Special, Help) and a toolbar with various icons. The main form contains the following fields and values:

Tester Type	J750	Teradyne J750 (all models)
Program ID	122222	LEAR0_FT. FT-PRD-CERLER
Revision	1	New Rev
Version	0	New Ver
Status	ACT	Prog Overrides
Test Program	LEAR0_FT_A48e.XLS	Prog HW Limits
Job Name	FT-PRD-CERLER	Model Limit
Checksum	16540E2	
Bin Group	STANDARD	Bins 2 - 5
Correlation Process Code		
Correlation Good Bin	0	
Special Instructions		
J750 OI Ver		
CN Number	1700910	
Comments		
RMA Disposition	No Special Requirements	
OS Version		Job Number



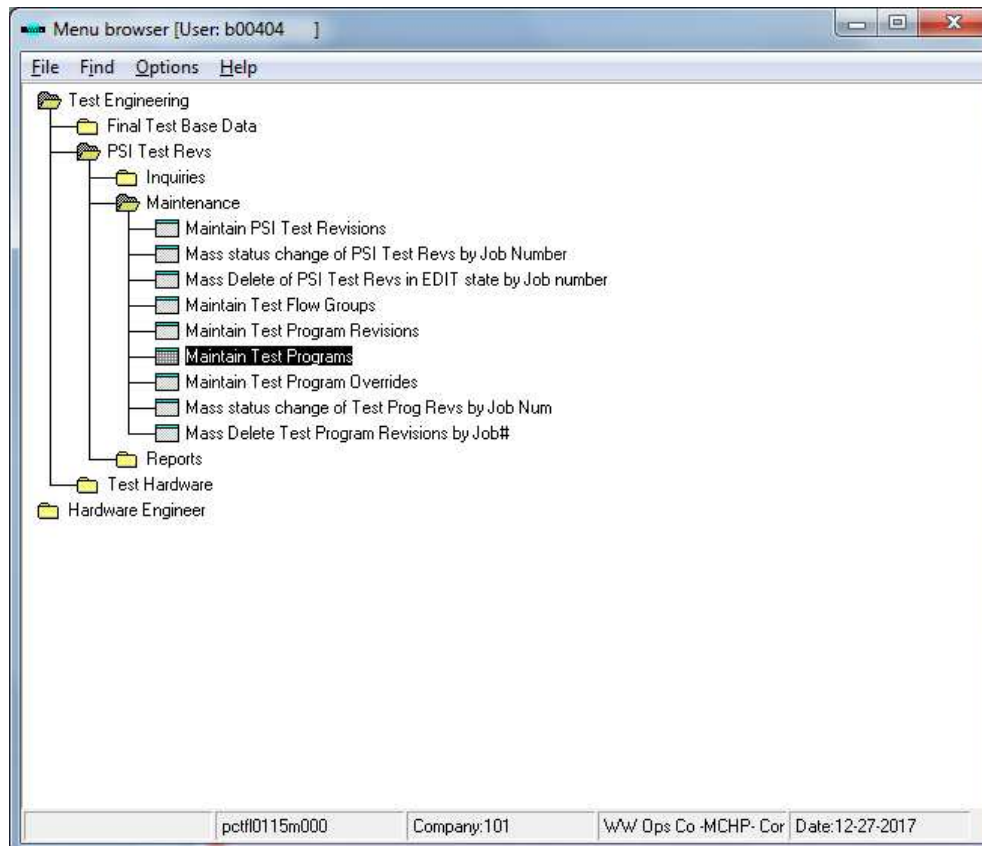
Create Test Program ID - 13

- When you need to release the new Test Program revision to production, click “New Rev” button, Update Test Program and Checksum.
- Click “Save” button and change Status from “EDIT” to “PEND”




Create Test Program ID Description - 1

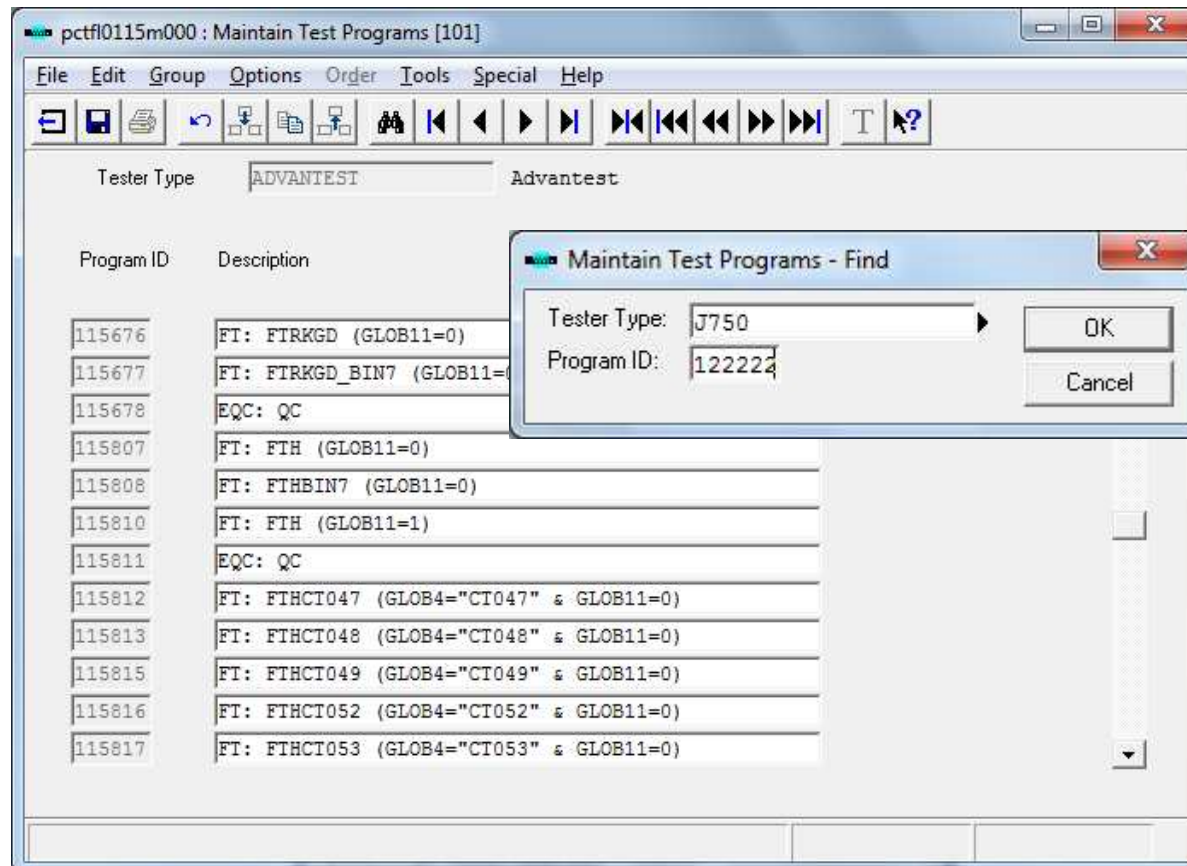
- Program ID is 6-digit number which is very hard to remember so, we need to put description to each ID.
- Under “Test Engineering”, Double clicks “PSI Test Revs”.
- Double clicks “Maintenance” then “Maintain Test Programs”.






Create Test Program ID Description - 2

- Click Find button  (binoculars symbol), select Tester Type and click OK.
- The new Program ID you just added will be at the bottom of the table.





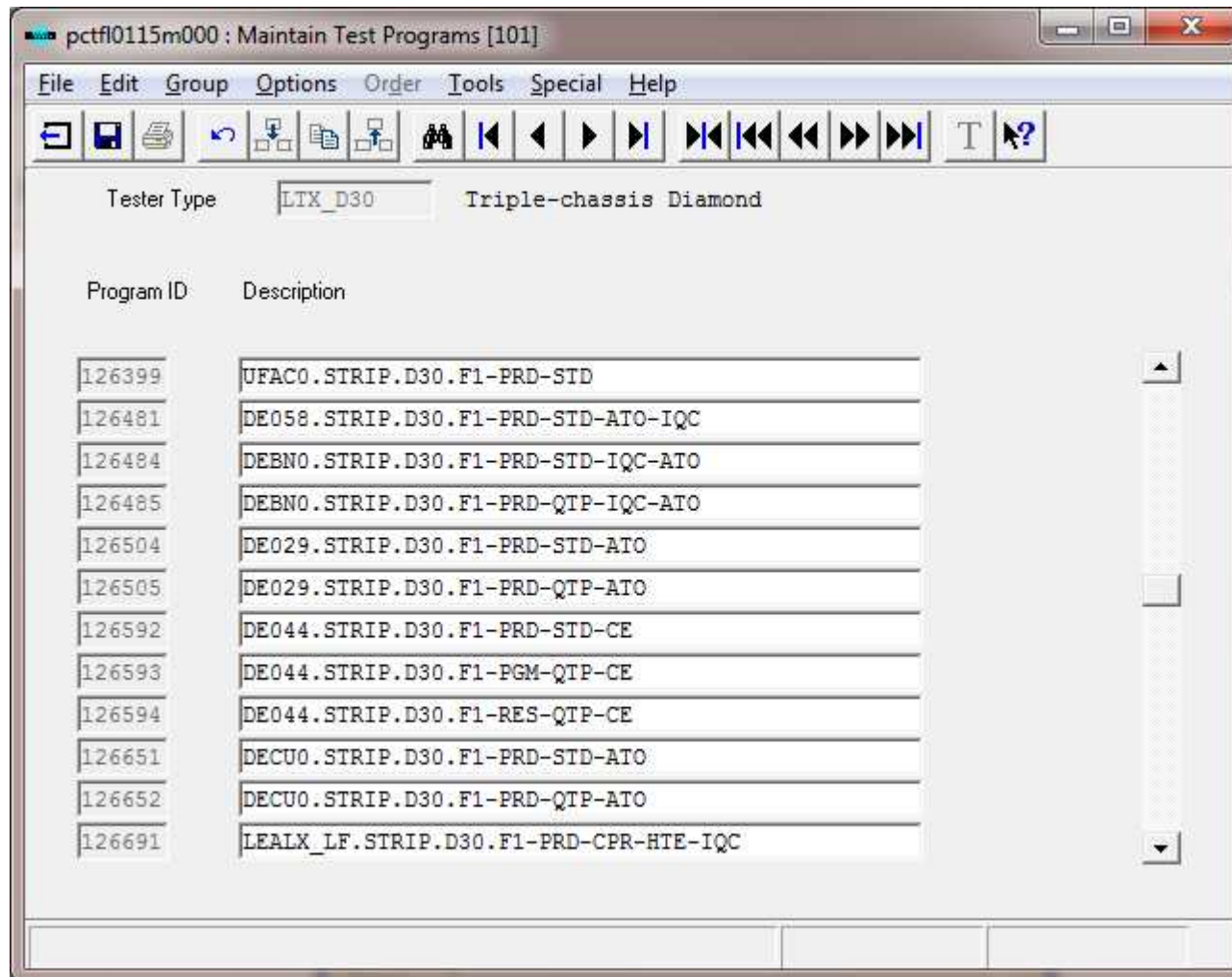
Create Test Program ID Description - 3

- Input unique Description (**50 characters maximum**) for each Program ID you created.
- We usually put Test Program Name with no revision and Test Program Option.
- Click  “Save & Exit” button after finished adding description.



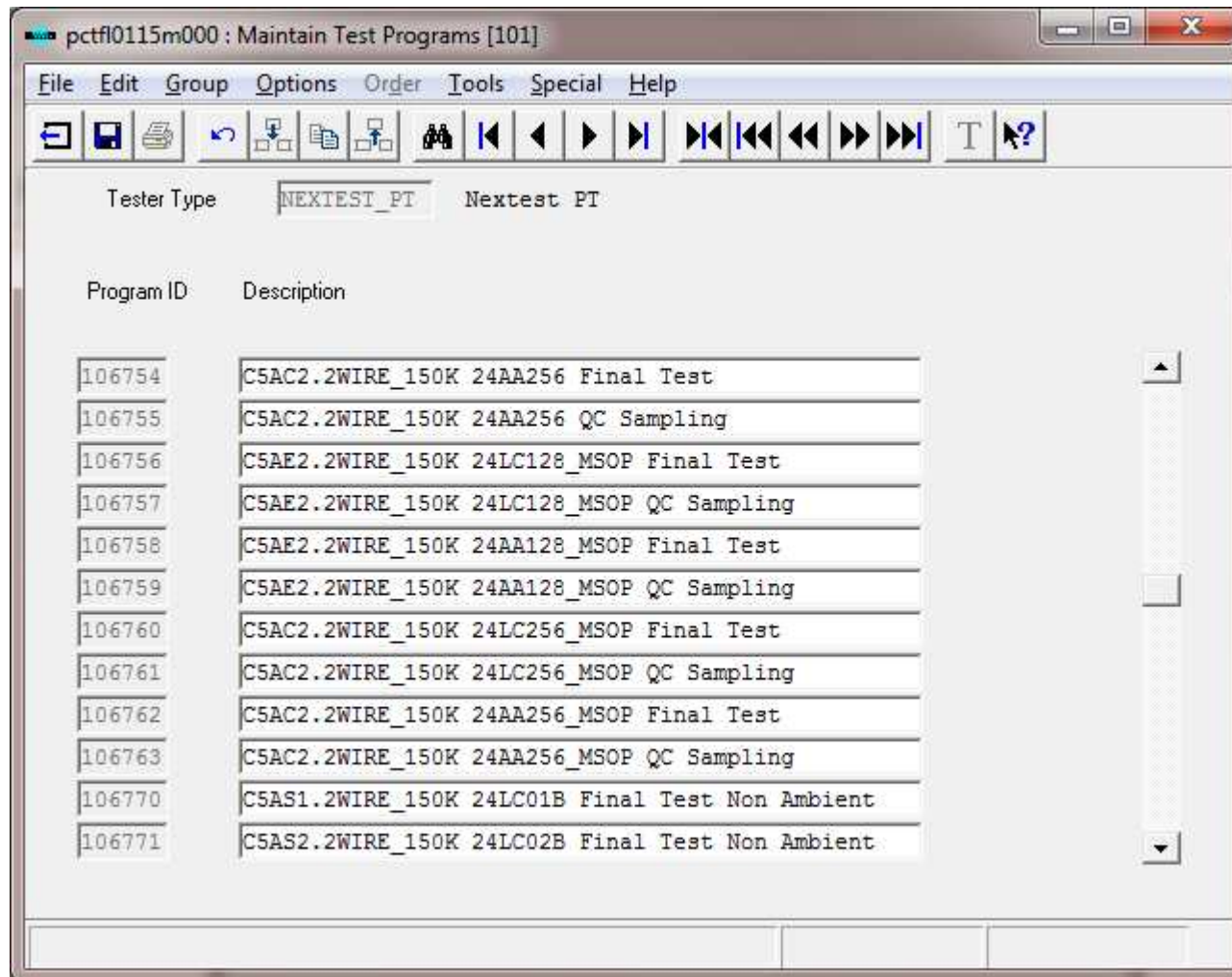


Create Test Program ID Description - 4



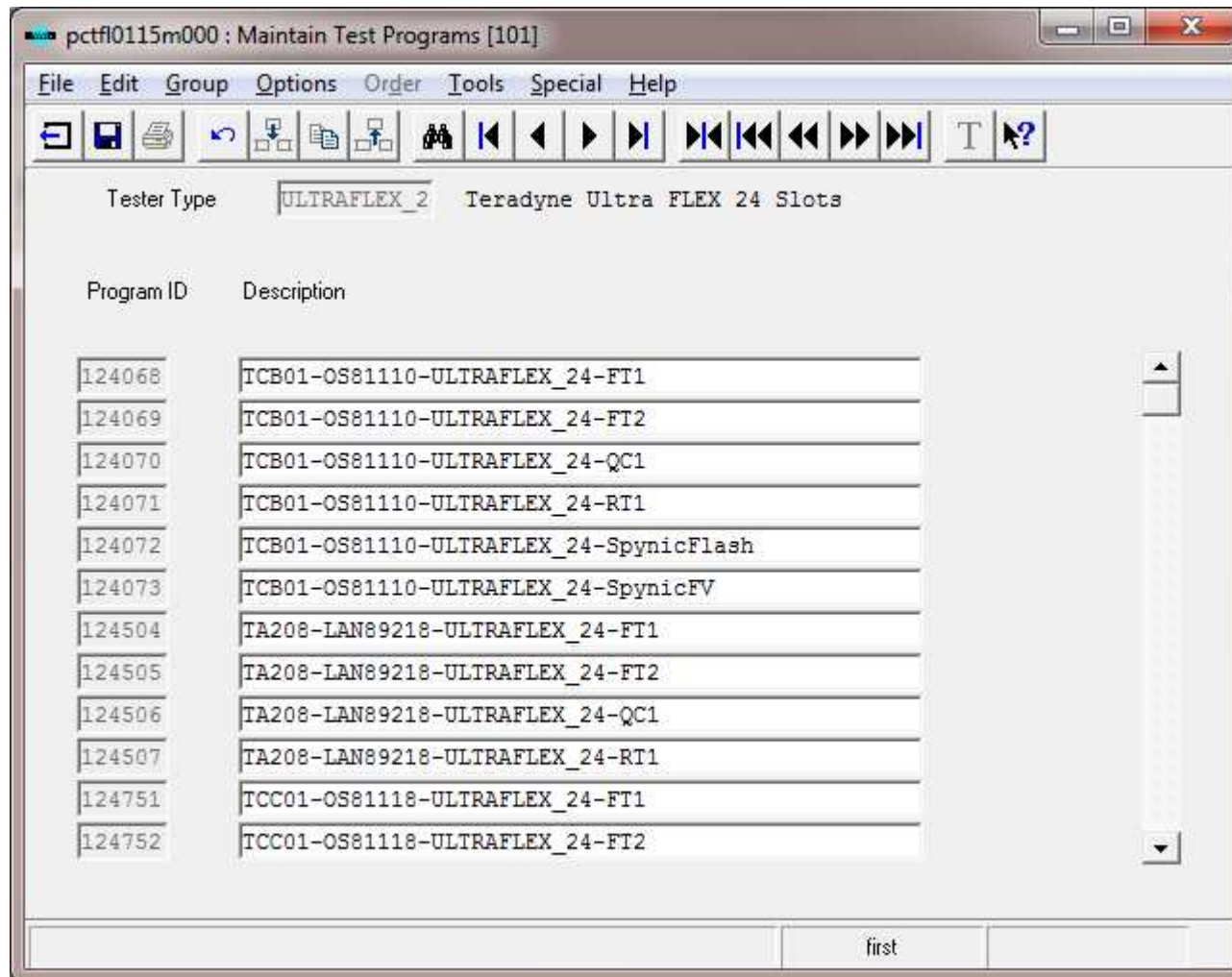


Create Test Program ID Description - 5



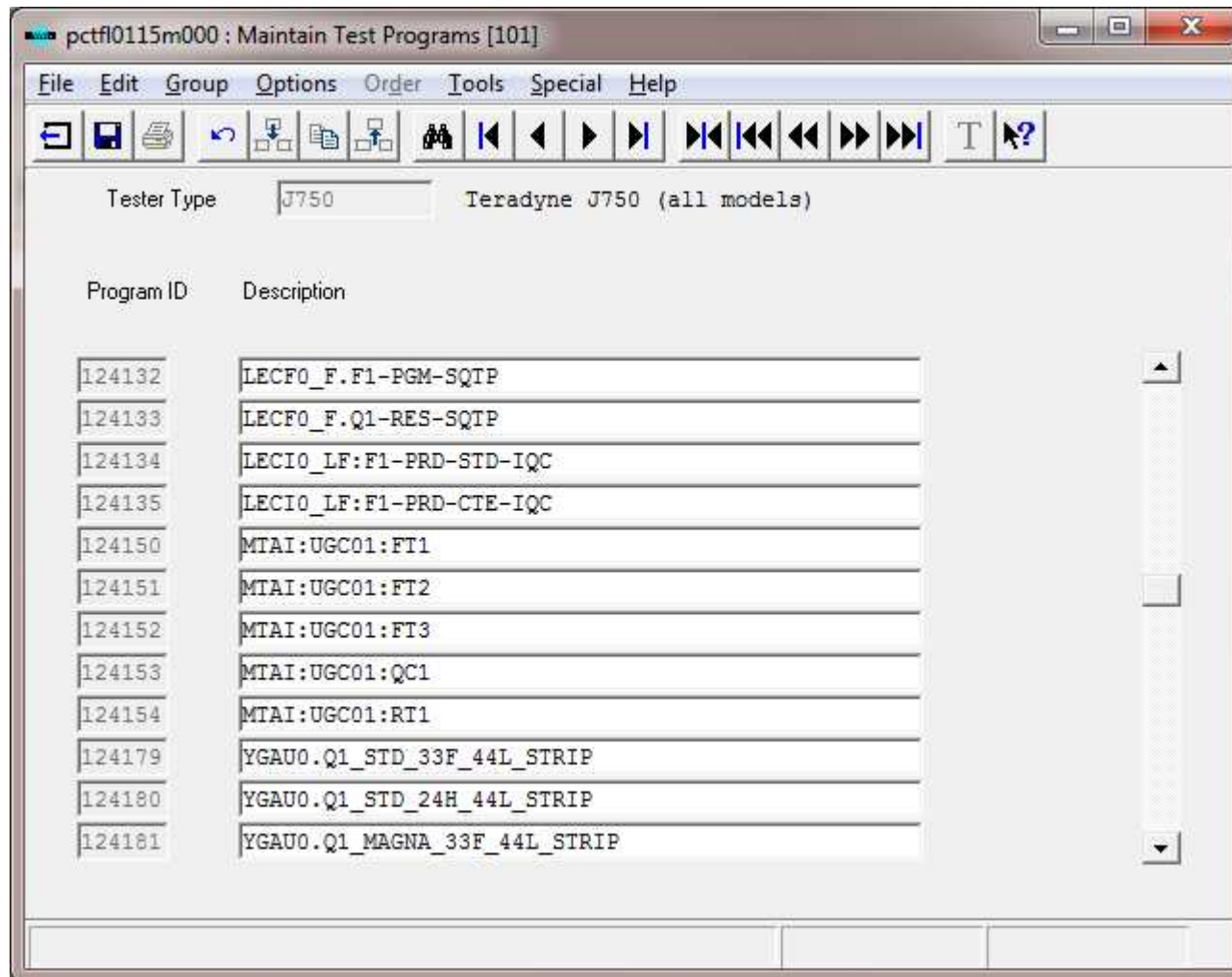


Create Test Program ID Description - 6



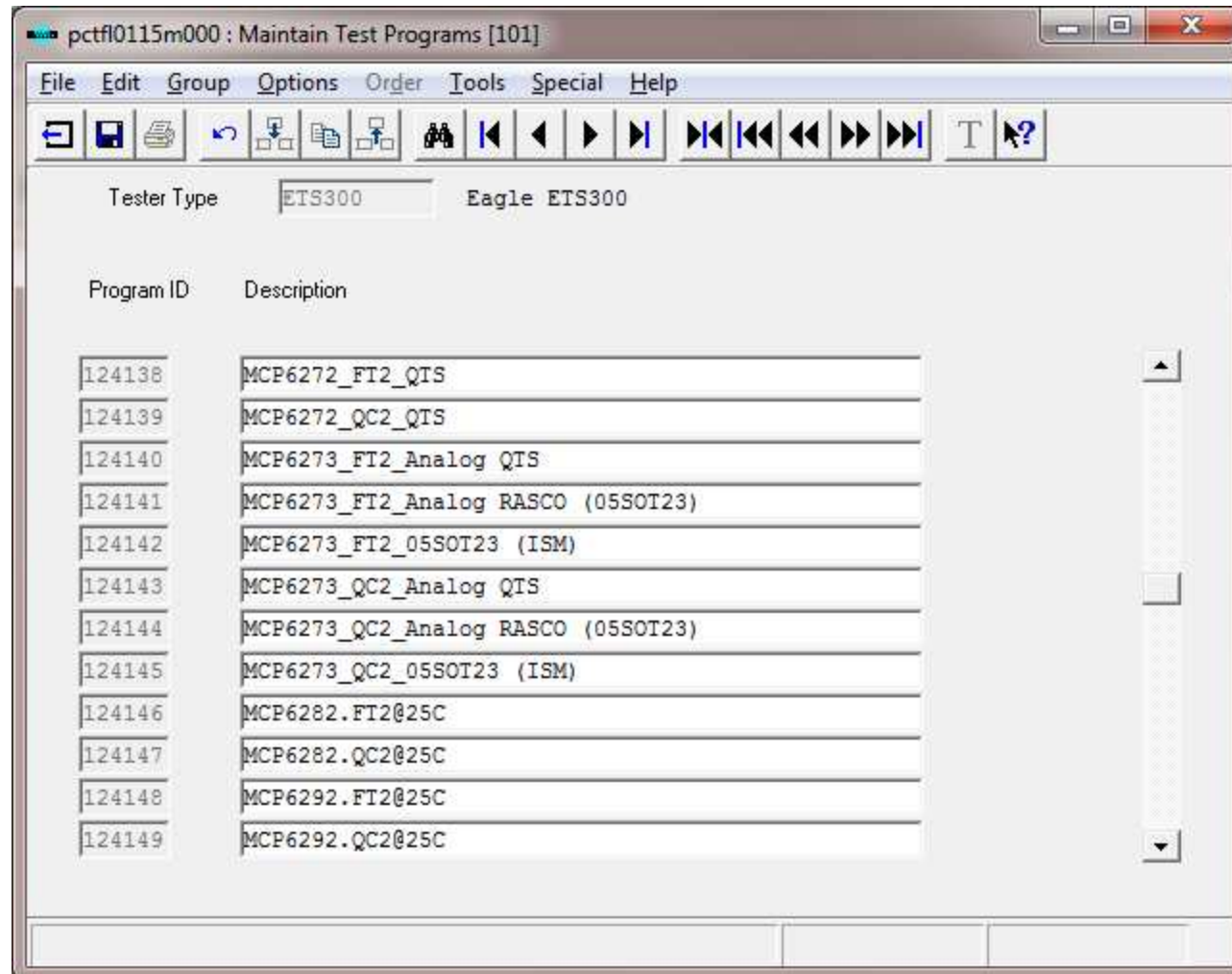


Create Test Program ID Description - 7



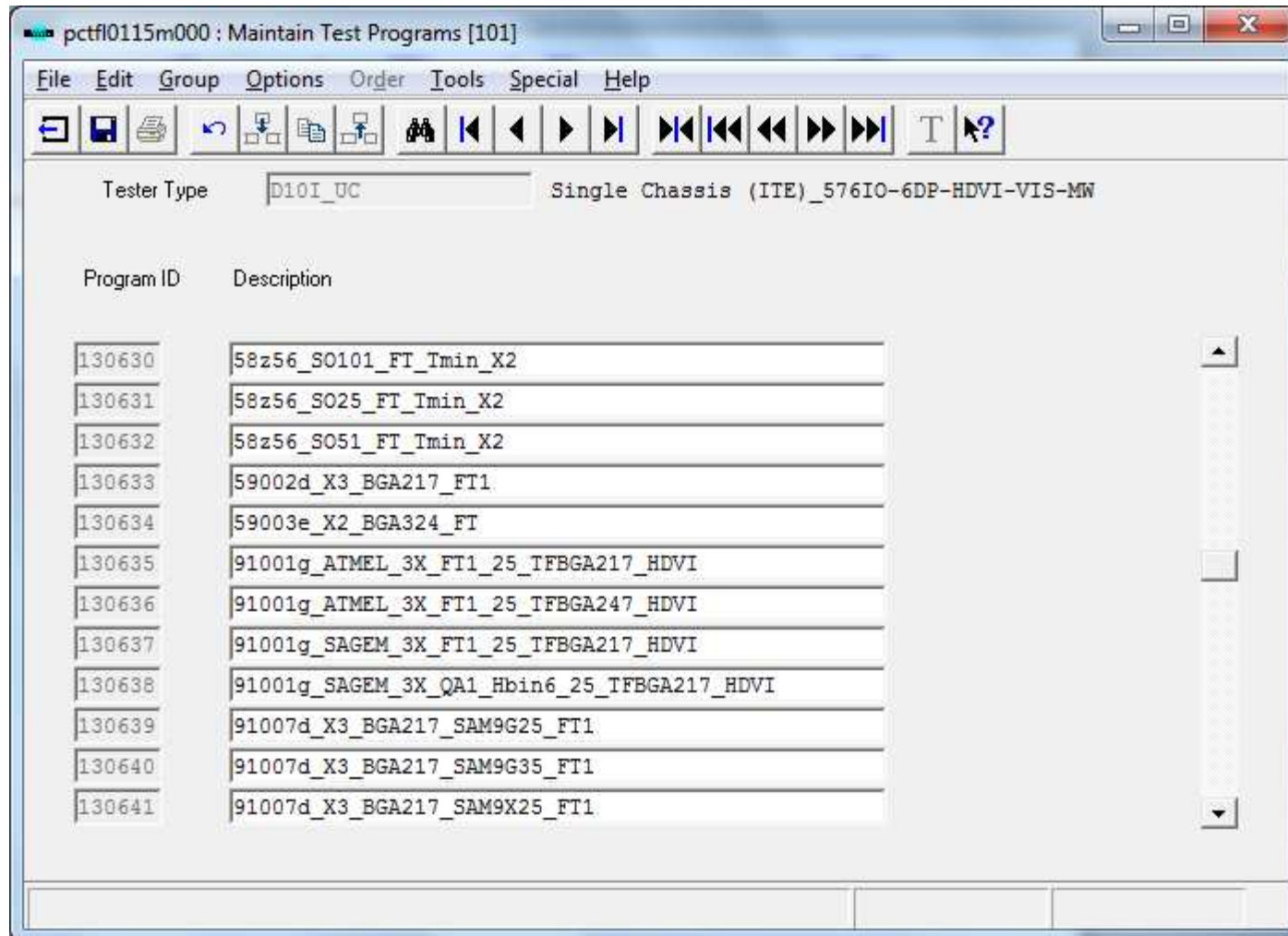


Create Test Program ID Description - 8



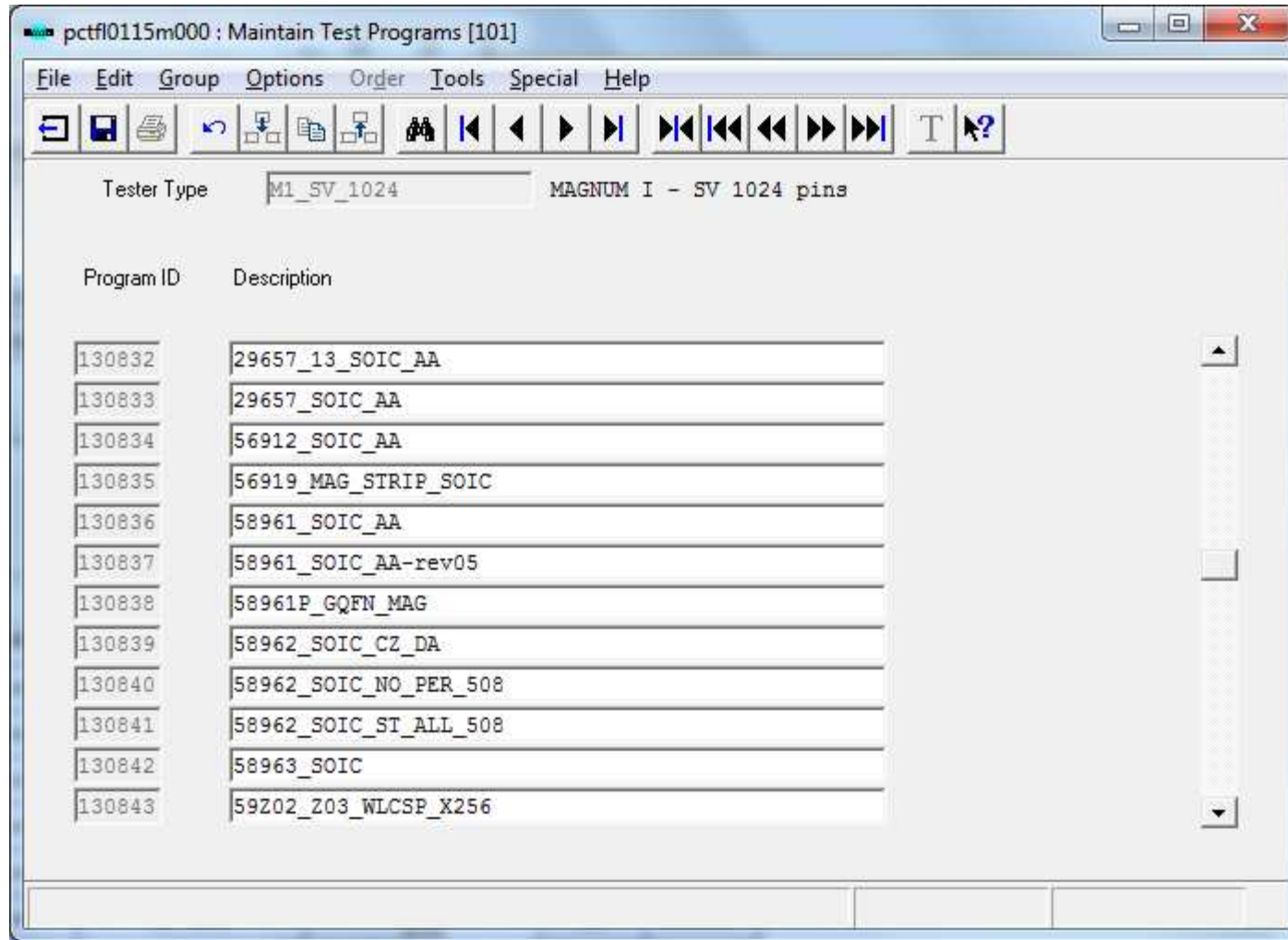


Create Test Program ID Description - 9



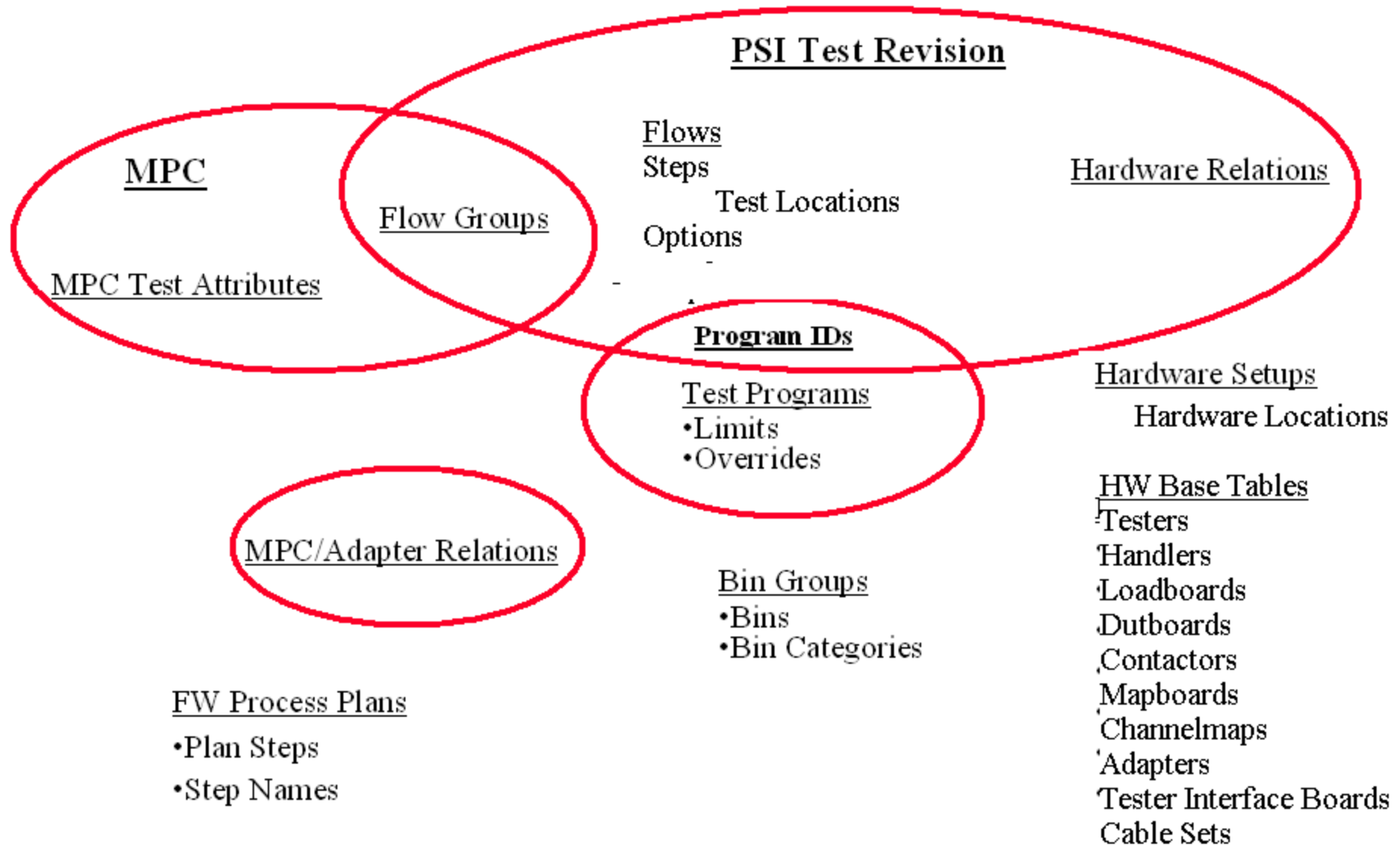


Create Test Program ID Description - 10





PDC Data Structure





Create Test Flow Group Name - 1

- Review all Test Flows and Test Program Options at every test step of all MPCs (MPNs) of that Mask to minimize the number of Test Flows.
- Define name of each Test Flow.
- Create Test Flow Group Name in PDC.

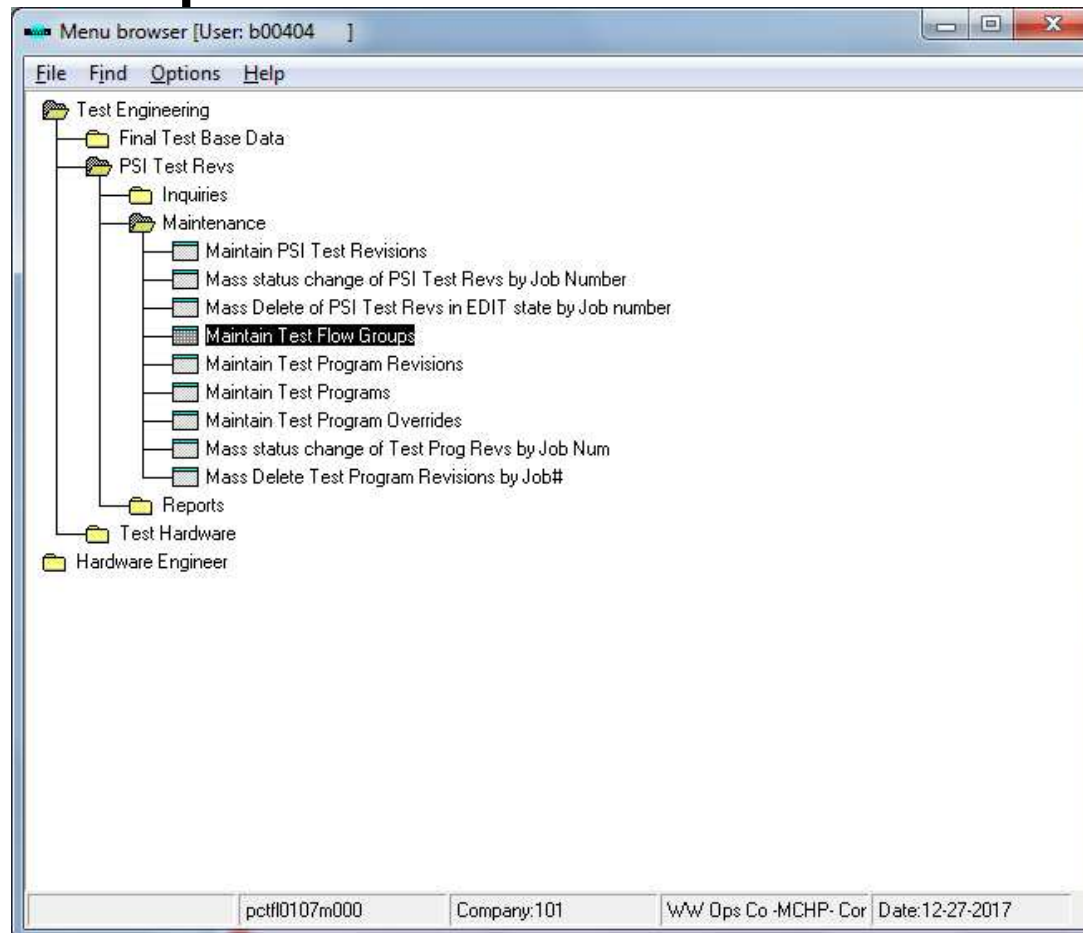
Note 1 : We call Flow Group since we can have more than 1 Test Flows under the same Flow Group Name.

Note 2 : The MPC can be linked to only one Flow Group.





Create Test Flow Group Name - 2

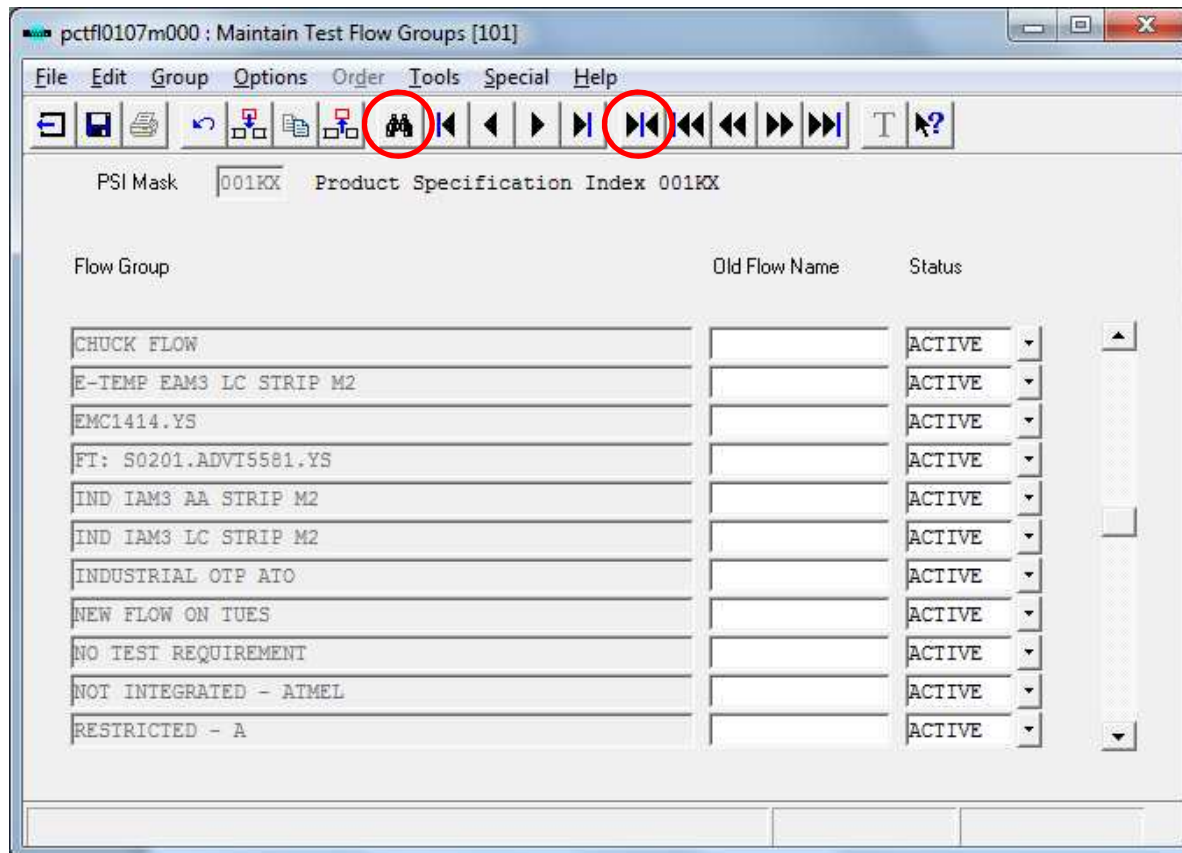
- Under “Test Engineering”, Double clicks “PSI Test Revs”.
- Double clicks “Maintenance” then Double clicks “Maintain Test Flow Groups”.







Create Test Flow Group Name - 3

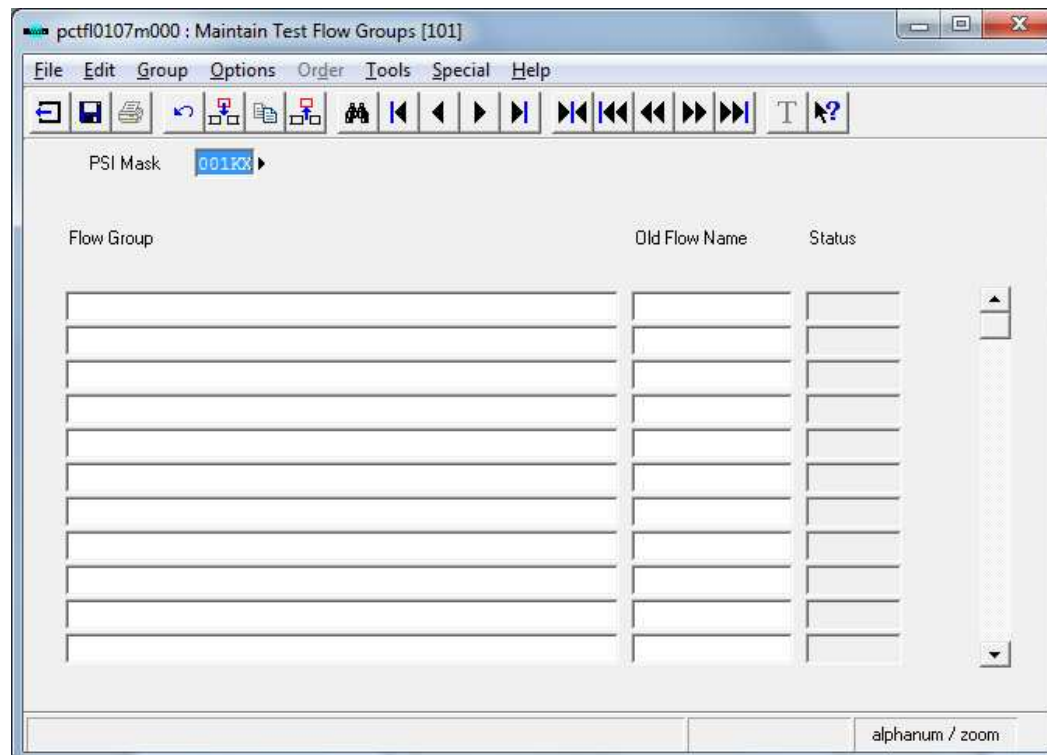
- Click Find button, input Mask number then click OK.
- If it is the initial release product, click  Bowtie button to add new Mask.
- Input new Mask then click  Save button.





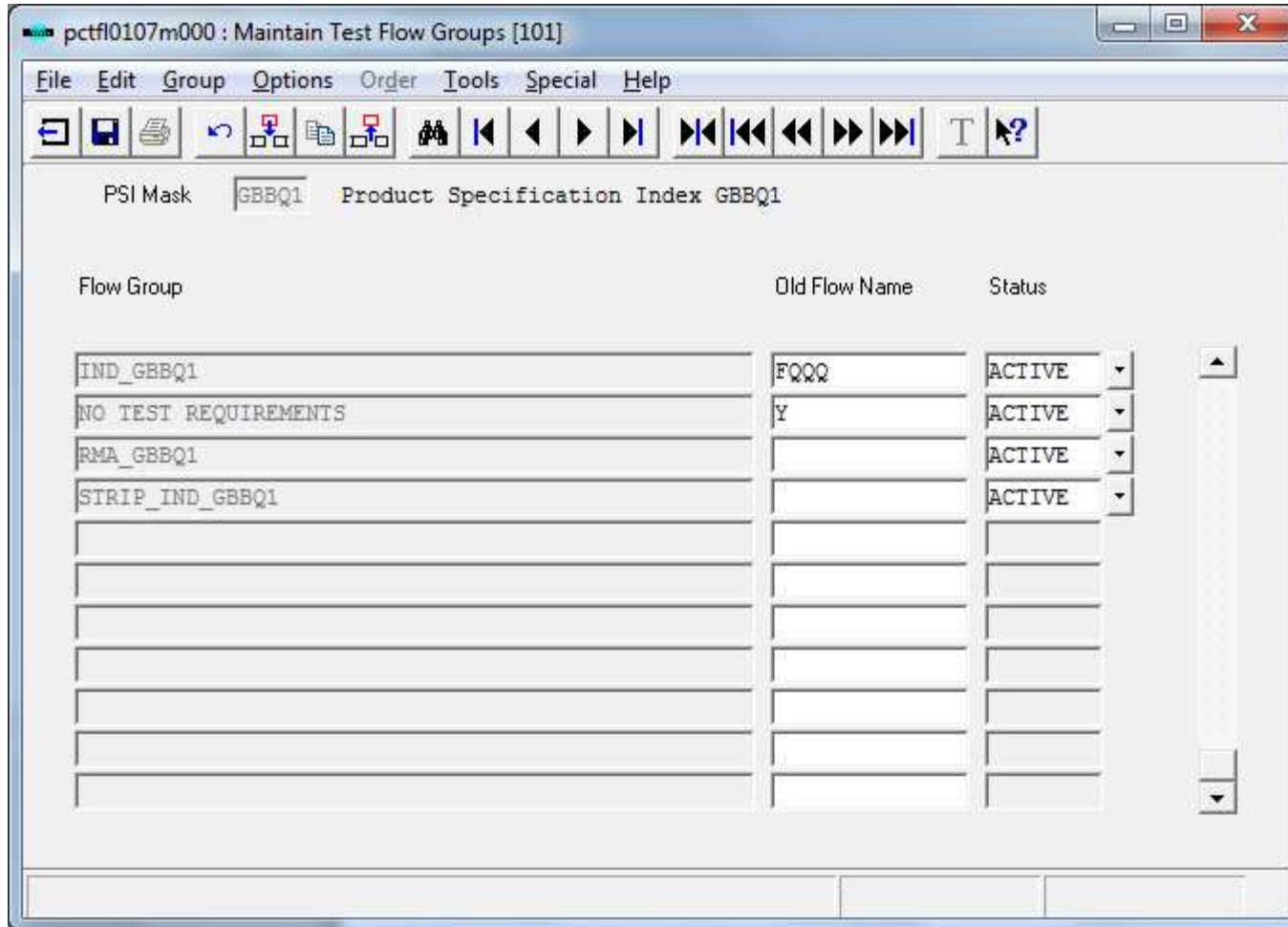
Create Test Flow Group Name - 4

- Click  Insert button. Input the 1st Flow Group Name.
- Press “Tab” key for 3 times, the cursor will go to the next line.
- Input the 2nd Flow Group Name, the 3rd, the 4th,... until complete all of them.
- Click  Save & Exit button.



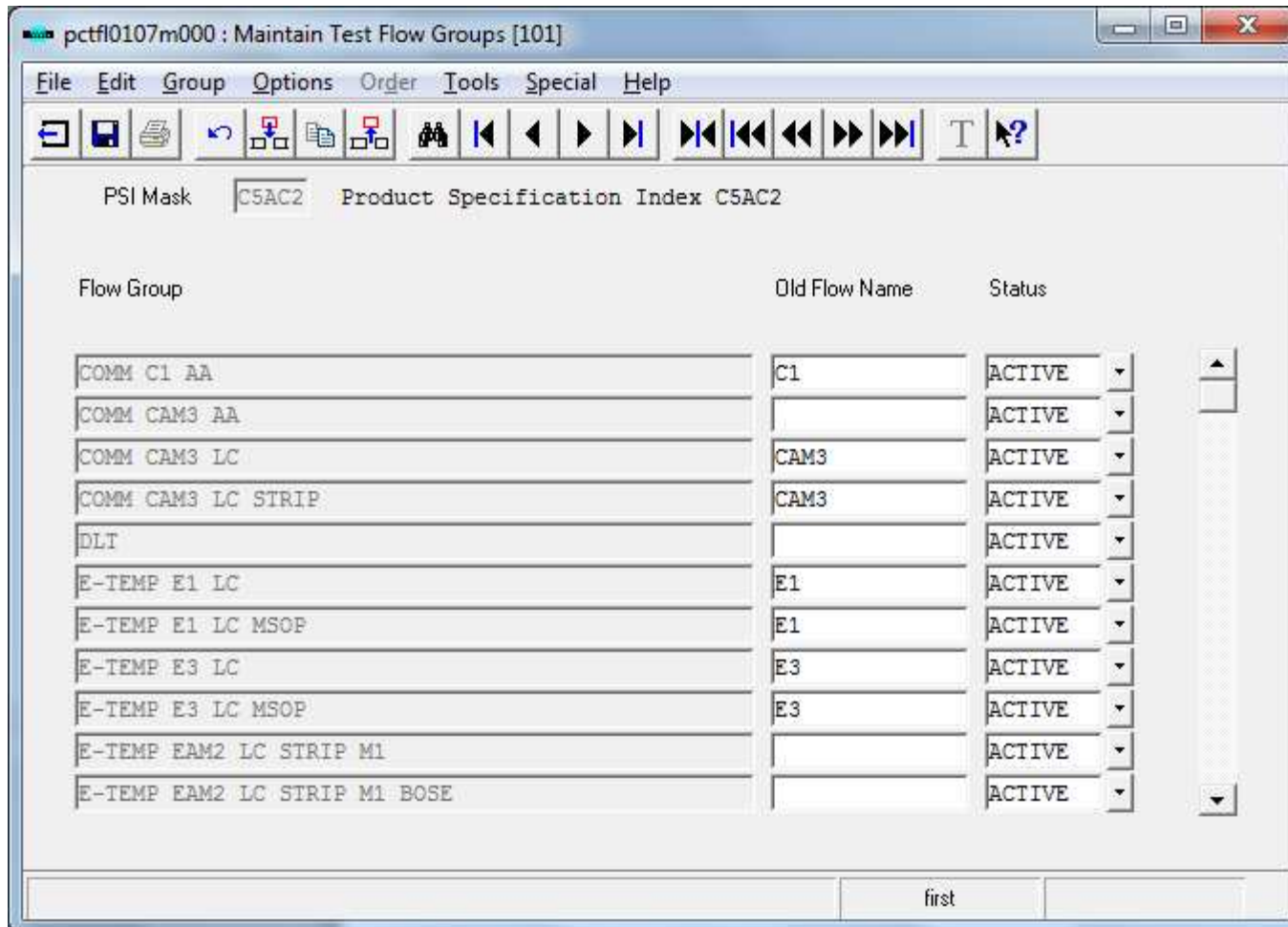


Create Test Flow Group Name - 5



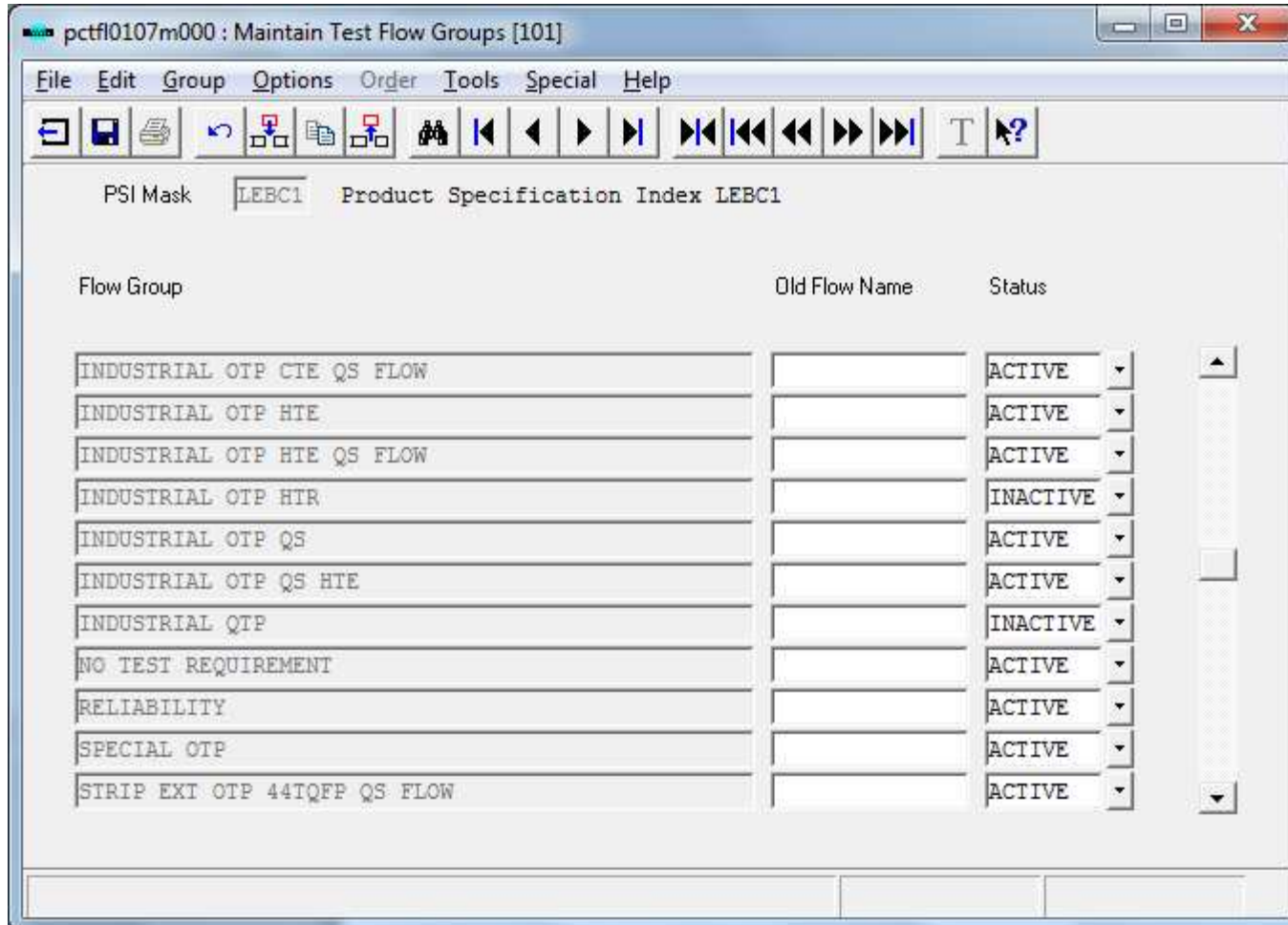


Create Test Flow Group Name - 6



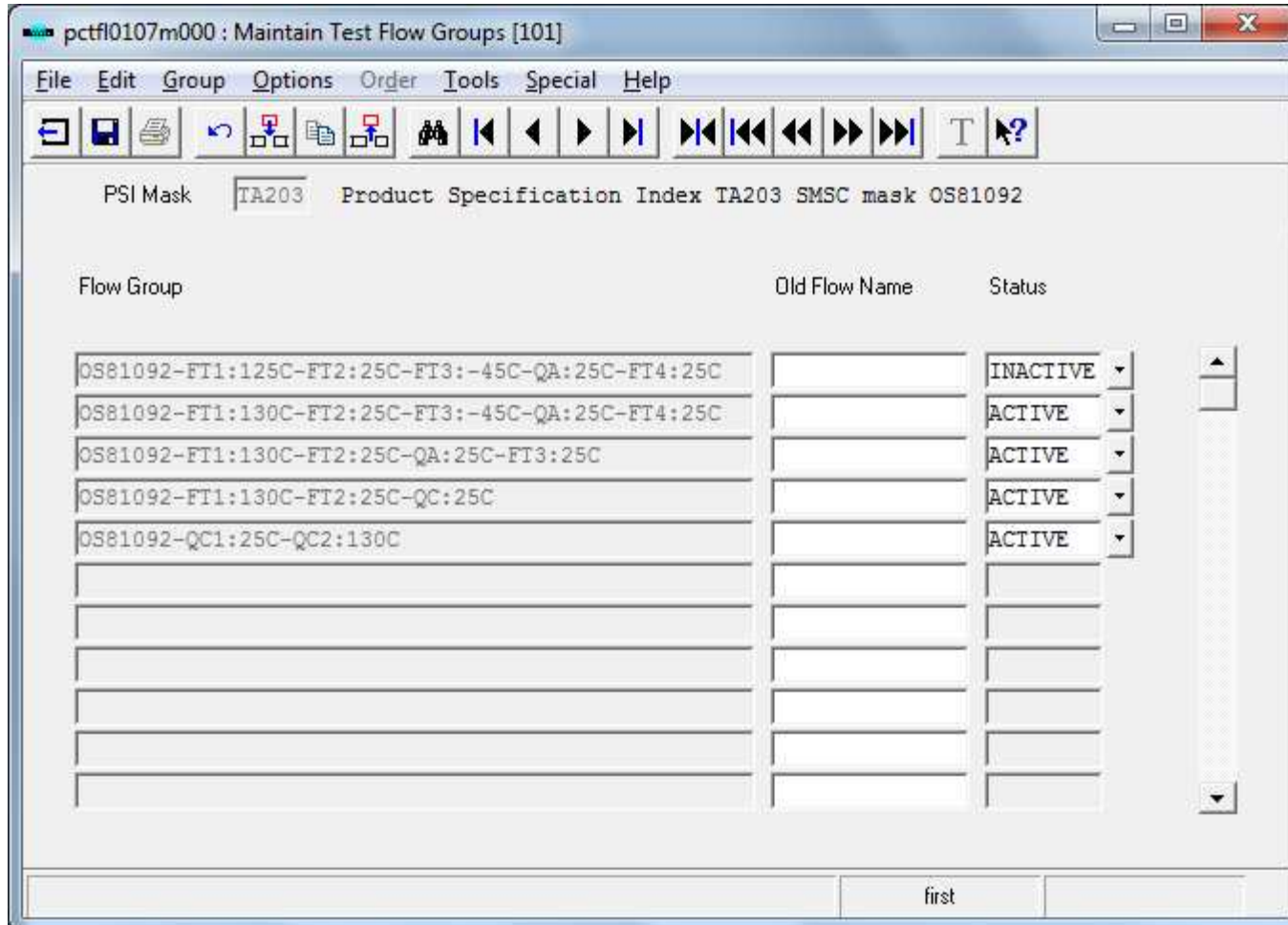


Create Test Flow Group Name - 7



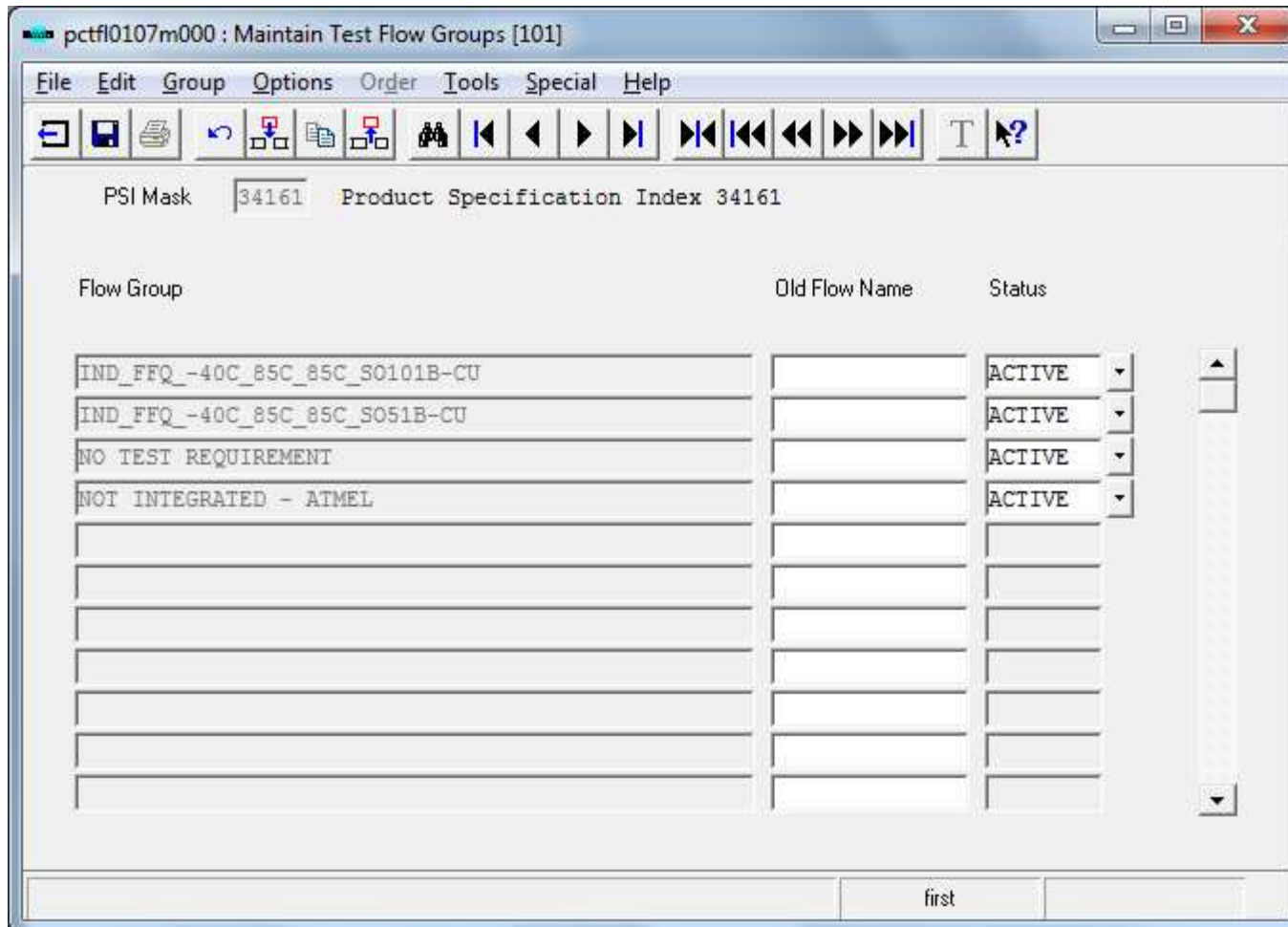


Create Test Flow Group Name - 8



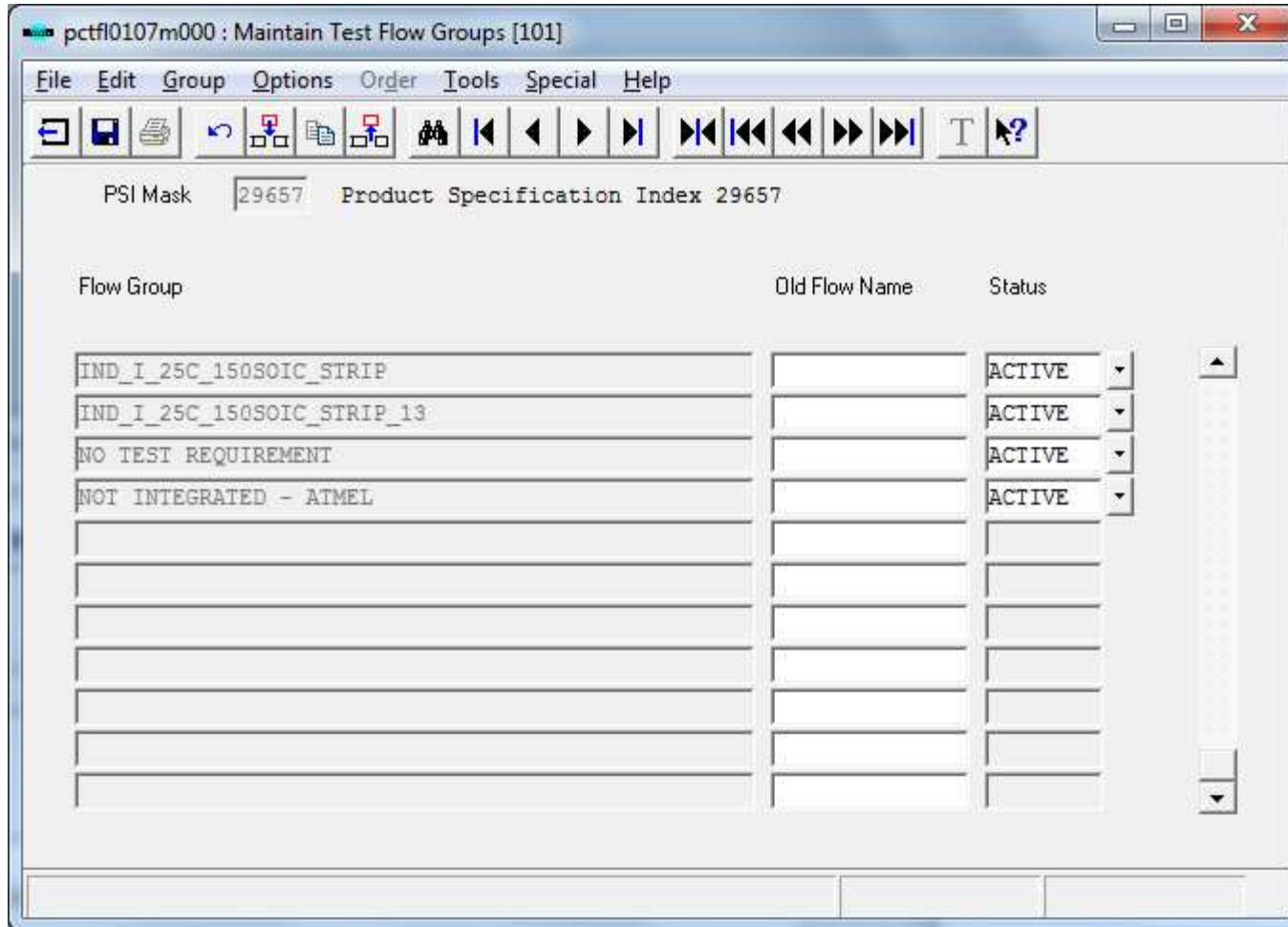


Create Test Flow Group Name - 9





Create Test Flow Group Name - 10

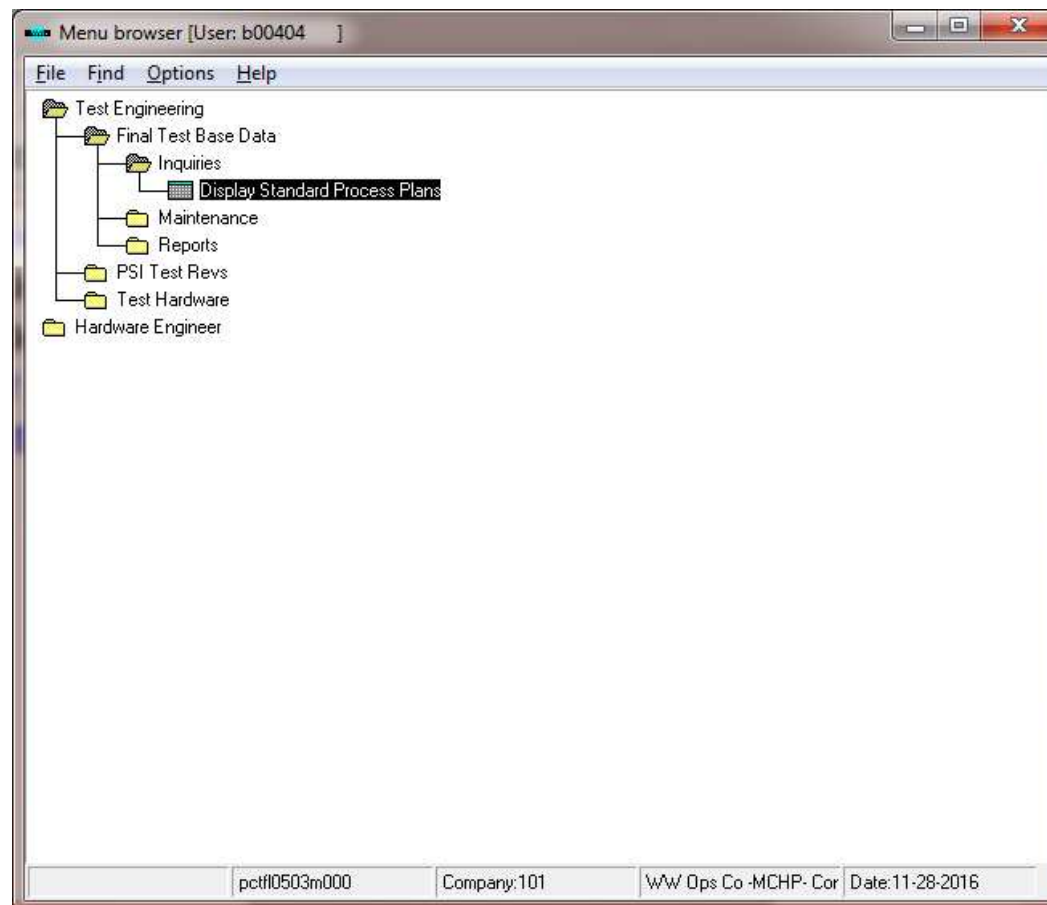




Process Plans - 1

To see all available STD/REL/RSN Process Plans :

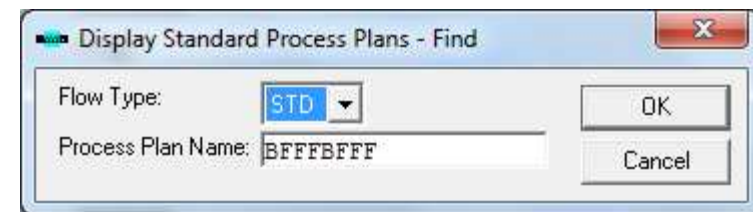
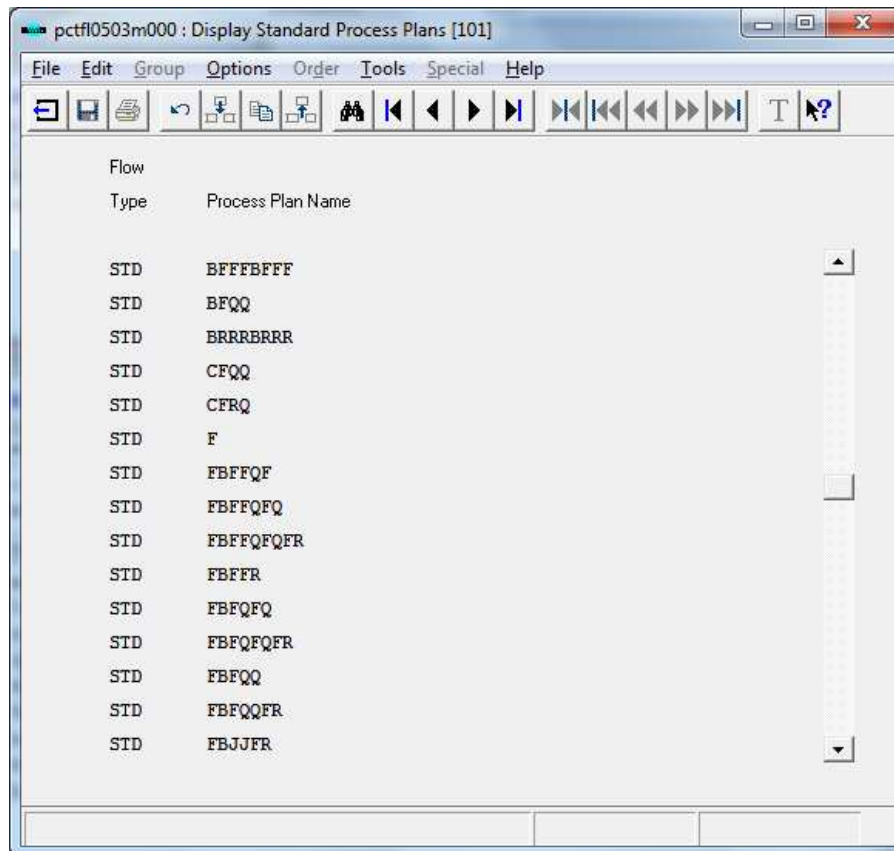
- Under “Test Engineering”, Double clicks “Final Test Base Data”
- Double clicks “Inquires” & “Display Standard Process Plans”.





Process Plans - 2

- Click Find button, input Flow Type & Process Plan you require.
- If you can't find the Process Plan you needed, please contact **Surasak Chairattanarom - B00717 (MThai IS)**.





Process Plans - 3



Process Plan

pctf10503m000 : Display Standard Process Plans [101]

Flow	Type	Process Plan Name
REL	BF	
REL	BFF	
REL	BFFBFF	
REL	BFFF	
REL	BFFBFFF	
REL	BFFFBFFF	
REL	BFFFF	
REL	BFFFFBFFF	
REL	BFFFFFFBFFF	
REL	BQ	
REL	BQBQ	
REL	BRRBR	
REL	BRRR	
REL	BRRRBRR	

pctf10503m000 : Display Standard Process Plans [101]

Flow	Type	Process Plan Name
RSN	CFQ	
RSN	CRQ	
RSN	F	
RSN	FF	
RSN	FFF	
RSN	FFFFQ	
RSN	FFQ	
RSN	FQ	
RSN	FQFQ	
RSN	FQFQFQ	
RSN	FQQ	
RSN	FQQFR	
RSN	FQQFRQ	
RSN	FQQQQ	
RSN	FQR	

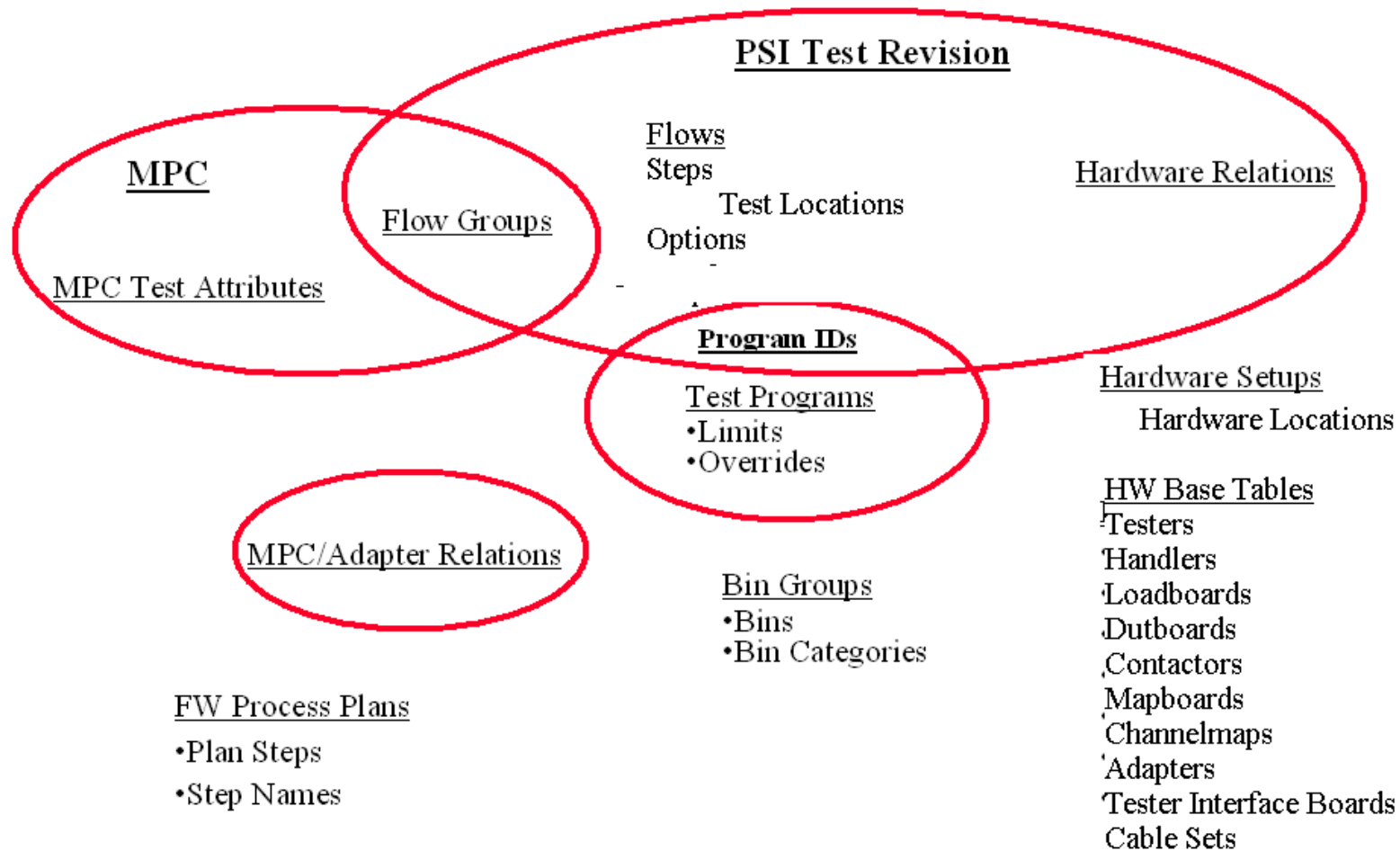
pctf10503m000 : Display Standard Process Plans [101]

Flow	Type	Process Plan Name
STD	BFFBFFF	
STD	BRRRBRR	
STD	CFQQ	
STD	CFRQ	
STD	F	
STD	FBFFQF	
STD	FBFFQFQ	
STD	FBFFQFQFR	
STD	FBFFR	
STD	FBFQFQ	
STD	FBFQFQFR	
STD	FBFQQ	
STD	FBFQQFR	
STD	FBJJFR	
STD	FBJQ	



Create PSI Test Revision - 1

- PSI Test Revision is the place to create relationship among Hardware Setups, Test Flows and Program IDs.





Create PSI Test Revision - 2

- PSI Test Revision data will be shown at **FLOW STEPS**, **FLOW DESC**, Hardware Setups, Temperature, Programming Type on Test Setup Sheet.

SPECIAL TEST SETUP OPTIONS -- MPC: LEAD1TT4X030 FLOW#: 245849 REV: AD VER: 0

MPC: LEAD1TT4X030 		LOT:- 		CAT PART NUMBER: PIC18F46K20T-I/PT030
				WAFER LOT 1: -
				WAFER LOT 2:
				DUE DATE:
				LEAD/CONFIG: 44/44TQFP_10 (T4X)
FLOW STEPS:	FS1@IN25C, IS1@85C, QC1@IN-40, FT1@IN25C, RT1@IN25C			
FLOW DESC:	J750 STRIP INDUSTRIAL QTP X20LF			
PSI REV:	M	TRACECODE:		
PSI TEST REV:	LEAD1 Rev. AD Ver. 0	MASK CALL REV:	-	
SDP PROD ID:		CP ON CHECKSUM:	0339	
SDP CUST ID:		CP OFF CHECKSUM:	AA8C	
SDP MPC:		QCODE:		
TEMP CN NO.:	1701736	COMMENTS: Change hardware id of 44L QFN 8x8		
TEST LOCATION.:	MTAI,			
MULTI-STEP TEST PROGRAM.: NO				

CP ON :



CP OFF :



QCODE :





Create PSI Test Revision - 3

- PSI Test Revision data will be shown at FLOW STEPS, FLOW DESC, **Hardware Setups**, Temperature, Programming Type on Test Setup Sheet.





2) Hardware Setups for 44/44TQFP_10 (T4X)

	SETUP ID	HANDLR. CHANNELMAP	LOADBOARD ADAPTER	CONTACTOR TESTER INTERFACE BOARD	DUTBOARD CABLE SET	MAPBOARD HW LOCATION
<input type="checkbox"/>	44	CAS x3dlc44tqfp_128p	14-A3100	14-A1961		MTAI,
<input type="checkbox"/>	45	CAS x4dlc44tqfp_256p	14-A3100	14-A1961		MTAI,
<input type="checkbox"/>	63	SCH x20mct44tqfp	14-A4254	14-A2834		MTAI,



Create PSI Test Revision - 4

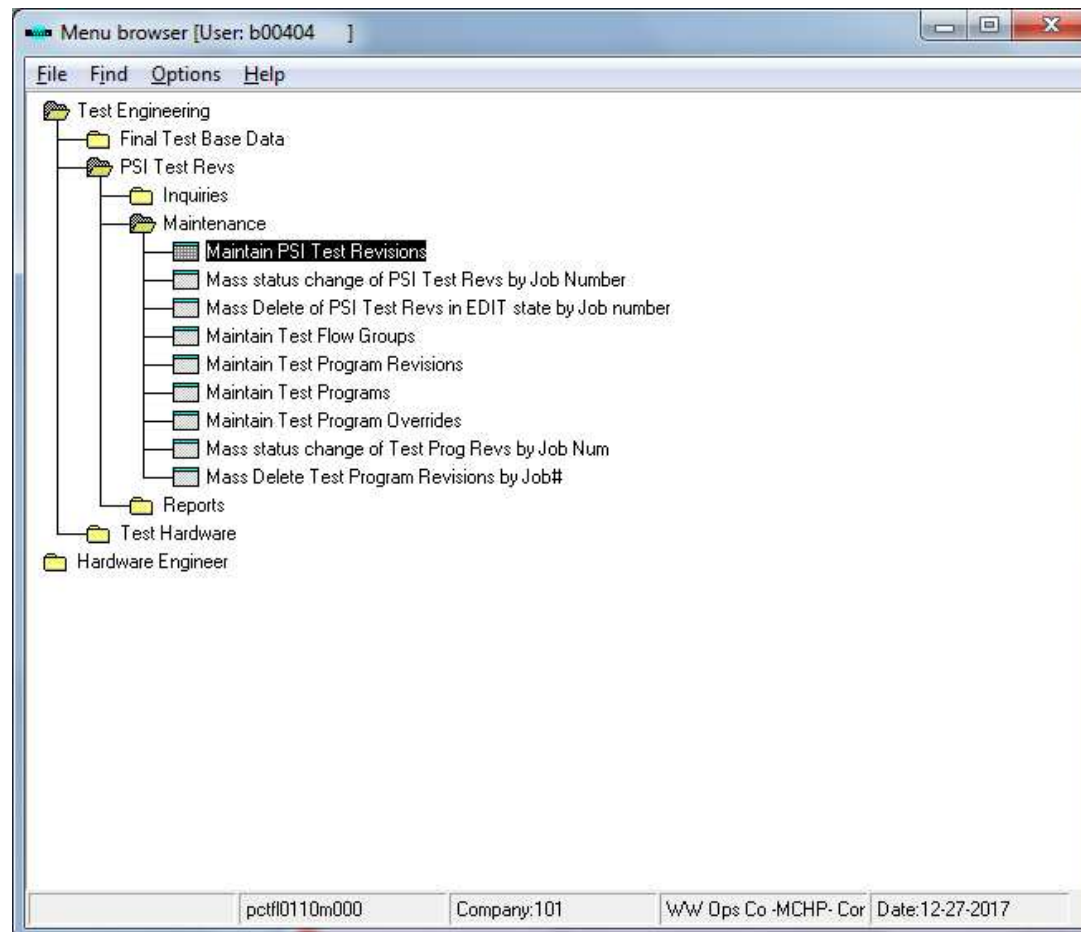
- PSI Test Revision data will be shown at FLOW STEPS, FLOW DESC, Hardware Setups, **Temperature**, **Programming Type** on Test Setup Sheet.

<input type="checkbox"/> ID: 114137	Rev: AX	Ver: 0	Status: ACT	Verified
Main Source:	LEAR0_FT_A48e.xls			<input type="checkbox"/>
Checksum:	16540E2			<input type="checkbox"/>
Executable Name:	ft-qtp-std			<input type="checkbox"/>
Part Number:	18F46K20			<input type="checkbox"/>
Temperature:	IN25C			<input type="checkbox"/>
Programming Type:	QTP			<input type="checkbox"/>
Bins:	2-PASS, 3-PA, 4-FU, 5-O/S			<input type="checkbox"/>
Hardware Limits:				<input type="checkbox"/>
Correl. Process Code:				<input type="checkbox"/>
Correlation Good Bin:	0			<input type="checkbox"/>
OS Version				<input type="checkbox"/>
Special Instructions:				<input type="checkbox"/>
CN Number:	1700910			
Comments:				



Create PSI Test Revision - 5

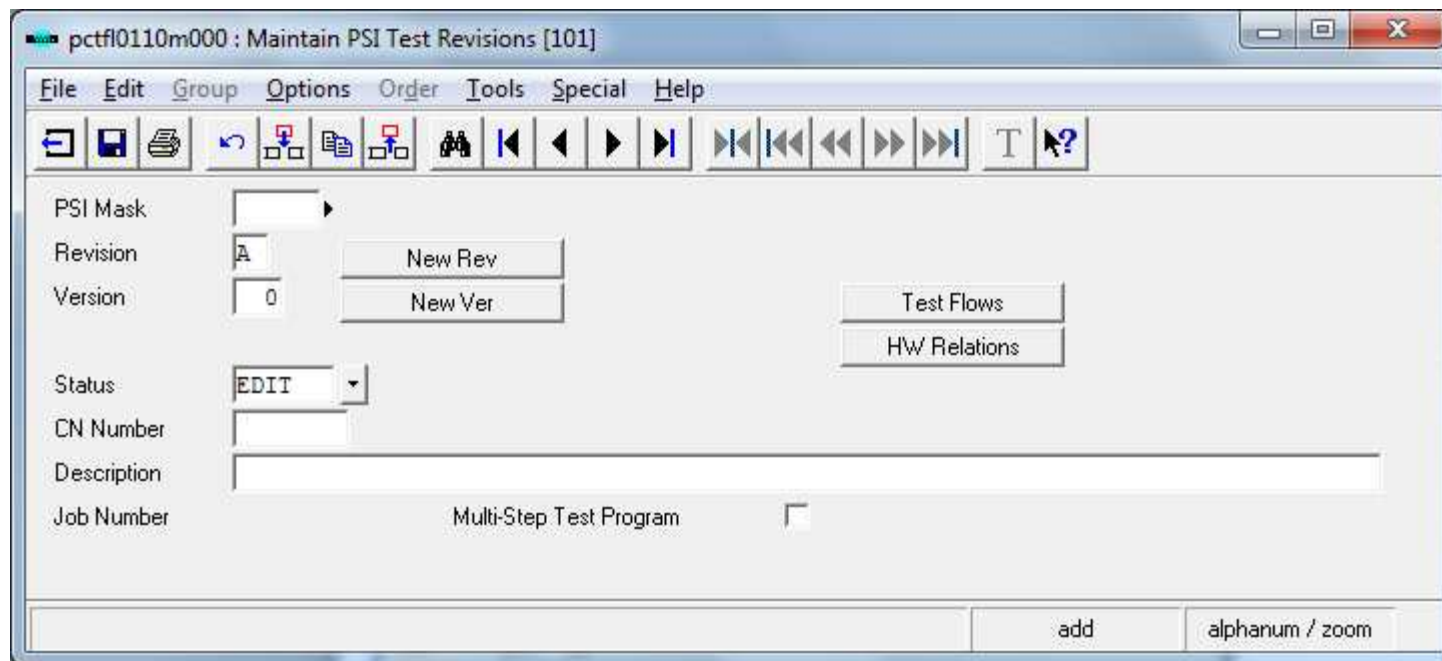
- Under “Test Engineering”, Double clicks “PSI Test Revs”.
- Double clicks “Maintenance” then Double clicks “Maintain PSI Test Revisions”.





Create PSI Test Revision - 6

- For initial release, need to add new Mask in PDC.
- Click Insert button, input Mask number.
- Input Description.
- Click Save button.





Create PSI Test Revision - 7

- Click HW Relations button to add Hardware Setup.
- Click Insert button, Input Lead Count, Select Configuration (Package), Select Hardware Setup.

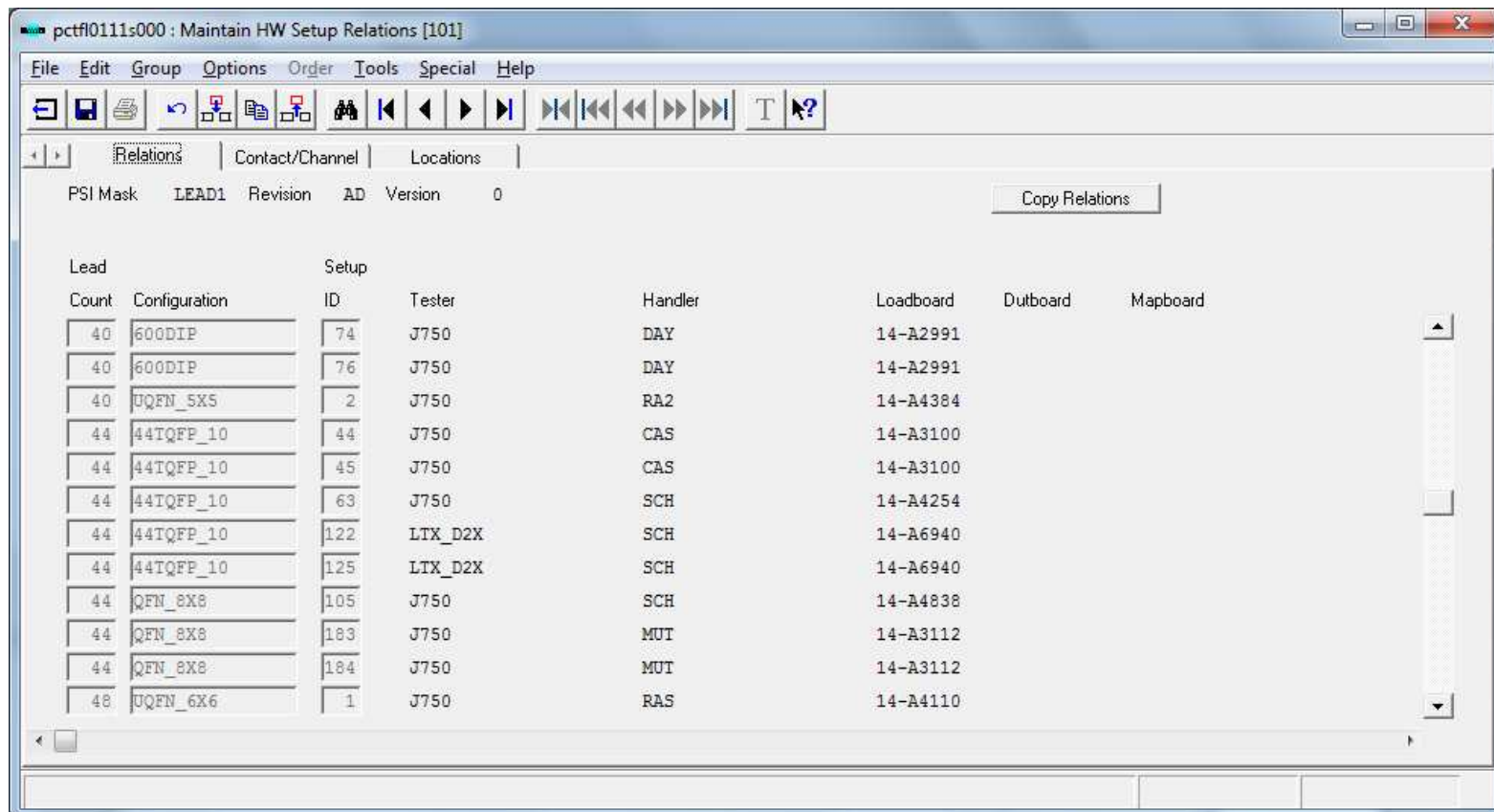
The screenshot displays two overlapping windows from a Microchip software application. The background window, titled "pctf0111s000 : Maintain HW Setup Relations [101]", shows a menu bar (File, Edit, Group, Options, Order, Tools, Special, Help) and a toolbar. Below the toolbar, there are tabs for "Relations", "Contact/Channel", and "Locations". The "Relations" tab is active, showing fields for "PSI Mask" (LEAD1), "Revision" (AD), and "Version" (1). A "Copy Relations" button is visible. The main area contains a table with columns: Lead Count, Configuration, Setup ID, Tester, Handler, Loadboard, Dutboard, and Mapboard. The "Lead Count" field is set to 44, and "Configuration" is set to 44TQFP_10. The foreground window, titled "pcthw0509s000 : Display Setups by Lead Cnt and Config [101]", has a menu bar and toolbar. It shows a "Hardware Setups" tab with "Details" selected. It displays a table with columns: Setup ID, Tester, Handler, Loadboard, Dutboard, Mapboard, and Contactor. The "Lead Count" is 44 and "Configuration" is 44TQFP_10. The table lists various hardware setups with their respective details.

Setup ID	Tester	Handler	Loadboard	Dutboard	Mapboard	Contactor
78	ADVANTEST	SUB	14-A4890			14-A1062
37	DATA IO			14-A0131	14-A2845	
38	DATA IO				14-A2845	
133	ETS300	CAS	14-A9016			14-A1961
57	FLEX	CAS	14-A3903			14-A1062
112	FLEX_MICRO	CAS	14-A3903			14-A1961
9	J750	CAS	14-A0253			14-A1062
10	J750	CAS	14-A0253			14-A1062
1	J750	CAS	14-A0287			14-A1062
2	J750	CAS	14-A0287			14-A1062



Create PSI Test Revision - 8

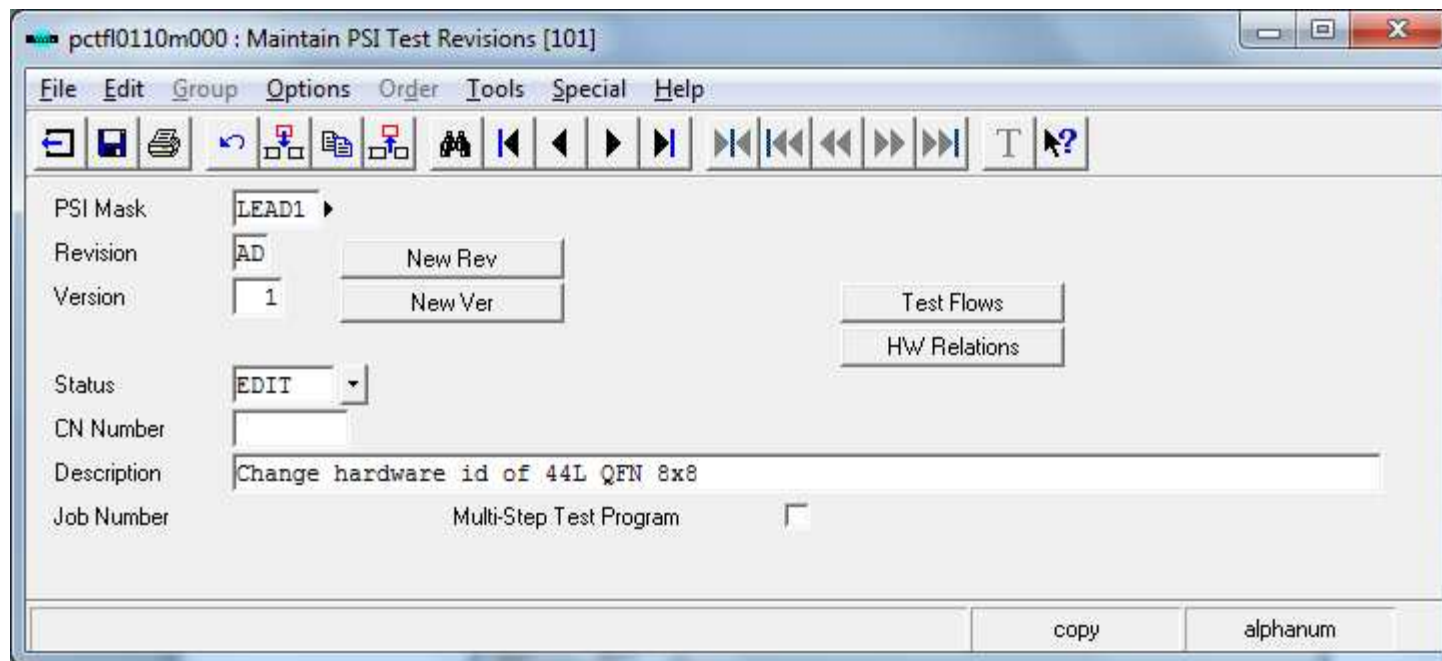
- Add the next Hardware Setup.
- Click Save & Exit button after finished adding all Hardware Setups you needed (need to have at least 1 Setup per Package Type).





Create PSI Test Revision - 9

- Click Test Flows button to start adding Test Flow Group.





Create PSI Test Revision - 10

- Click  Bowtie button then select Flow Type.
- The default Flow Type is “STD” (Standard Production Flow).

pctf0112s000 : Maintain Test Flows [101]

File Edit Group Options Order Tools Special Help

Form 1 Form 2

PSI Mask LEAD1 Revision AD Version 1 Steps Test Location

Flow Type STD

Copy Flow

Flow Number	Mont	Bank	Flow	Flow Group	Process Plan Name	Description	Test Location
	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>				
	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>				
	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>				
	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>				
	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>				
	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>				
	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>				
	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>				
	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>				
	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>				

enum



Create PSI Test Revision - 12

- **Flags :**

Mont : “Monitor” – Tick on special flow

[Ex : - QC Test on every 15 lots

(STD Flow : FQ , Mont Flow : FQQQ)

- Keep the previous test flow for the lot in pipeline]

Note : Assembly loader can't see this flow when issues the AI (Assembly Instruction).

Config Bank : Tick when Test Flow is shared across many configurations of the product.

(For Micrel – TCG (MEMS) products)

STRIP Flow : Tick when there is at least 1 Strip Test Step in the Test Flow



Create PSI Test Revision - 14

- Press Tab key to add the 2nd Flow Group,
- After finish adding all Test Flow Groups, select one test flow then click Steps.

The screenshot shows the 'Maintain Test Flows' window with the following details:

- PSI Mask: LEAD1
- Revision: AD
- Version: 1
- Flow Type: SID
- Buttons: Steps, Test Location, Copy Flow
- Table with columns: Flow Number, Mont, Config Bank, STRIP Flow, Flow Group, Process Plan Name, Description, Test Location

Flow Number	Mont	Config Bank	STRIP Flow	Flow Group	Process Plan Name	Description	Test Location
296947	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	INDUSTRIAL OTP	FQ	Standard Production Flow	
296948	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	INDUSTRIAL OTP	FQQQ	QC Monitoring Flow	
	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>				
	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>				
	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>				
	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>				
	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>				
	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>				
	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>				
	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>				
	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>				



Create PSI Test Revision - 15

- Select Test Temperature for each Test Step.

The screenshot shows a software window titled "pctf0113s000 : Maintain Flow Steps [101]". The window has a menu bar with "File", "Edit", "Group", "Options", "Order", "Tools", "Special", and "Help". Below the menu bar is a toolbar with various icons for file operations and navigation. The main area contains a form with the following fields:

- PSI Mask: LEAD1
- Revision: AD
- Version: 1
- Flow No.: 296947
- Standard Production Flow

Below these fields is a table with the following columns: Step No., Step Name, Temp, Time(hr), Rule, Prog Type, Opts, Special Instructions, and Legacy Op Nbr. The table contains two rows of data:

Step No.	Step Name	Temp	Time(hr)	Rule	Prog Type	Opts	Special Instructions	Legacy Op Nbr
1	ET1		0.00	N/A		0		
2	QC1		0.00	N/A		0		



Create PSI Test Revision - 16

- **Recipe Time (hr)** – Required for Burn-In, Endurance and Retention Bake steps. Not allowed on the other steps. The default value is 0.00.

The screenshot shows a software window titled "pctf0113s000 : Maintain Flow Steps [101]". The window has a menu bar (File, Edit, Group, Options, Order, Tools, Special, Help) and a toolbar with various icons. Below the toolbar, there are input fields for "PSI Mask" (LEAD1), "Revision" (AD), "Version" (0), "Flow No." (179207), and "DLT MONITOR". The main area contains a table with columns: Step No., Step Name, Temp, Recipe Time(hr), Cksum Rule, Prog Type, Options (Opts, Special Instructions), and Legacy Op Nbr. A red box highlights the "Recipe Time(hr)" column.

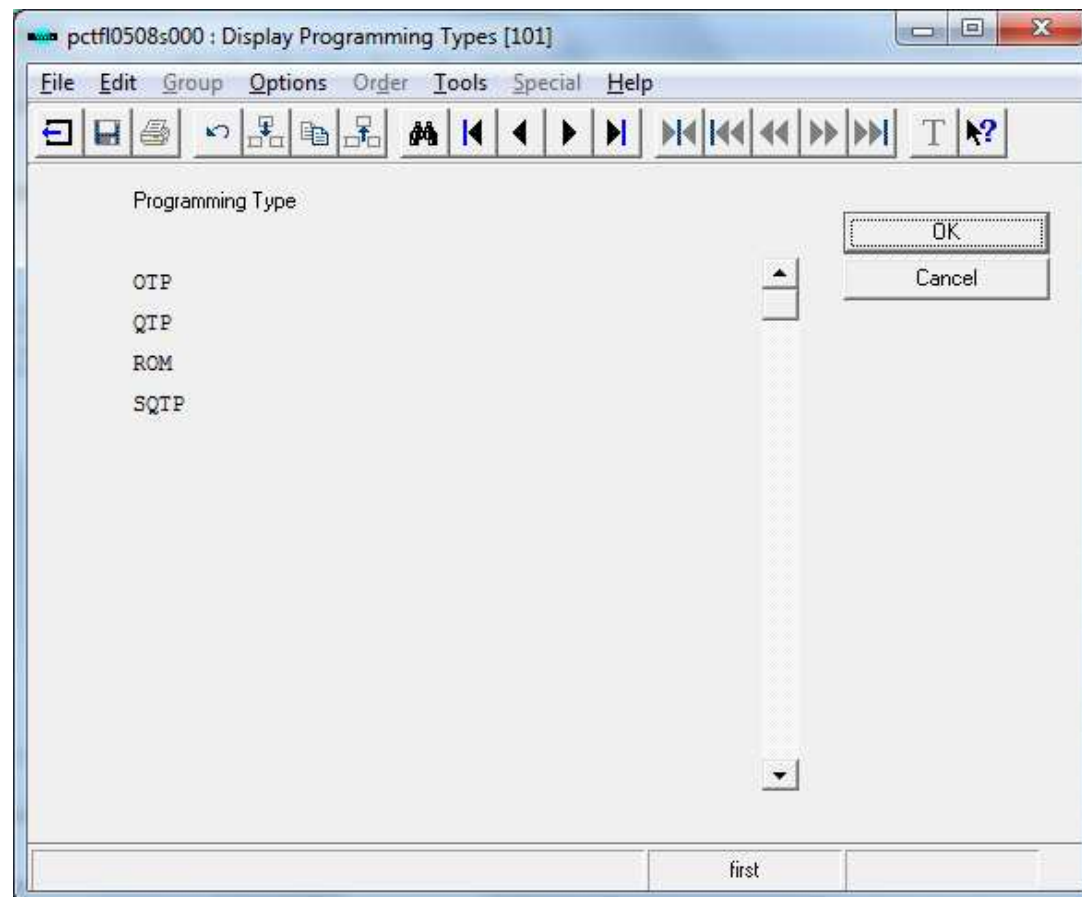
Step No.	Step Name	Temp	Recipe Time(hr)	Cksum Rule	Prog Type	Options	Legacy Op Nbr
1	BI1	150C	96.00	N/A	OTP	0	
2	FT1	25C	0.00	N/A	OTP	1	
3	FT2	25C	0.00	N/A	OTP	1	
4	FT3	125C	0.00	N/A	OTP	1	
5	FT4	25C	0.00	N/A	OTP	1	
6	FT5	25C	0.00	N/A	OTP	1	
7	BI2	150C	408.00	N/A	OTP	0	
8	FT6	25C	0.00	N/A	OTP	1	
9	FT7	25C	0.00	N/A	OTP	1	
10	FT8	125C	0.00	N/A	OTP	1	



Create PSI Test Revision - 18

- **Prog Type** (Programming Type) is the part that will be shown on Test Setup Sheet at each test step.

There are 5 options : **Blank (default), ROM, OTP, QTP, SQTP.**





Create PSI Test Revision - 19

- We can put Special Instructions for each test step but it will be shown on Test Traveler only. 

TestTrav_LEAD1TT4X030_245848_20161030_215841.pdf - Adobe Reader

File Edit View Window Help

3 / 4 124%

Tools Sign Comment

MPC: LEAD1TT4X030 FLOW#: 245848 REV: AC VER: 0 LOT: -

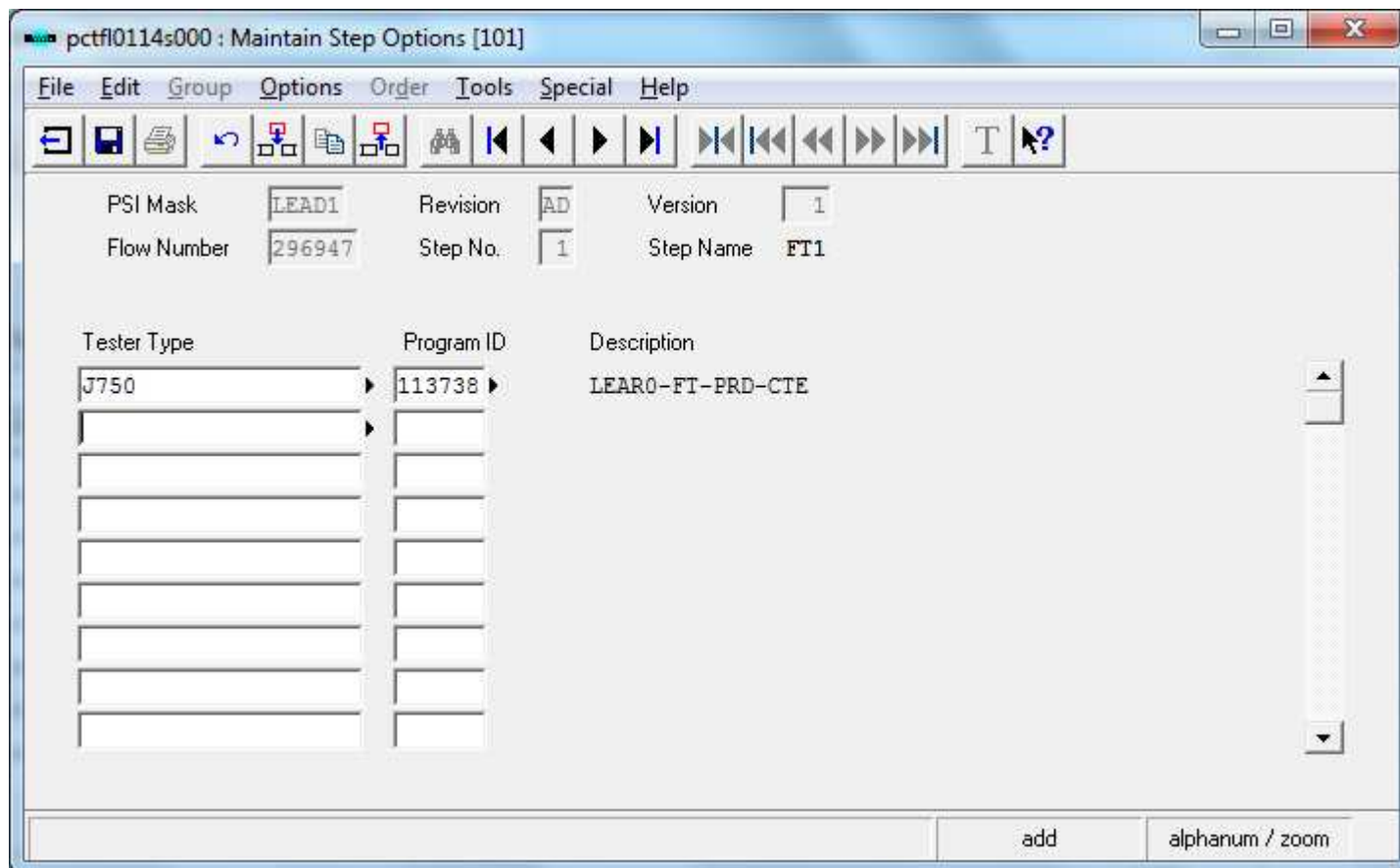
FT3: FINAL TEST (PI-91139, PI-92001, PI-92002)

FIRST TEST	RETEST	TOTAL	TEST PROGRAM	
QTY IN:	QTY IN:	QTY IN:	TESTER ID:	PM DUE:
QTY OUT BIN1:	QTY OUT BIN1:	QTY OUT BIN1:	HANDLER ID:	PM DUE:
QTY OUT BIN2:	QTY OUT BIN2:	QTY OUT BIN2:	TEMPERATURE:	
YIELD:	YIELD:	YIELD:	SOAK TIME:	
REJECTS:	REJECTS:	REJECTS:	LOADBOARD ID:	PM DUE:
PA:	PA:	PA:	CONTACTOR ID:	PM DUE:
FU:	FU:	FU:	ESD BOX ID:	PM DUE:
OS:	OS:	OS:	SETUP OPR:	
PAT:	PAT:	PAT:	PROMATE CSUM: 0339	
VM:	VM:	VM:	CSUM: 1: 2:	BY:
LOSS:	LOSS:	LOSS:	CSUM(IF DOWN): 1: 2:	BY:
DROP:	DROP:	DROP:	OPR1 NO.:	DATE/SHIFT:
			OPR2 NO.:	DATE/SHIFT:
CTRL YIELD: 0.00			OPR3 NO.:	DATE/SHIFT:
PPO:	MACHINE PERFORMANCE: SITE=	PROBLEM=:		
SPECIAL INSTRUCTIONS:				



Create PSI Test Revision - 21

- Click Insert button. Select Tester Type, input Program ID.
- If you did not input Program ID description previously, it will be very hard to tell if you selected the right Program ID.





Create PSI Test Revision - 22

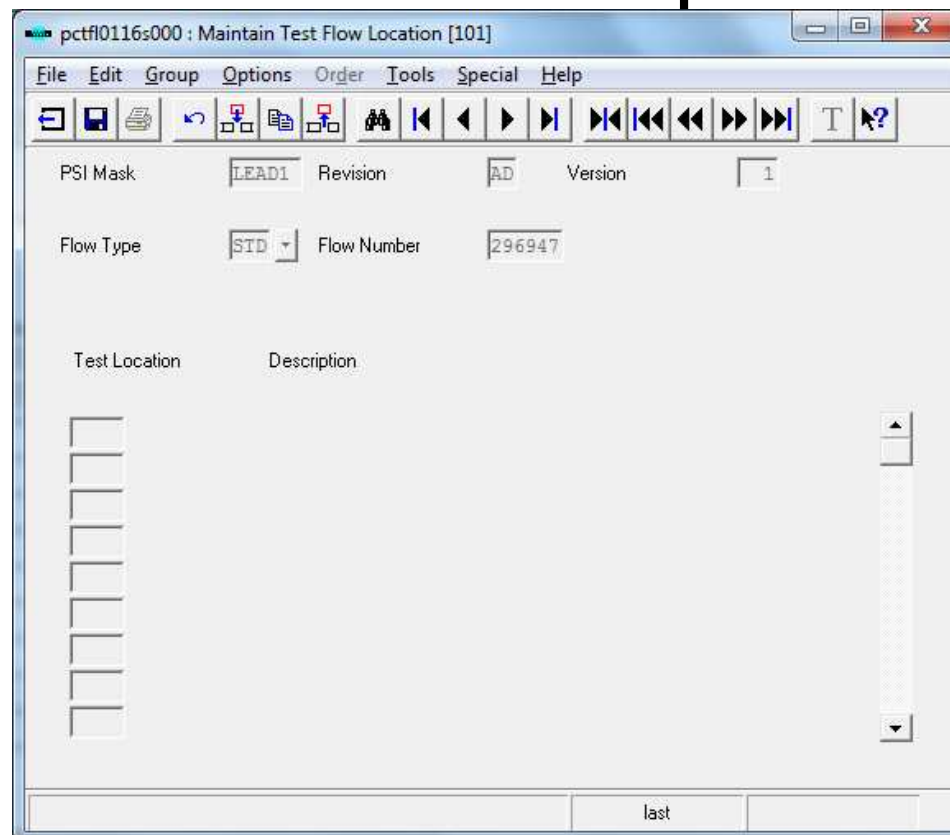
- If we can test the part on >1 Tester Types, input another Tester Type and Program ID.
- Click Save & Exit button.
- Assign Test Options for the remaining steps.

Tester Type	Program ID	Description
J750	113738	LEAR0-FT-PRD-CTE
LTX_D2X	124604	LEAR0.STRIP.D2X.FT-PRD-CTE_ROOM-CONTACTOR



Create PSI Test Revision - 24

- Click insert to add Test Location button.
- Can put > 1 Test Locations.
- Add Test Location for the rest of test flows.
- Click Save & Exit button after completed.





Create PSI Test Revision - 25

- Example : LEAD1 (MCU) Test Flow Groups.

The screenshot shows the 'Maintain Test Flows' window for 'pctf0112:000'. The interface includes a menu bar (File, Edit, Group, Options, Order, Tools, Special, Help) and a toolbar with various navigation icons. The main area displays the following information:

- PSI Mask: LEAD1
- Revision: AD
- Version: 0
- Flow Type: STD
- Buttons: Steps, Test Location, Copy Flow

Flow Number	Mont	Config Bank	STRIP Flow	Flow Group	Process Plan Name	Description	Test Location
178939	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	EXTENDED OTP	FFQQ	EXTENDED OTP	MTAI,
178937	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	INDUSTRIAL OTP	FFQQ	INDUSTRIAL OTP	MTAI,
205091	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	INDUSTRIAL QTP	FFQQFR	INDUSTRIAL QTP	MTAI,
212095	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	INDUSTRIAL SQIP	FFQQFR	INDUSTRIAL SQIP	MTAI,
161760	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	NO TEST REQUIREMENT	Y	NO TEST REQUIREMENT	
178943	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	STRIP EXTENDED OTP	FFQQ	EXTENDED OTP	MTAI,
178944	<input type="checkbox"/>	<input type="checkbox"/>	<input checked="" type="checkbox"/>	STRIP EXTENDED OTP	SIQ	STRIP EXTENDED OTP	MTAI,
244913	<input type="checkbox"/>	<input type="checkbox"/>	<input checked="" type="checkbox"/>	STRIP EXTENDED OTP	SIT	STRIP EXTENDED OTP [QC COLI	MTAI,
228038	<input type="checkbox"/>	<input type="checkbox"/>	<input checked="" type="checkbox"/>	STRIP EXTENDED OTP CERLER	SIQ	STRIP EXTENDED OTP CERLER	MTAI,
228039	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	STRIP EXTENDED OTP CERLER	FFQQ	EXTENDED OTP CERLER	MTAI,
244914	<input type="checkbox"/>	<input type="checkbox"/>	<input checked="" type="checkbox"/>	STRIP EXTENDED OTP CERLER	SIT	STRIP EXTENDED OTP CERLER	MTAI,
179592	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	STRIP INDUSTRIAL OTP	FFQQ	INDUSTRIAL OTP	MTAI,



Create PSI Test Revision - 26

- Example : TA201 (HMID) Test Flow Groups.

The screenshot shows the 'Maintain Test Flows' window for PSI Mask TA201, Revision B, Version 0. The interface includes a menu bar (File, Edit, Group, Options, Order, Tools, Special, Help) and a toolbar with various navigation and editing icons. The main area displays a table of test flow groups with columns for Flow Number, Mont, Bank, Flow, Flow Group, Process Plan Name, Description, and Test Location. A 'Copy Flow' button is visible above the table.

Flow Number	Mont	Bank	Flow	Flow Group	Process Plan Name	Description	Test Location
216723	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	CAP1006 - FT/QC@25C	FQ	CAP1006 : FT@25C --> QC@25C	SIGI,
216724	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	CAP1028 - FT/QC@50C	FQ	CAP1028 : FT@50C --> QC@50C	SIGI,
216725	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	CAP1066 - FT/QC@50C	FQ	CAP1066 : FT@50C --> QC@50C	SIGI,
216726	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	CAP1088 - FT/QC@50C	FQ	CAP1088 : FT@50C --> QC@50C	SIGI,
216386	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	NO INTERNAL TEST REQUIREMENT	Y	FOR FINAL TEST	
216387	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	NO TEST REQUIREMENT	Y	FOR DIE/WAFER SALE, BUY/RE:	
	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>				
	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>				
	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>				
	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>				
	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>				
	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>				
	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>				



Create PSI Test Revision - 27

- Example : DFAW1 (Memory) Test Flow Groups.

pctf0112:000 : Maintain Test Flows [101]

File Edit Group Options Order Tools Special Help

PSI Mask **DFAW1** Revision **AS** Version **0** Steps Test Location

Flow Type **STD** Copy Flow

Flow Number	Mont	Config Bank	STRIP Flow	Flow Group	Process Plan Name	Description	Test Location
179616	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	COMM CAM3 AA	FQ	COMM CAM3 AA	MTAI,
166857	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	COMM CAM3 AA STRIP	FQ	COMM CAM3 AA SINGULATED TE:	MTAI,
166858	<input type="checkbox"/>	<input type="checkbox"/>	<input checked="" type="checkbox"/>	COMM CAM3 AA STRIP	I	COMM CAM3 AA STRIP TEST	MTAI,
166860	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	COMM CAM3 AA STRIP	FQQ	COMM CAM3 AA SINGULATED TE:	MTAI,
166874	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input checked="" type="checkbox"/>	COMM CAM3 AA STRIP	IQ	COMM CAM3 AA STRIP TEST MOI	MTAI,
231761	<input type="checkbox"/>	<input type="checkbox"/>	<input checked="" type="checkbox"/>	COMM CAM3 AA STRIP M1	I	COMM CAM3 AA STRIP M1	MTAI,
231762	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	COMM CAM3 AA STRIP M1	FQ	COMM CAM3 AA SINGULATED M1	MTAI,
231763	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input checked="" type="checkbox"/>	COMM CAM3 AA STRIP M1	IQ	COMM CAM3 AA STRIP M1 MONI	MTAI,
231764	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	COMM CAM3 AA STRIP M1	FQQ	COMM CAM3 AA SINGULATED M1	MTAI,
214637	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	COMM CAM3 AA STRIP M2	FQ	COMM CAM3 AA SINGULATED M2	MTAI,
214638	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	COMM CAM3 AA STRIP M2	FQQQ	COMM CAM3 AA SINGULATED M2	MTAI,
280242	<input type="checkbox"/>	<input type="checkbox"/>	<input checked="" type="checkbox"/>	COMM CAM3 AA STRIP M2	I	COMM CAM3 AA STRIP M2 - DE	MTAI,



Create PSI Test Revision - 28

- Example : 34161 (MPU32) Test Flow Groups.

The screenshot shows the 'Maintain Test Flows' application window. The title bar reads 'pctf0112:000 : Maintain Test Flows [101]'. The menu bar includes 'File', 'Edit', 'Group', 'Options', 'Order', 'Tools', 'Special', and 'Help'. The toolbar contains various icons for navigation and editing. The main window displays the following information:

PSI Mask: 34161 Revision: B Version: 0

Flow Type: STD

Buttons: Steps, Test Location, Copy Flow

Flow Number	Mont	Config Bank	STRIP Flow	Flow Group	Process Plan Name	Description	Test Location
296077	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	IND_FFQ_-40C_85C_85C_S0101B-CU	FFQ	34161FFQ-40C85C85CSINGLE	ASE9,
296076	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	IND_FFQ_-40C_85C_85C_S051B-CU	FFQ	34161FFQ-40C85C85CSINGLE	ASE9,
281415	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	NO TEST REQUIREMENT	Y	WAFER DIE SALES	
282633	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	NOT INTEGRATED - ATMEL	Y	FOR FINAL TEST	
	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>				
	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>				
	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>				
	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>				
	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>				
	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>				
	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>				
	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>				
	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>				
	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>				
	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>				
	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>				



Create PSI Test Revision - 29

- Example : 29657 (SCBU) Test Flow Groups.

pctf0112s000 : Maintain Test Flows [101]

File Edit Group Options Order Tools Special Help

Form 1 Form 2

PSI Mask 29657 Revision B Version 0 Steps Test Location

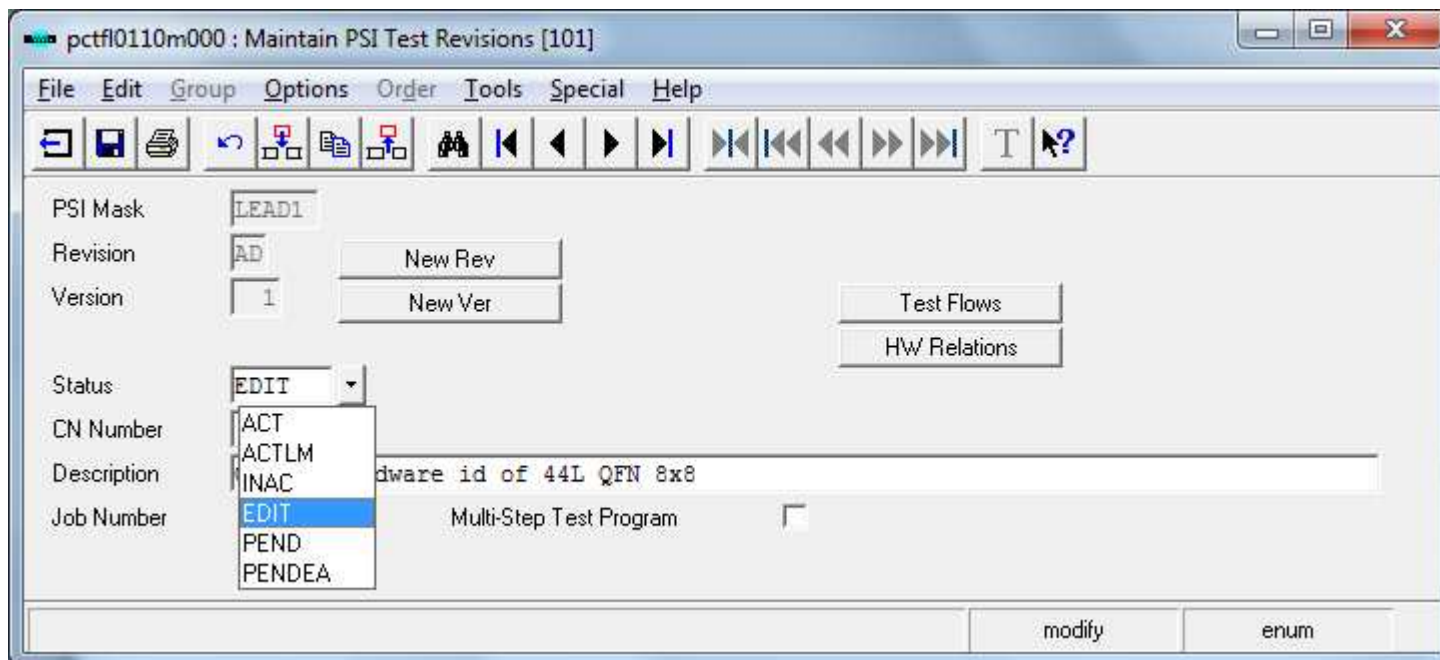
Flow Type STD Copy Flow

Flow Number	Mont	Config Bank	STRIP Flow	Flow Group	Process Plan Name	Description	Test Location
296444	<input type="checkbox"/>	<input type="checkbox"/>	<input checked="" type="checkbox"/>	IND_I_25C_150SOIC_STRIP	I	29657I25CSTRIP	ASSH,
296445	<input type="checkbox"/>	<input type="checkbox"/>	<input checked="" type="checkbox"/>	IND_I_25C_150SOIC_STRIP_13	I	29657I25CSTRIP	ASSH,
281373	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	NO TEST REQUIREMENT	Y	WAFER DIE SALES	
282591	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	NOT INTEGRATED - AIMEL	Y	FOR FINAL TEST	
	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>				
	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>				
	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>				
	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>				
	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>				
	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>				
	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>				
	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>				
	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>				
	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>				
	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>				



Create PSI Test Revision - 30

- Click “Save” button.
- Change PSI Test Revision Status from EDIT to PEND then click “Save” button.
- If there is missing/incomplete data, the system will not allow to change the status from EDIT to PEND.





Create PSI Test Revision - 31

- After the CN to release PSI Test Revision has been approved, MThai Document Control team will put CN Number and change Status from PEND to ACT.

pctf0110m000 : Maintain PSI Test Revisions [101]

File Edit Group Options Order Tools Special Help

PSI Mask LEAD1

Revision AD New Rev

Version 0 New Ver

Status ACT

CN Number 1701736

Description Change hardware id of 44L QFN 8x8

Job Number Multi-Step Test Program

Test Flows

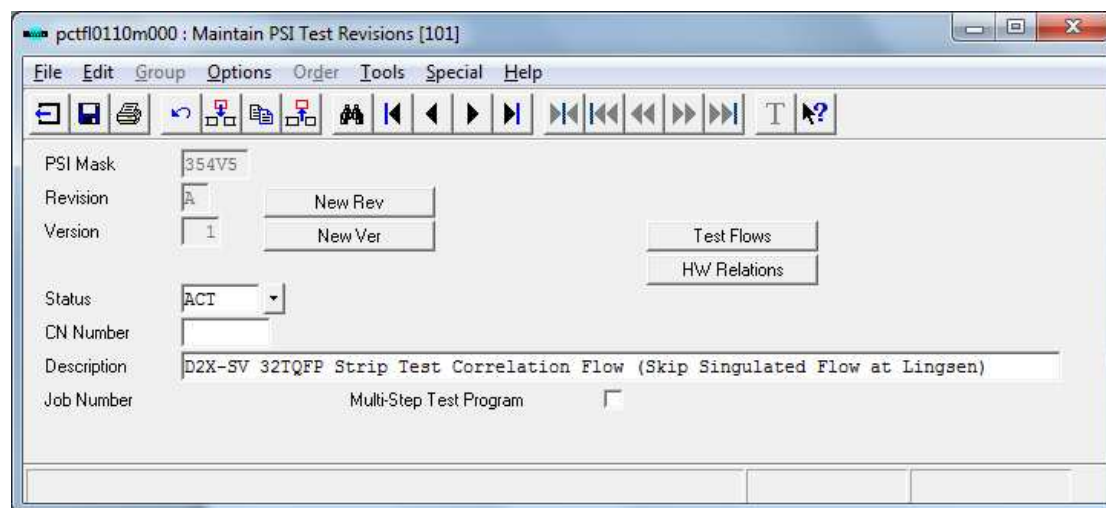
HW Relations

- When you need to release/revise the new Test Flow/Test Hardware to production, click “New Rev” button.
- Update Description then update Test Flows/HW Relations.



Create PSI Test Revision - 32

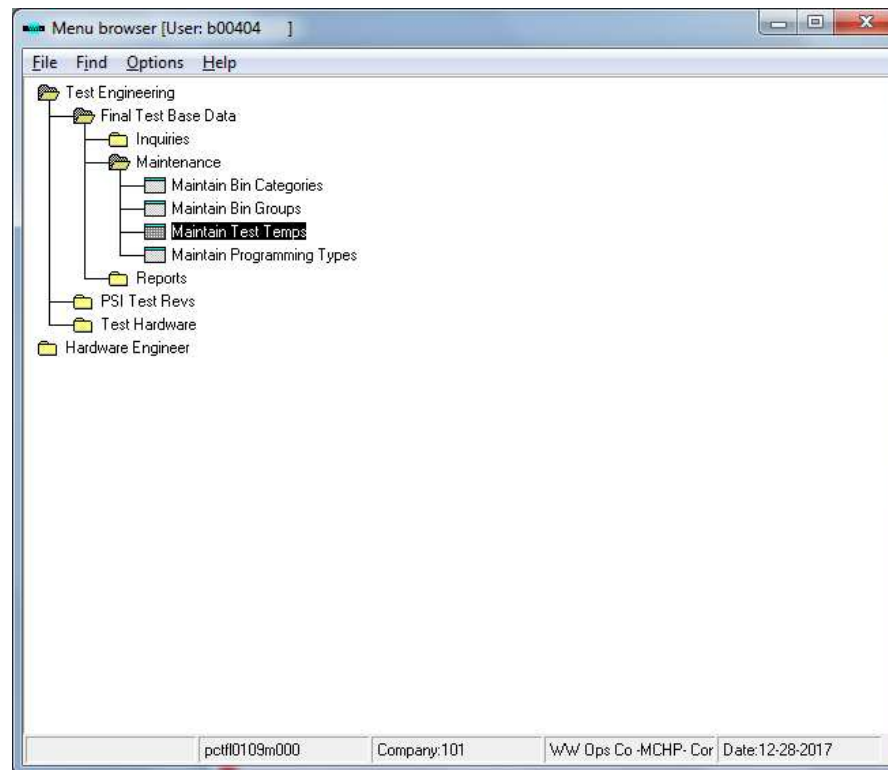
- When you need to create engineering Test Flow for correlation lot, data collection, etc. Click “New Ver” button.
- Update Description then click “Test Flows” button.
- Delete all Flow Groups that you don’t use them from every Flow Type.
- Press “Shift” key and select all Flow Groups.
- Click “Delete” button.
- Start adding engineering flow.





Add New Test Temperature - 1

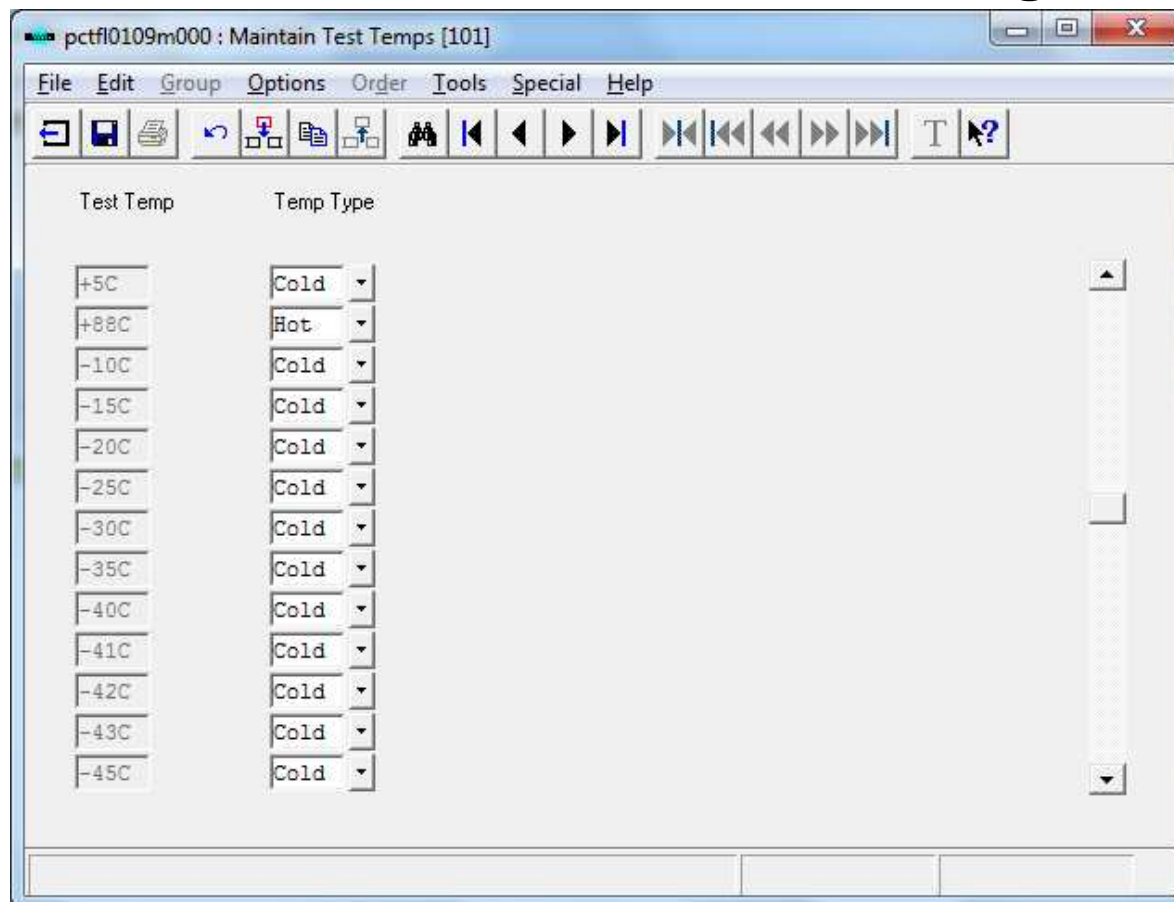
- In case, you need Test Temperature that does not exist in PDC, Here is the procedure to add it.
- Under “Test Engineering”, Double clicks “Final Test Base Data”.
- Double clicks “Maintenance” then “Maintain Test Temps”.





Add New Test Temperature - 2

- Click  insert button.
- Add new Test Temperature and select Temperature Type.
- Click  Save & Exit button after finish adding it.





Tool for Reviewing PDC Data

- After finish creation relationship among Hardware Setups, Test Flows, Program IDs at PSI Test Revision, you can print out the reports to review.
- If find any error, you can go back to correct it.
(Need to change Status from PEND to EDIT before make any change.)
- We also print out these reports to attach to the Test Change Notice (eCN).
- The tool is called “**Approval Helper**”.
<http://beweb/MTAI/BaanApprovalHelper.aspx>



Print out the list of Program IDs - 1

- Input Mask number, select Test Rev. and Test Vers. then click Test Program Revision button.

Mask: LEAD1
Test Rev.: AD - ACT
Test Vers.: 0

Test Flow Options Ex
Test Hardware Setup
Test Program Revision

Fill Main Source 1 & Main Source 2
M/S 1:
M/S 2:
 Include not use in TestFlow
Program Revs. By Main Source

Mask:
MPCs and Flow Group by Mask
PSI Revision Compare
PSI Hardware Compare



Print out the list of Program IDs - 2

- Select the Tester Type(s) (If there is >1 Tester Types).

The screenshot shows the 'Approval Helper' web application. The browser address bar displays the URL 'http://beweb/MTAI/BaanApprovalHelper.aspx'. The page title is 'Approval Helper'. The main content area is a form with various fields and buttons. A modal dialog box titled 'Select Tester Type' is open in the center, showing a list of checkboxes: 'All Tester Type', 'LTX_D2X', and 'J750'. The 'All Tester Type' checkbox is checked. A 'Continue' button is at the bottom of the dialog. The background form includes fields for 'Mask', 'Test Rev.', 'Test Vers.', and 'Test Flow Options', along with buttons for 'Test Hardware Setup' and 'Test Program Revision'.



Print out the list of Program IDs - 3

- You can save the report in pdf, Word, Excel file to review.

Microchip Technology Thailand Approval Helper
The Embedded Control Solutions C Manufacturing Project | Control Yield | Home

AOQ | Yield Report | Engineering | Production | SCP Reports | Subcon Production | MPD - QC Monitor | Equipment | Demo | Others | IE | Control Yield | Engineering Project | Smart Box

Mask: LEAD1
Test Rev: AD - ACT
Test Vers: 0

1 / 2 Main Report 100%

Program Revs by Mask LEAD1

TESTER: J750

Program ID	Rev	Vers	Date Mod	Time Mod	Main Source	Executable	Checksum	BIN Group	OS Version	Status	Badge
113606	BC	0	06/06/2017	21:33:53	LEAR0_FT_A48e.XLS	FT-PRD-STD	16540E2	STANDARD		ACT	b01861
113607	BC	0	06/06/2017	21:34:05	LEAR0_FT_A48e.XLS	QC-PRD-STD	16540E2	STANDARD		ACT	b01861
113720	BA	0	06/06/2017	21:35:13	LEAR0_FT_A48e.xls	ft-res-qtq	16540E2	STANDARD		ACT	b01861
113738	AZ	0	06/06/2017	21:35:34	LEAR0_FT_A48e.XLS	FT-PRD-CTE	16540E2	STANDARD		ACT	b01861
113759	AZ	0	06/06/2017	21:36:03	LEAR0_FT_A48e.XLS	QC-PRD-CTE	16540E2	STANDARD		ACT	b01861
114107	BB	0	06/06/2017	21:36:24	LEAR0_FT_A48e.XLS	ft-prd-cte	16540E2	STANDARD		ACT	b01861
HW LIMITS: Lead: 28 Config: 300SOIC Setup ID: 139 Loadboard: 14-A4243 : LB Teradyne 18F25K20, 18F24K20, 18F26K20 x32 28L SOIC 300 MCT-TAPESTRY Handler: PH1 Contactor: 14-A1898 : CT x32 28L SOIC 300 MCT ChannelMap: x32mct28soic											
HW LIMITS: Lead: 28 Config: 300SOIC Setup ID: 162 Loadboard: 14-A4243 : LB Teradyne 18F25K20, 18F24K20, 18F26K20 x32 28L SOIC 300 MCT-TAPESTRY Handler: SCH Contactor: 14-A1898 : CT x32 28L SOIC 300 MCT ChannelMap: x32mct28soic											
HW LIMITS: Lead: 28 Config: QFN_8X8 Setup ID: 91 Loadboard: 14-A4244 : LB Teradyne 18F25K20, 18F24K20, 18F26K20 x21 28L QFN 8X8 MCT-TAPESTRY Handler: SCH Contactor: 14-A3974 : CT TERADYNE QFN 8X8 28L MCT-TAPESTRY ChannelMap: x21mct28qfn											
HW LIMITS: Lead: 28 Config: SSOP Setup ID: 77 Loadboard: 14-A4122 : LB Teradyne PIC18F24K20/25K20/26K20 x24 28L SSOP 209 MCT-TAPESTRY Handler: SCH Contactor: 14-A3919 : CT 28L SSOP x 48 MCT-TAPESTRY ChannelMap: x24mct28ssop											
HW LIMITS: Lead: 44 Config: 44TQFP_10 Setup ID: 83 Loadboard: 14-A4254 : ST J750 18F45K20/18F46K20 x20 44L TQFP 10X10X1 MCT-TAPESTRY Handler: SCH Contactor: 14-A2834 : CONTACTOR 44TQFP STRIPTEST ChannelMap: x20mct44tqfp											
HW LIMITS: Lead: 44 Config: QFN_8X8 Setup ID: 105 Loadboard: 14-A4838 : ST J750 PIC18F45K20 x25 44L QFN 8X8 MCT-TAPESTRY Handler: SCH Contactor: 14-A4091 : CT X25 44L QFN 8X8 MCT ChannelMap: x25mct44qfn											
114137	AX	0	06/06/2017	21:37:03	LEAR0_FT_A48e.xls	ft-qtq-std	16540E2	STANDARD		ACT	b01861
114138	AX	0	06/06/2017	21:37:17	LEAR0_FT_A48e.xls	ft-sqtp-std	16540E2	STANDARD		ACT	b01861
114139	AX	0	06/06/2017	21:37:30	LEAR0_FT_A48e.xls	ft-res-sqtp	16540E2	STANDARD		ACT	b01861
114593	AU	0	06/06/2017	21:39:18	LEAR0_FT_A48e.XLS	E2-QUAL-DLT-WR	16540E2	STANDARD		ACT	b01861
114594	AU	0	06/06/2017	21:39:35	LEAR0_FT_A48e.xls	E2-QUAL-DLT-POST	16540E2	STANDARD		ACT	b01861
116649	AL	0	06/06/2017	21:41:52	LEAR0_FT_A48e.XLS	E2-QUAL-DLT-POST	16540E2	STANDARD		ACT	b01861
119277	Z	0	06/06/2017	21:46:51	LEAR0_FT_A48e.XLS	FT-SQTP-STD	16540E2	STANDARD		ACT	b01861
119278	Z	0	06/06/2017	21:47:06	LEAR0_FT_A48e.XLS	FT-RES-SQTP	16540E2	STANDARD		ACT	b01861
121364	Y	0	06/06/2017	21:48:12	LEAR0_FT_A48e.XLS	qc-prd-cte	16540E2	STANDARD		ACT	b01861
HW LIMITS: Lead: 28 Config: SSOP Setup ID: 77 Loadboard: 14-A4122 : LB Teradyne PIC18F24K20/25K20/26K20 x24 28L SSOP 209 MCT-TAPESTRY Handler: SCH Contactor: 14-A3919 : CT 28L SSOP x 48 MCT-TAPESTRY ChannelMap: x24mct28ssop											
HW LIMITS: Lead: 44 Config: 44TQFP_10 Setup ID: 83 Loadboard: 14-A4254 : ST J750 18F45K20/18F46K20 x20 44L TQFP 10X10X1 MCT-TAPESTRY											



Print out the list of Program IDs - 4

- In case that TP supports >1 Masks.

The screenshot shows the 'Approval Helper' web application interface. The browser address bar displays 'http://beweb/MTA/BaanApprovalHelper.aspx'. The page header includes 'Microchip Technology Thailand' and 'Approval Helper'. A navigation menu at the top lists various options like 'Yield Report', 'Engineering', 'Production', etc. On the left side, there are input fields for 'Mask' (C5035), 'Test Rev.' (AC - ACT), and 'Test Vers.' (0). Below these are buttons for 'Test Flow Options', 'Test Hardware Setup', and 'Test Program Revision'. The main content area is titled 'Program Revs by Main Source' and shows a table of program revisions. The table has columns for Program ID, Rev, Vers, Date Mod, Time Mod, Main Source, Executable, Checksum, BIN Group, OS Version, Status, and Badge. Below the table, there is a section for 'PSI Mask by Main Source' listing masks C5035, C5B7, and C5B8.

Program ID	Rev	Vers	Date Mod	Time Mod	Main Source	Executable	Checksum	BIN Group	OS Version	Status	Badge
130972	A	0	01/23/2018	0:53:24	C5BH0_USDPGX_A0.una	f1-prd-std	483517451	STANDARD		ACT	b01861
HW LIMITS: Lead: 20 Config: SSOP Setup ID: 188 Handler: SCH Loadboard: 14-A9084 : ST CREDENCE DIAMOND D2X 16F1827 x168 20L SSOP MCT-TAPESTRY Contactor: 14-A9085 : CTx168 20L SSOP 209MIL MCT-TAPESTR (-40C/25C) ChannelMap: x168mct20ssop											
130973	A	0	01/23/2018	0:54:01	C5BH0_USDPGX_A0.una	f1-prd-std-lv	483517451	STANDARD		ACT	b01861
HW LIMITS: Lead: 20 Config: SSOP Setup ID: 188 Handler: SCH Loadboard: 14-A9084 : ST CREDENCE DIAMOND D2X 16F1827 x168 20L SSOP MCT-TAPESTRY Contactor: 14-A9085 : CTx168 20L SSOP 209MIL MCT-TAPESTR (-40C/25C) ChannelMap: x168mct20ssop											
130977	A	0	01/23/2018	0:54:40	C5BH0_USDPGX_A0.una	f1-prd-std	483517451	STANDARD		ACT	b01861
HW LIMITS: Lead: 20 Config: SSOP Setup ID: 189 Handler: SCH Loadboard: 14-A9084 : ST CREDENCE DIAMOND D2X 16F1827 x168 20L SSOP MCT-TAPESTRY Contactor: 14-A9115 : CT x168 20L SSOP 209MIL MCT-TAPESTRY (85C/125C) ChannelMap: x168mct20ssop											
130978	A	0	01/23/2018	0:55:47	C5BH0_USDPGX_A0.una	f1-prd-std-lv	483517451	STANDARD		ACT	b01861
HW LIMITS: Lead: 20 Config: SSOP Setup ID: 189 Handler: SCH Loadboard: 14-A9084 : ST CREDENCE DIAMOND D2X 16F1827 x168 20L SSOP MCT-TAPESTRY Contactor: 14-A9115 : CT x168 20L SSOP 209MIL MCT-TAPESTRY (85C/125C) ChannelMap: x168mct20ssop											

PSI Mask by Main Source
C5035
C5B7
C5B8



Print out the list of HW Setup IDs

- Input Mask number, select Test Rev. and Test Vers. then click Test Hardware Setup button.
- You can save the report in pdf, Word, Excel file to review.

The screenshot shows the 'Approval Helper' web application interface. The browser address bar displays 'http://beweb/MTA/BaanApprovalHelper.aspx'. The page header includes 'Microchip Technology Thailand' and 'Approval Helper'. The main content area is titled 'Test Hardware Options' and displays a table of test hardware configurations. A red box highlights a PDF icon in the top navigation bar, indicating the option to print the report as a PDF file.

Mask:	LEAD1	CN#:	1701736	Date Mod:	10/16/2017
Revision:	AD	Status:	ACT	Time Mod:	18:14:59
Version:		User Mod:	b01861		
Lead/Config: 40 600DIP					
Setup#	Tester	Handler	Loadboard:	14-A2991 : LB Teradyne J750 PIC18F46K20 x4 PDIP 800 DAY3287	Dutboard :
74	J750	DAY	Contacto:	14-A1062 : STANDARD CONTACTOR	Channelmap :
			Cable Set :		Tester Interface :
Setup#	Tester	Handler	Loadboard:	14-A2991 : LB Teradyne J750 PIC18F46K20 x4 PDIP 800 DAY3287	Dutboard :
78	J750	DAY	Contacto:	14-A1062 : STANDARD CONTACTOR	Channelmap :
			Cable Set :		Tester Interface :
Lead/Config: 40 UQFN_5X5					
Setup#	Tester	Handler	Loadboard:	14-A4384 : LB J750 PIC18FL18K20(LEAD1) x4 UQFN 5X5 40L RASCO SO2000	Dutboard :
2	J750	RA2	Contacto:	14-A4385 : CT 40L UQFP 5X5 RASCO	Channelmap :
			Cable Set :		Tester Interface :
Lead/Config: 44 44TQFP_10					
Setup#	Tester	Handler	Loadboard:	14-A3100 : ATQ10X10-44DC-PIC18F4XK20-X4	Dutboard :
44	J750	CAS	Contacto:	14-A1981 : CT 44LTQFP 10X10X1 DELTA CASTLE - L	Channelmap :
			Cable Set :		Tester Interface :
Setup#	Tester	Handler	Loadboard:	14-A3100 : ATQ10X10-44DC-PIC18F4XK20-X4	Dutboard :
45	J750	CAS	Contacto:	14-A1981 : CT 44LTQFP 10X10X1 DELTA CASTLE - L	Channelmap :
			Cable Set :		Tester Interface :
Setup#	Tester	Handler	Loadboard:	14-A4254 : ST J750 18F45K20/18F46K20 x20 44L TQFP 10X10X1 MCT-TAPEST	Dutboard :
63	J750	SCH	Contacto:	14-A2834 : CONTACTOR 44TQFP STRIPTEST	Channelmap :
			Cable Set :		Tester Interface :



Print out Test Flow Options

- Input Mask number, select Test Rev. and Test Vers. then click Test Flow Options button.
- You can save the report in pdf, Word, Excel file to review.

Microchip Technology Thailand
Approval Helper

Mask: LEAD1
Test Rev.: AD - ACT
Test Vers.: 0

Test Flow Options

Test Flow Options: Ex

Test Hardware Setup

Test Program Revision

Fill Main Source 1 & Main Source 2

M/S 1:
M/S 2:

Include not use in TestFlow

Program Revs. By Main Source

Mask:

MPCs and Flow Group by Mask

PSI Revision Compare

PSI Hardware Compare

1 / 26

Main Report

100%

Test Flow Options

CN#: 1701736 Date Mod: 10/16/2017
Mask: LEAD1 Revision: AD Version: 0 Status: ACT Time Mod: 18:14:59
Change Comment: Change hardware id of 44L QFN 8x8 User Mod: b01861

Flow Group: D2X-J750 STRIP EXTENDED OTP CERLER

STD
Flow # 260366 D2X STRIP EXTENDED OTP CERLER X56LF (SIQ) STD
Strip Flow = 'Yes' Config Bank = 'No' Monitor = 'No' Test Location: MTAI,

Tester	Prm ID	Description	NOTE	Legacy Op Nbr
FS1@25C	128239	LEAR0_STRIP.D2X.FT-PRD-CERLER_ROOM-CONTACTOR	NOTE: Please see "Program Revs by Mask" report for program details.	
IS1@125C	128240	LEAR0_STRIP.D2X.FT-PRD-CERLER_HOT-CONTACTOR	NOTE: Please see "Program Revs by Mask" report for program details.	
QC1@-40C	122532	LEAR0_QC-PRD-CERLER-APG	NOTE: Please see "Program Revs by Mask" report for program details.	

Flow # 260368 J750 STRIP EXTENDED OTP CERLER X20LF (SIQ) STD
Strip Flow = 'Yes' Config Bank = 'No' Monitor = 'No' Test Location: MTAI,

Tester	Prm ID	Description	NOTE	Legacy Op Nbr
FS1@25C	122223	LEAR0_STRIP_FT-PRD-CERLER	NOTE: Please see "Program Revs by Mask" report for program details.	
IS1@125C	122530	STRIP_LEAR0_FT-PRD-CERLER-APG	NOTE: Please see "Program Revs by Mask" report for program details.	

QC1@-40C



Assign Test Flow Group to the MPC -- 1

- After Test Flow Groups have been activated in PDC, need to request Engineering Planner to issue CN to assign Test Flow Group to each MPC.
- Each MPC can be linked to only one Test Flow Group.

MPC	Catalog Part Number	Flow_Group	Tester Type	Tester Device	Pattern	Restr Lvl	Rev	Stage	Qcode	Device Checksum	Blank Checksum	CP ON	CP OFF
LEAD14T4XC03	PIC18F46K20-E/PTC03	D2X-J750 STRIP EXTENDED OTP CERLER	LTX_D2X	18F46K20	OTP	CUSTOM	F	REL		0362	0362		
LEAD14T4XC03	PIC18F46K20-E/PTC03	D2X-J750 STRIP EXTENDED OTP CERLER	J750	18F46K20	OTP	CUSTOM	F	REL		0362	0362		
LEAD17T4XB04	PIC18F46K20-VPT	D2X-J750 STRIP INDUSTRIAL OTP	J750	18F46K20	OTP	STANDARD	E	REL		0362	0362		
LEAD17T4XB04	PIC18F46K20-VPT	D2X-J750 STRIP INDUSTRIAL OTP	LTX_D2X	18F46K20	OTP	STANDARD	E	REL		0362	0362		
LEAD17T4X022	PIC18F46K20-VPT022	D2X-J750 STRIP INDUSTRIAL QTP	J750	18F46K20	QTP	CUSTOM	D	REL		0371	362	0371	F8F4
LEAD17T4X022	PIC18F46K20-VPT022	D2X-J750 STRIP INDUSTRIAL QTP	LTX_D2X	18F46K20	QTP	CUSTOM	D	REL		0371	362	0371	F8F4
LEAD14S2XB04	PIC18F46K20-E/P	EXTENDED OTP	J750	18F46K20	OTP	STANDARD	C	REL		0362	0362		
LEAD17S2XB04	PIC18F46K20-VP	INDUSTRIAL OTP	J750	18F46K20	OTP	STANDARD	C	REL		0362	0362		
LEAD14T4XB04	PIC18F46K20-E/PT	STRIP EXTENDED OTP	J750	18F46K20	OTP	STANDARD	D	REL		0362	0362		
LEAD17T3XV01	PIC18F46K20-VMLV01	UTAC 44QFN STRIP INDUSTRIAL OTP	J750	18F46K20	OTP	QS9000	D	REL		0362	0362		



Assign Test Flow Group to the MPC -- 2

MPC	Catalog Part Number	Flow_Group	Tester Type	Tester Device	Pattern	Restr Lvl	Rev	Stage	Qcode	Device Checksum	Blank Checksum	CP ON	CP OFF
XG4017RKXABC	LAN8720AI-CP	LAN7820-OCTAL-FT/QC 100C	LTX_D10	LAN8720	N/A	CUSTOM	N	REL		N/A	N/A		
XG401SRKXA0C	LAN8720A-CP-TR	LAN7820-OCTAL-FT/QC 100C	LTX_D10	LAN8720	N/A	CUSTOM	L	PHAS		N/A	N/A		
XG4011RNXA0B	LAN8710A-EZK	LAN8710-FT/QC 100C	LTX_D10	LAN8710	N/A	CUSTOM	S	REL		N/A	N/A		
XG401SS8XA0C	LAN8710A-EZK-TR	LAN8710-FT/QC 100C	EX_DIGITAL	8710BS4QE3CCSTTR	N/A	CUSTOM	F	REL		N/A	N/A		
XG4017J3XABC	LAN8720AI-CP-ABC	STRIP LAN8720-FT/QC 85C	LTX_D2X	LAN8720A	N/A	CUSTOM	L	REL		N/A	N/A		
XG4017J3XABC	LAN8720AI-CP-ABC	STRIP LAN8720-FT/QC 85C	LTX_D10	LAN8720A	N/A	CUSTOM	L	REL		N/A	N/A		
MPC	Catalog Part Number	Flow_Group	Tester Type	Tester Device	Pattern	Restr Lvl	Rev	Stage	Qcode	Device Checksum	Blank Checksum	CP ON	CP OFF
DFAX71A3XC00	24VL024H/MS	COMM CAM3 VL	NEXTTEST_PT	24VL024H	N/A	STANDARD	A	REL		N/A	N/A		
DFAX7SC5XC00	24VL024HT/ST	COMM CAM3 VL AO	NEXTTEST_PT	24VL024H	N/A	STANDARD	C	REL		N/A	N/A		
DFAX74A3XA00	24LC024H-E/MS	E-TEMP E1 LC	NEXTTEST_PT	24LC024H	N/A	STANDARD	A	REL		N/A	N/A		
DFAX74C5XA00	24LC024H-E/ST	E-TEMP E3 LC	NEXTTEST_PT	24LC024H	N/A	STANDARD	A	REL		N/A	N/A		
DFAX74C2XF00	24AA025E48-E/SN	E-TEMP EAM3 AA EU148	NEXTTEST_PT	24AA025E48	N/A	STANDARD	B	REL		N/A	N/A		
DFAX7YC8XF00	24AA025E48T-E/OT	E-TEMP EAM3 AA EU148 AO	NEXTTEST_PT	24AA025E48_SOT23	N/A	STANDARD	B	REL		N/A	N/A		
DFAX74C2XG00	24AA025E64-E/SN	E-TEMP EAM3 AA EU164	NEXTTEST_PT	24AA025E64	N/A	STANDARD	B	REL		N/A	N/A		
DFAX7YC8XG00	24AA025E64T-E/OT	E-TEMP EAM3 AA EU164 AO	NEXTTEST_PT	24AA025E64_SOT23	N/A	STANDARD	B	REL		N/A	N/A		
DFAX7Y5QXA00	24LC024HT-E/MNY	E-TEMP EAM3 LC STRIP M1 AO	NEXTTEST_PT	24LC024H	N/A	STANDARD	E	REL		N/A	N/A		
DFAX77A3XB00	24AA024H-VMS	IND IAM3 AA	NEXTTEST_PT	24AA024H	N/A	STANDARD	A	REL		N/A	N/A		
DFAX77C2XD00	24AA025E48-VSN	IND IAM3 AA EU148	NEXTTEST_PT	24AA025E48	OTP	STANDARD	B	REL		N/A	N/A		
DFAX7TC8XD00	24AA025E48T-VOT	IND IAM3 AA EU148 AO	NEXTTEST_PT	24AA025E48_SOT23	OTP	STANDARD	C	REL		N/A	N/A		
DFAX77C2XG00	24AA025E64-VSN	IND IAM3 AA EU164	NEXTTEST_PT	24AA025E64	OTP	STANDARD	A	REL		N/A	N/A		
DFAX7TC8XG00	24AA025E64T-VOT	IND IAM3 AA EU164 AO	NEXTTEST_PT	24AA025E64_SOT23	OTP	STANDARD	A	REL		N/A	N/A		
DFAX77C2XB00	24AA024H-VSN	IND IAM3 AA STRIP M1	NEXTTEST_PT	24AA024H	N/A	STANDARD	E	REL		N/A	N/A		
DFAX77C2XB00	24AA024H-VSN	IND IAM3 AA STRIP M1	NEXTTEST_SSV	24AA024H	N/A	STANDARD	E	REL		N/A	N/A		
DFAX77C5XB00	24AA024H-VST	IND IAM3 AA STRIP M1 AO	NEXTTEST_SSV2	24AA024H	N/A	STANDARD	E	REL		N/A	N/A		
DFAX77C5XB00	24AA024H-VST	IND IAM3 AA STRIP M1 AO	NEXTTEST_PT	24AA024H	N/A	STANDARD	E	REL		N/A	N/A		
DFAX77C2XH00	24AA025UID-VSN	IND IAM3 AA UID	NEXTTEST_PT	24AA025UID	OTP	STANDARD	A	REL		N/A	N/A		
DFAX7TC8XH00	24AA025UIDT-VOT	IND IAM3 AA UID AO	NEXTTEST_PT	24AA025UID_SOT23	OTP	STANDARD	A	REL		N/A	N/A		
DFAX77A3XA00	24LC024H-VMS	IND IAM3 LC	NEXTTEST_PT	24LC024H	N/A	STANDARD	A	REL		N/A	N/A		
DFAX77C2XA00	24LC024H-VSN	IND IAM3 LC STRIP M1	NEXTTEST_PT	24LC024H	N/A	STANDARD	C	REL		N/A	N/A		
DFAX77C2XA00	24LC024H-VSN	IND IAM3 LC STRIP M1	NEXTTEST_SSV	24LC024H	N/A	STANDARD	C	REL		N/A	N/A		
DFAX77C5XA00	24LC024H-VST	IND IAM3 LC STRIP M1 AO	NEXTTEST_SSV2	24LC024H	N/A	STANDARD	E	REL		N/A	N/A		
DFAX77C5XA00	24LC024H-VST	IND IAM3 LC STRIP M1 AO	NEXTTEST_PT	24LC024H	N/A	STANDARD	E	REL		N/A	N/A		
DFAX7TC5XC00	24VL024HT-VST		NEXTTEST_PT	24VL024H	N/A	STANDARD	02	NREL		N/A	N/A		



MPC Test Attributes - 1

- Use Tester Device as one of Test Program Options in order to reduce the number of Test Flow Groups.

The screenshot shows the MPC Viewer web application interface. The browser address bar displays the URL: `mchpweb-netapps/Dataviewer/MPC?MPC=LEAD1TT4X030&revision=A`. The main content area is divided into several sections:

- Mask Pattern Attributes:** A table with two columns of attributes and their values.

Pattern	030	SQTP Source	None
Checksum OFF	AA8C	Start Address	
Checksum ON	0339	Byte Count	0
Checksum ROM		Promote Method	None
QCode		Start Value	
Cust Hex ID	FFFFFFFF	Increment	0
SDP Prod ID		Hex File	
SDP Cust ID		SQTP File	
Code Source	E:Mail	Serial Hex File	
Promote Version	PM3V8.91	Status Register	
- MPC Test Attributes:** A table with two columns of attributes and their values, highlighted with a red box.

Tester Type	J750	Test Device	18F46K20
	LTX_D2X		18F46K20
- Essential Element Constraints:** A section header.
- NSCARs:** A section header.
- Wafer Map Communication:** A section header.
- Acquired Part Information:** A section header.
- Assembly Instructions:** A section header.
- Bill Of Material (BOM):** A section header.
- Starting Material # 1:** A table with columns: Component Item, BOM level, CPN, Use Priority, Qty, Effective Date.





Component Item	BOM level	CPN	Use Priority	Qty	Effective Date
LEAD1101XXXX	DIS		10	1	02/21/2015
- CPN:** A section header.
- Note:** - For convenience, a snapshot of the CPN information is shown below.
- MPC data will continue to contain references to CPN data which may no longer be pertinent to the MPC revision when looking at INACTIVE MPC revs.
- Catalog Part Number:** [PIC18F46K20T-I/PT030](#)
- Release to Sample:** No
- CPN Stage:** REL
- Release to Buy-Microchip:** No
- Web Page Part #:** PIC18F46K20
- NCNR Flag:** No
- End Customer:** TRIDONIC GmbH & Co. KG
- Stop Orders:** No



MPC Test Attributes - 2

ID: 114137 Rev: AX Ver: 0 Status: ACT

Verified

Main Source:	LEAR0_FT_A48e.xls	<input type="checkbox"/>
Checksum:	16540E2	<input type="checkbox"/>
Executable Name:	ft-qtp-std 	<input type="checkbox"/>
Part Number:	18F46K20 	<input type="checkbox"/>
Temperature:	IN25C 	<input type="checkbox"/>
Programming Type:	QTP 	<input type="checkbox"/>
Bins:	2-PASS, 3-PA, 4-FU, 5-O/S	<input type="checkbox"/>
Hardware Limits:		<input type="checkbox"/>
Correl. Process Code:		<input type="checkbox"/>
Correlation Good Bin:	0	<input type="checkbox"/>
OS Version		<input type="checkbox"/>
Special Instructions:		<input type="checkbox"/>
CN Number:	1700910	
Comments:		



Print out the list of MPCs - 1

- Go to Approval Helper web.
- Input Mask number then click “MPCs and Flow Group by Mask”.

Microchip Technology Thailand
Approval Helper
Embedded Control Solutions Company

Manufacturing Project | Control Yield | Home

AOQ > Yield Report > Engineering > Production > SCP Reports > Subcon Production > MPD - QC Monitor > Equipment > Demo > Others > IE > Control Yield > Engineering Project > Smart Box

Mask . R .

Test Rev.

Test Vers.

Test Flow Options

Test Hardware Setup

Test Program Revision

Fill Main Source 1 & Main Source 2

M/S 1

M/S 2

Include not use in TestFlow

Program Revs. By Main Source

Mask

MPCs and Flow Group by Mask

PSI Revision Compare

PSI Hardware Compare



Print out the list of MPCs - 2

- You can export the report to Excel, Word, pdf file.

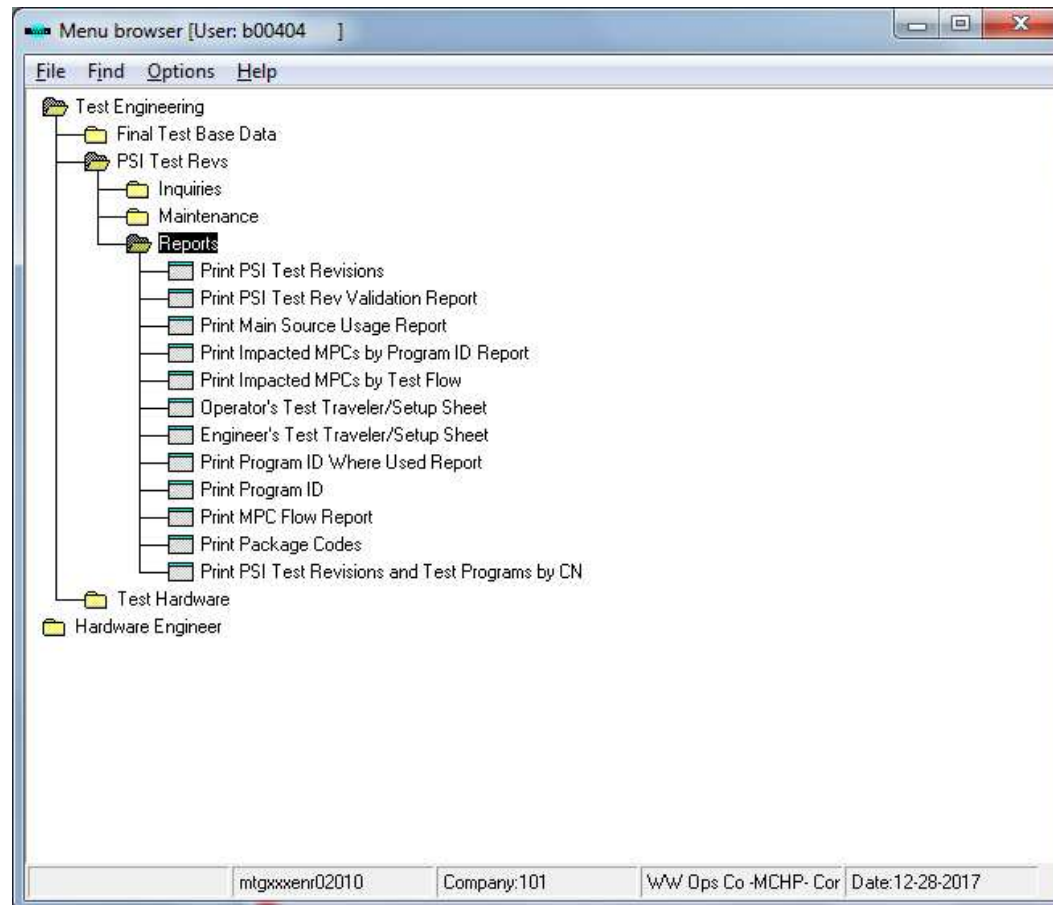
The screenshot shows the 'Approval Helper' web application interface. The main content area displays a table titled 'MPCs and Flow Groups'. The table has columns for MPC, Catalog Part Number, Flow_Group, Tester Type, Tester Device, Pattern, Restr Lvl, Rev Stage, Qcode, Device Checksum, Blank Checksum, CP ON, and CP OFF. The table lists various MPCs and their associated flow groups and test parameters.

MPC	Catalog Part Number	Flow_Group	Tester Type	Tester Device	Pattern	Restr Lvl	Rev Stage	Qcode	Device Checksum	Blank Checksum	CP ON	CP OFF
LEAD14S2XB04	PIC18F48K20-E/P	EXTENDED OTP	J750	18F48K20	OTP	STANDARD	C REL	0362	0362			
LEAD14S2XREL	PIC18F48K20-E/PREL	RELIABILITY			OTP	STANDARD	D2 NREL	0362	0362			
LEAD14S5XB04	PIC18F48K20-E/INV	EXTENDED OTP	J750	18F48K20	OTP	STANDARD	B REL	0362	0362			
LEAD14T3XB04	PIC18F48K20-E/IML	UTAC 44QFN STRIP EXTENDED OTP	J750	18F48K20	OTP	STANDARD	D REL	0362	0362			
LEAD14T4XB04	PIC18F48K20-E/PT	STRIP EXTENDED OTP	J750	18F48K20	OTP	STANDARD	D REL	0362	0362			
LEAD14T4XC03	PIC18F48K20-E/PTC03	D2X-J750 STRIP EXTENDED OTP CERLER	LTX_D2X	18F48K20	OTP	CUSTOM	F REL	0362	0362			
LEAD14T4XC03	PIC18F48K20-E/PTC03	D2X-J750 STRIP EXTENDED OTP CERLER	J750	18F48K20	OTP	CUSTOM	F REL	0362	0362			
LEAD17R7XB04	PIC18F48K20-I/INV				OTP	STANDARD	D2 NREL	0362	0362			
LEAD17S2XB04	PIC18F48K20-I/P	INDUSTRIAL OTP	J750	18F48K20	OTP	STANDARD	C REL	0362	0362			
LEAD17S5XB04	PIC18F48K20-I/INV	INDUSTRIAL OTP	J750	18F48K20	OTP	STANDARD	B REL	0362	0362			
LEAD17T3XB04	PIC18F48K20-I/ML	UTAC 44QFN STRIP INDUSTRIAL OTP	J750	18F48K20	OTP	STANDARD	D REL	0362	0362			
LEAD17T3XV01	PIC18F48K20-I/MLV01	UTAC 44QFN STRIP INDUSTRIAL OTP	J750	18F48K20	OTP	QS9000	D REL	0362	0362			
LEAD17T4X022	PIC18F48K20-I/PT022	D2X-J750 STRIP INDUSTRIAL QTP	J750	18F48K20	QTP	CUSTOM	D REL	0371	362	0371	F8F4	
LEAD17T4X022	PIC18F48K20-I/PT022	D2X-J750 STRIP INDUSTRIAL QTP	LTX_D2X	18F48K20	QTP	CUSTOM	D REL	0371	362	0371	F8F4	
LEAD17T4XB04	PIC18F48K20-I/PT	D2X-J750 STRIP INDUSTRIAL OTP	LTX_D2X	18F48K20	OTP	STANDARD	E REL	0362	0362			
LEAD17T4XB04	PIC18F48K20-I/PT	D2X-J750 STRIP INDUSTRIAL OTP	J750	18F48K20	OTP	STANDARD	E REL	0362	0362			
LEAD17T4XC05	PIC18F48K20-I/PTC05	D2X-J750 STRIP INDUSTRIAL OTP	J750	18F48K20	OTP	CUSTOM	F REL	0362	0362			
LEAD17T4XC05	PIC18F48K20-I/PTC05	D2X-J750 STRIP INDUSTRIAL OTP	LTX_D2X	18F48K20	OTP	CUSTOM	F REL	0362	0362			
LEAD17T4XC06	PIC18F48K20-I/PTC06	D2X-J750 STRIP INDUSTRIAL OTP	J750	18F48K20	OTP	CUSTOM	C REL	0362	0362			
LEAD17T4XC06	PIC18F48K20-I/PTC06	D2X-J750 STRIP INDUSTRIAL OTP	LTX_D2X	18F48K20	OTP	CUSTOM	C REL	0362	0362			
LEAD17T4XC07	PIC18F48K20-I/PTC07	D2X-J750 STRIP INDUSTRIAL OTP	LTX_D2X	18F48K20	OTP	CUSTOM	C REL	0362	0362			
LEAD17T4XC07	PIC18F48K20-I/PTC07	D2X-J750 STRIP INDUSTRIAL OTP	J750	18F48K20	OTP	CUSTOM	C REL	0362	0362			
LEAD1T5SXB04	PIC18F48K20T-I/INV	INDUSTRIAL OTP	J750	18F48K20	OTP	STANDARD	B REL	0362	0362			
LEAD1T3XB04	PIC18F48K20T-I/ML	UTAC 44QFN STRIP INDUSTRIAL OTP	J750	18F48K20	OTP	STANDARD	D REL	0362	0362			
LEAD1T3XNSB	PIC18F48K20T-I/ML	UTAC 44QFN STRIP INDUSTRIAL OTP	J750	18F48K20	OTP	CUSTOM	A REL	0362	0362			
LEAD1T3XV01	PIC18F48K20T-I/MLV01	UTAC 44QFN STRIP INDUSTRIAL OTP	J750	18F48K20	OTP	QS9000	D REL	0362	0362			
LEAD1T3XVB1	PIC18F48K20T-I/MLVA0	UTAC 44QFN STRIP INDUSTRIAL OTP	J750	18F48K20	OTP	QS9000	F REL	0362	0362			
LEAD1T4X024	PIC18F48K20-I/PT024	D2X-J750 STRIP INDUSTRIAL QTP	J750	18F48K20	QTP	CUSTOM	C REL	02A9	0362	02A9	8083	



Print out Test Traveler / Setup Sheet from Baan PDC - 1

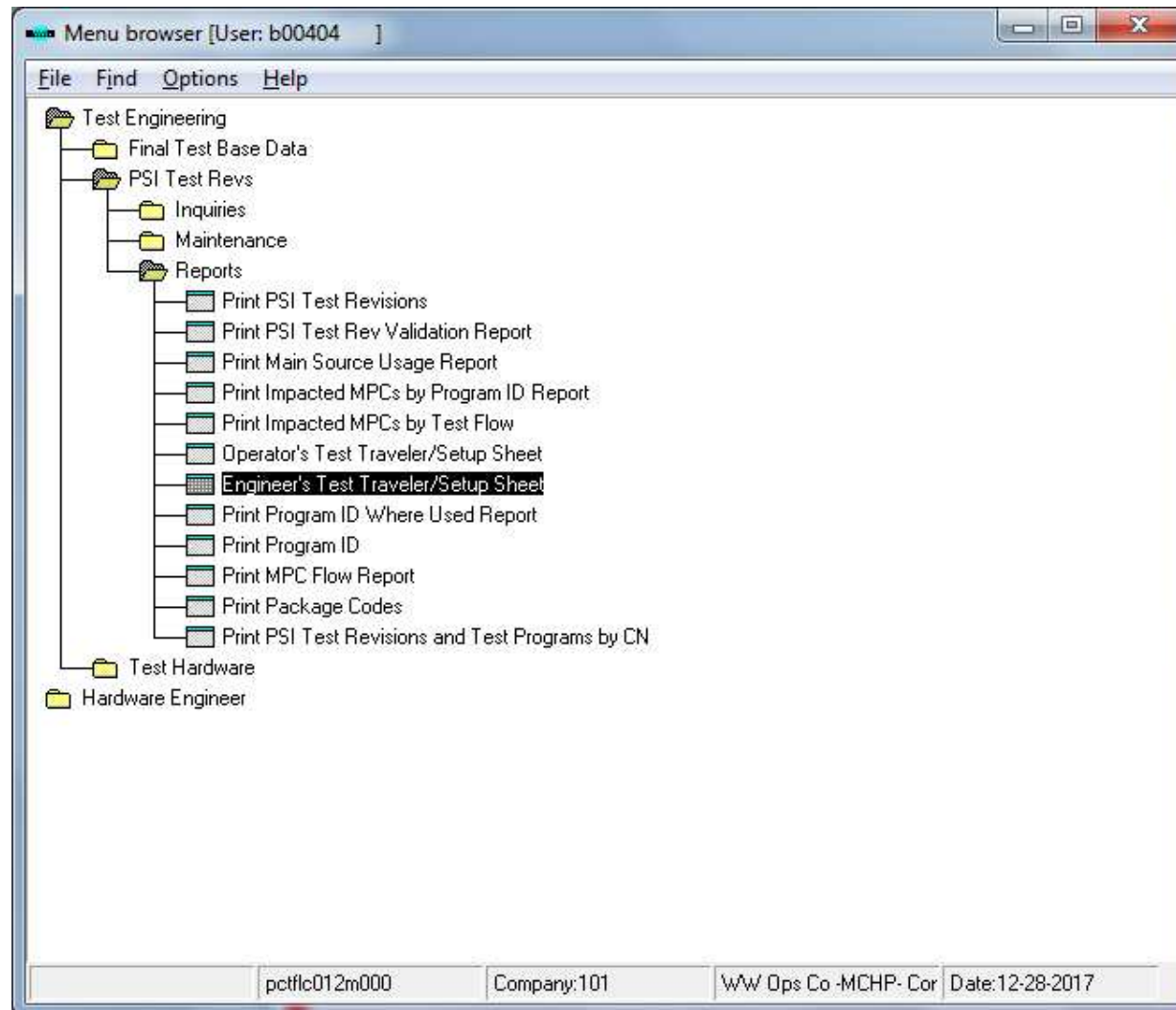
- Able to print out Test Traveler/Setup Sheet after Test Flow Group has been assigned to the MPC.
- Under Test Engineering, Double clicks PSI Test Revs, Double clicks Reports.





Print out Test Traveler / Setup Sheet from Baan PDC - 2

- Double clicks at Engineer's Test Traveler/Setup Sheet.





Print out Test Traveler / Setup Sheet from Baan PDC - 3

- Input MPC then press Tab key.
- Select Test Flow.
- Put “-” at Mask Call Rev, Assy Lot no and Wafer Lot 1.
- Click Setup Sheet button.

The screenshot shows a software window titled "pctflc012m000 : Engineer's Test Traveler/Setup Sheet [101]". The window has a menu bar with "File", "Edit", "Group", "Options", "Order", "Tools", "Special", and "Help". Below the menu bar is a toolbar with various icons, including a printer icon. The main form area contains the following fields and controls:

- MPC: LEAD1TT4X030
- PSI Rev: M
- Lead/Config: 44/44TQFP_10
- Flow Group: D2X-J750 STRIP INDUSTRIAL QTP
- Flow Number: 245849
- PSI Test Rev: LEAD1
- Rev AD Ver: 0
- Mask Call Rev: -
- Assy Lot: -
- Wafer Lot 1: -
- Wafer Lot 2: -
- Trace Code: -

At the bottom right of the form, there are two buttons: "Setup Sheet" and "Traveler". At the bottom of the window, there is a status bar with the text "alphanum".



Print out Test Traveler / Setup Sheet from Baan PDC - 4

- Select printer by using magnifier at Device.

The screenshot shows two overlapping windows from the Baan PDC software. The top window is titled 'ttstpslopen : Select Device [000]' and has a menu bar with 'File', 'Edit', 'Group', 'Options', 'Order', 'Tools', 'Special', and 'Help'. Below the menu is a toolbar with various icons. A red box highlights the 'Device' dropdown menu. Below the dropdown are fields for 'Driver', 'Type', 'Paper Type', 'Page Width' (set to 196), 'File', and 'Output File'. There are 'Continue' and 'Cancel' buttons on the right. The bottom window is titled 'ttaad3500s000 : Display Device Data [000]' and contains a table with three columns: 'Device', 'Device Type', and 'Description'. The 'Find...' button is circled in red.

Device	Device Type	Description
1FN3031-L	Direct	Finance Admin Landscape
1FN3031-P	Direct	Finance Admin Bldg Portrait
1FNOFF22	Direct	Ricoh in MTAI Finance off 22 (A4 Portrait)
1FNOFF22_L	Direct	Ricoh in MTAI Finance off 22 (A4 Landscap
1PUR3032-L	Direct	Purchase Admin Landscape
1PUR3032-P	Direct	Purchase Admin Bldg Portrait
2OFFFAC39	Direct	2-OFF-FAC-39 MMT Printer (Portrait)



Print out Test Traveler / Setup Sheet from Baan PDC - 5

- Click Find. At Device, input “FILE” then click OK.
- Select FILEONLY-SS Device and click OK.

The image shows two screenshots of the Baan PDC software interface. The top screenshot shows the 'Display Device Data [000]' window with a list of devices. The 'Find...' button is circled in red. To the right, a smaller 'Display Device Data - Find' dialog box is shown with 'FILE' entered in the 'Device:' field, also circled in red. The bottom screenshot shows the same 'Display Device Data [000]' window, but now the 'FILEONLY-SS' device is selected and circled in red.

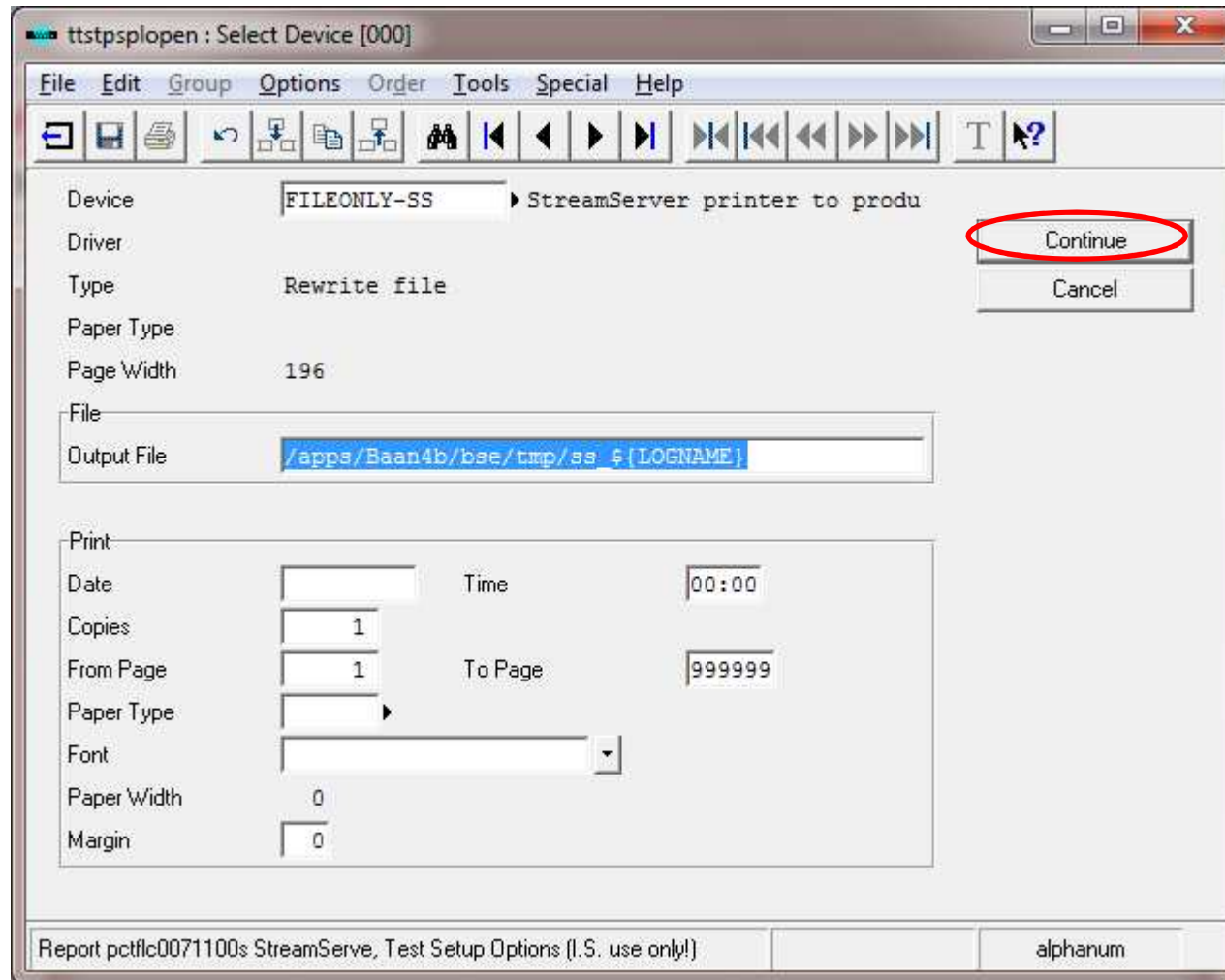
Device	Device Type	Description
1FN3031-L	Direct	Finance Admin Landscape
1FN3031-P	Direct	Finance Admin Bldg Portrait
1FNOFF22	Direct	Ricoh in MTAI Finance off 22(A4 Portrait)
1FNOFF22_L	Direct	Ricoh in MTAI Finance off 22 (A4 Landscap
1PUR3032-L	Direct	Purchase Admin Landscape
1PUR3032-P	Direct	Purchase Admin Bldg Portrait
2OFFFAC39	Direct	2-OFF-FAC-39 MMT Printer (Portrait)

Device	Device Type	Description
FILEONLY-SS	Rewrite file	StreamServer printer to produce files onl
FILEONLYD-SS	Rewrite file	StreamServer printer to produce files onl
FINGOOD	Rewrite file	Print to Finished Goods Share
FINLSR1	Direct	C3 Second Floor Finance
FINLSR1-L	Direct	C3 Second Floor Finance - Landscape
FINLSR1-SS	Rewrite file	Finance Laser #1 StreamServe
FINLSR2	Direct	Finance Laser #2



Print out Test Traveler / Setup Sheet from Baan PDC - 6

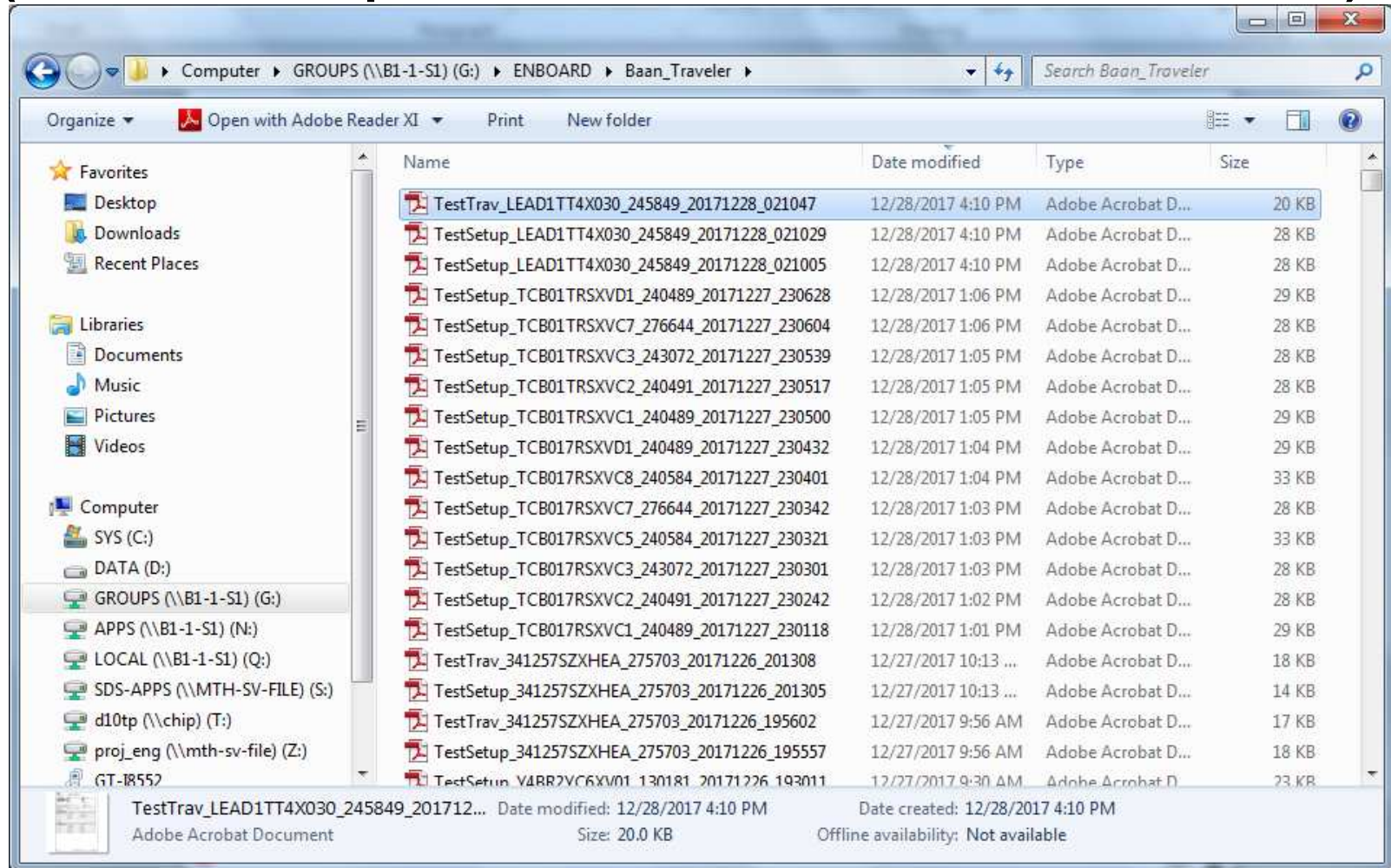
- Click Continue button.





Print out Test Traveler / Setup Sheet from Baan PDC - 7

- Get pdf file from G:\ENBOARD\Baan_Traveler share drive. (\B1-1-s1\Groups\ENBOARD\Baan_Traveler share drive)





Print out Test Traveler / Setup Sheet from Baan PDC - 8

- **Note :** For subcontractor testing, you can send him Test Setup Sheet as a reference test specification.
- If you need Test Traveler, click Traveler button.



Setup Sheet



Traveler



PSI/Test Program eCN - 1

- **PSI/Test Program CN** : <http://mchpweb/dms/testprogram/default.aspx>

The screenshot shows a web browser window with the URL <http://mchpweb/dms/testprogram/default.aspx>. The page title is "Test Program". The user is logged in as "Welcome Terdsak Somboonchan - B00404". The page features a navigation menu with "Test Program" selected, and a search bar with "Enter CN Number" and an "Advanced Search" button. The main content area includes an "Announcement" section, a "My To-Do List" section with a "Refresh" button, and a "My Test Program CNs" section. The "My Test Program CNs" section contains a table with columns: Type, Name, Peer Reviewer, Approver, CN Current Status, Masks Selected, Review Cycle, and Originator. The table shows two entries: "CN Current Status : Activated (1)" and "CN Current Status : Cancelled (1)".

Test Program

Welcome Terdsak Somboonchan - B00404 | My Site | My Links

Enter CN Number [] Advanced Search

Test Program Search Test Program CN About Contact SharePointHelp Log Table Viewers Final Test Base Data in PDC PSI Test Rev Data in PDC Test Hardware Data in PDC Site Actions

New Test Program CN

Documents

CNs

- By Status
- By Originator
- By Mask
- By Division
- Reviewed
- Approved
- Committed
- My Test Program CNs
- User Manual (UM-00012 Spec Index)

Test Data Template

Error Code Glossary

Announcement

My To-Do List

If you don't see your file, press [Refresh] button to refresh your To-Do List. Refresh

No items were found for user: MCHP-MAIN\B00404.

My Test Program CNs

New Upload Actions

Type	Name	Peer Reviewer	Approver	CN Current Status	Masks Selected	Review Cycle	Originator
				CN Current Status : Activated (1)			
				CN Current Status : Cancelled (1)			



PSI/Test Program eCN - 2

- 3 Change Types

Close Save Save & Submit Delegate CN

MICROCHIP

Test Change Notice (PSI/PDC)

Created By: Terdsak Somboonchan - B00404
Created On: 2/2/2018
CN Status: Draft
CN Number:

Home Attachments Approver History

Change Details

Change Type: PSI Test Revisions Test Program ID Both

Microchip Division:

Product(s)/Mask(s) affected:

Mask	Revision	Version	Date	Time
<input type="text"/>	<input type="text"/>	<input type="text"/>	<input type="text"/>	<input type="text"/>

Insert item

Risk Priority Metrics:

Complexity High Med Low

Impact (to customer) High Med Low

Processes Affected: Wafer Sort Final Test Other

CN Type: Revision Notice Information Notice Temporary Authorization

Are Datasheets Affected? No Yes



PSI/Test Program eCN - 3

- Attachments

Home | **Attachments** | Approver | History

PSI Test Revisions Attachments
For PSI Test Revisions, you must add appropriate attachments to Test Flows and/or Test Hardware Options section(s) below:

Test Flows
Attach appropriate Test Flow document(s)

Date	Author	File	Comments	
2/1/2018	Nares Yaiying - B01352	Test Flow - 3546S.pdf		Remove
2/1/2018	Nares Yaiying - B01352	Test Flow - 354VS.pdf		Remove

Add Attachment: [Click here to attach a file](#) [Insert to Table Above](#)

Test Hardware Options
Attach appropriate Test Hardware Options document(s)

Date	Author	File	Comments	
2/1/2018	Nares Yaiying - B01352	Test Hardware Options - 3546S.pdf		Remove
2/1/2018	Nares Yaiying - B01352	Test Hardware Options - 354VS.pdf		Remove

Add Attachment: [Click here to attach a file](#) [Insert to Table Above](#)

Test Program ID Attachments
For Test Program ID, you must select an option No Program Changes or With Program Changes. You must add appropriate attachments to both MCHP SVN and Program Revision Files sections below:

No Program Changes With Program Changes

MCHP SVN Files
Attach appropriate mchpSVN document(s)

Date	Author	File	Comments	
2/1/2018	Nares Yaiying - B01352	mchpSVN_Traveler-3546S_D2X1_TP-18020115332350.pdf		Remove

Add Attachment: [Click here to attach a file](#) [Insert to Table Above](#)

Program Revision files
Attach appropriate Program Revs document(s)

Date	Author	File	Comments	
2/1/2018	Nares Yaiying - B01352	Program Revs - 3546S_FT_DMD_A0.UNA.pdf		Remove

Add Attachment: [Click here to attach a file](#) [Insert to Table Above](#)



PSI/Test Program eCN - 4

- **Reviewer/Approver** – Contact DocControlHELP to add name.

Close Save Save & Submit Delegate CN

Test Change Notice (PSI/PDC) Created By: Terdsak Somboonchan - B00404
Created On: 2/2/2018
CN Status: Draft
CN Number:

Home Attachments Approver History

Comments:

Send out a delinquency reminder on 2/4/2018 to be repeated 6 times
(the reminder email will be CC'ed to the originator)

Reviewer Group

Peer Reviewers

Approver Group

Business Unit Test Engineer

Business Unit Product Engineer

Manufacturing Engineer

Observer Group

Search User In AD and add to Observer Group



PSI/Test Program eCN - 5

- Test Program Verification and Peer Review Checklist :

[FRM-41043-001](#)



FRM-41043-001



FRM-41043-001 -
Example

- Action :
 - NA - Not Applicable. Does not apply to this verification. No action was taken for this verification.
 - NV - Not Verified. This section applies to the change being made, but a verification of this section was not done.
 - RV - Reviewed and actions taken
- SPI-41043-003 - Test Verification, Review, and Release Procedure
- SPI-41043-004 - Test Program Release Criteria-Procedures



Appendix – Baan Notes

- Press “Tab” button to move the cursor to the next field.
- See the list of options by moving mouse to the triangle sign. Click mouse when it changes to magnifier.
- Can open several windows as needed.
- When update PSI Test Revision, always start from production active version (Version : 0).
- Document Control Team always verify if PDC Time Stamp matches the time stamp in eCN.
After submitted eCN, **DO NOT** make any change on PDC otherwise DCC will reject your eCN.



Appendix - Acronyms & Terms

- **ROM** : Read Only Memory
Program customer code during wafer fabrication
(Very old Product only)
- **OTP** : One-Time-Programmable
No customer code / Blank memory
(EPROM / EEPROM / FLASH)
No memory
Default for Strip Test Product Configuration
- **QTP** : Quick-Turnaround-Production
Program customer code during Final Test
- **SQTP** : Serialized Quick Turnaround Production
Program customer code with serialized number
during Final Test



Appendix – Links

- **MCHPWEB** : <http://microchipweb/>
- **DMS** : <http://microchipweb/pages/dms/Document%20Management%20System/toolsandresources>
- **Specification Index** : <http://mchpweb/dms/specindex/default.aspx>
- **MPC Lookup** : <http://mchpweb-pps/dms/pdcn/Pages/MPC%20Lookup.aspx>
- **Hardware Database System** : <http://hwdb/MainPage.aspx>
- **Hardware Specification (14-Axxxxx)** :
<http://mchpweb/dms/specindex/Pages/SpecDatabaseSearch.aspx?linkCat=NUMERIC>
- **PDC Approval Helper** : <http://beweb/MTAI/BaanApprovalHelper.aspx>
- **PSI/Test Program CN** : <http://mchpweb/dms/testprogram/default.aspx>
- **Tool to find lot information in MES** :
http://mth-vm-shareweb/Mtaiisdev/dataservices_lotproperties.aspx?lotid=MTAI183702430.000
- **Tool to find MES lot# from YYWWNNN trace code** :
<http://t04ftsrv/cgi-bin/mes/mtaiTraceCode.pl?TC=17494TS&B1=Fetch>
- **Acronyms and Terms** : <http://microchipweb/search/acronyms>
- **Application Request (iTURF)** : <http://mchpweb/corporate/UserRequestForm/default.aspx>